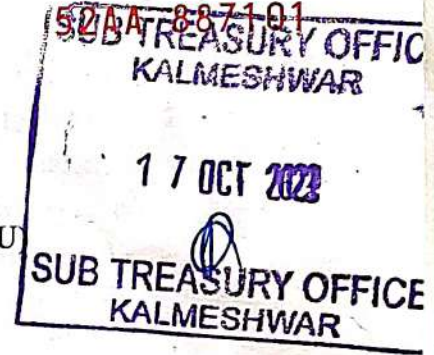




महाराष्ट्र MAHARASHTRA

© 2022 ©



MEMORANDUM OF UNDERSTANDING (MOU)

BETWEEN

GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Department of Electronics & Tele communication Engineering

&

ANISHREE TECHNOLOGIES LLP, Nagpur



प्रतिज्ञापत्र कोणाकडे सादर करावयाचे

प्रतिज्ञापत्रासाठीचे कारण

अनु. क्रमांक

दिनांक

किंमत

श्री/श्रीमती १२५

२३/१०/१५

८

मुद्रांक

मुद्रांक विकत घेणा-याची सही

अ. द. बन्सोड  
स्टॅम्पस केंद्र, कळमेरवार  
ला.न.२६/१५  
म.वि.४६०९००९

BUB TREASURY OFFICE  
KALMESHWAR



## MEMORANDUM OF UNDERSTANDING

This Memorandum of Understanding (hereinafter called as the 'MOU') is entered into on this the 26<sup>th</sup> day of October Two Thousand Twenty Two (26/10/2022).

BETWEEN

**GURUNANAK INSTITUTE OF ENGINEERING & TECHNOLOGY, Nagpur-441501,**  
The First Party represented herein by its Competent Authority: **Guru Nanak Institute of Engineering & Technology** (hereinafter referred as 'First Party', the institution which expression, unless excluded by or repugnant to the subject or context shall include its successors – in-office, administrators and assigns).

AND

**Anishree Technologies LLP, Nagpur, Plot No.9, Fci Housing Society, 120 Railway Crossing Above Medplus Medical, Manish Nagar, Nagpur-440015, the Second Party,** and represented herein by **Managing Director, Mr. Santosh Selokar** (hereinafter referred to as "Second Party", company which expression, unless excluded by or repugnant to the subject or context shall include its successors – in-office, administrators and assigns).

(First Party and Second Party are hereinafter jointly referred to as 'Parties' and individually as 'Party')

### WHEREAS:

A) First Party is a Higher Educational Institution named:

**Guru Nanak Institute of Engineering & Technology**

- B) First Party & Second Party believe that collaboration and co-operation between themselves will promote more effective use of each of their resources, and provide each of them with enhanced opportunities.
- C) The Parties intent to cooperate and focus their efforts on cooperation within area of Skill Based Training, Education and Research.
- D) Both Parties, being legal entities in themselves desire to sign this MOU for advancing their mutual interest.
- E) **Anishree Technologies LLP., Nagpur** – the Second Party is engaged in Electronics industry Training, Software development and Placement with Global Level Certifications, Skill Development, Education and R&D Services in the fields of – **Electronics, Software Development & Digital Marketing Company** and related fields.
- F) **Anishree Technologies LLP, Nagpur** – the Second Party is promoted by **Mr. Santosh Selokar** address: -Plot No.9, Fci Housing Society, 120 Railway Crossing Above Medplus Medical, Manish Nagar, Nagpur-440015. The Anishree Technologies LLP Nagpur looks after Electronics & IT training and Placement and Global Level Certification;

NOW THEREFORE, IN CONSIDERATION OF THE MUTUAL PROMISES SET FORTH IN THIS MOU, THE PARTIES HERETO AGREE AS FOLLOWS:

#### CLAUSE 1: CO-OPERATION

- 1.1 Both Parties are united by common interests and objectives, and they shall establish channels of communication and co-operation that will promote and advance their respective operations within the **Institution** and its related wings. The Parties shall keep each other informed of potential opportunities and shall share all information that may be relevant to secure additional opportunities for one another.
- 1.2 First Party and Second Party co-operation will facilitate effective utilization of the intellectual capabilities of the faculty of First Party providing significant inputs to them in developing suitable teaching / training systems, keeping in mind the needs of the industry, the Second Party.
- 1.3 The general terms of co-operation shall be governed by this MOU. The Parties shall cooperate with each other and shall, as promptly as is reasonably practical, enter into all relevant agreements, deeds and documents (the 'Definitive Documents') as may be required to give effect to the actions contemplated in terms of this MOU. The term of Definitive Documents shall be mutually decided between the Parties. Along with the Definitive Documents, this MOU shall represent the entire understanding as to the subject matter hereof and shall supersede any prior understanding between the Parties on the subject matter hereof.

#### CLAUSE 2: SCOPE OF THE MoU

- 2.1 The budding graduates from the institutions could play a key role in technological up-gradation, innovation and competitiveness of an industry. Both parties believe that close co-operation between the two would be of major benefit to the student community to enhance their skills and knowledge.
- 2.2 **Curriculum Design:** Second Party will give valuable inputs to the First Party in teaching / training methodology and suitably customize the curriculum so that the students fit into the industrial scenario meaningfully.
- 2.3 **Industrial Training & Visits:** Industry and Institution interaction will give an insight into the latest developments / requirements of the industries; the Second Party to permit the Faculty and Students of the First Party to visit its group companies and also involve in Industrial Training Programs for the First Party. The industrial training and exposure provided to students and faculty through this association will build confidence and prepare the students to have a smooth transition from academic to working career. The Second Party will provide its Labs / Workshops / Industrial Sites for the hands-on training of the learners enrolled with the First Party.



- 2.4 **Internships and Placement of Students:** Second Party will actively engage to help the delivery of the Internship and placement of students of the First Party into internships/jobs, as per AICTE internship Policy. The Second Party will also register itself on AICTE Internship Policy Portal for disseminating the Internship opportunities available with them.
- 2.5 **Live Project Training:** - Second Party will train the students of second year, third year and final year of ETC branch. Anishree Technologies LLP Nagpur provides software&tools to the ETC branch of Guru Nanak Institute of Engineering & Technology.
- 2.6 **Benefits for Students:-**Students can do practical on this software and enhance their technical skills & knowledge. Anishree Technologies providing live software for the mutual benefits of college and students.
- 2.7 **Project Development:** Both Parties have agreed to carry out the joint project development activities in the fields of – **Software Development, Electronics & Telecommunication applications & Digital Marketing Company.**
- 2.8 **Skill Development Programs:** Second Party to train the students of First Party on the emerging technologies in order to bridge the skill gap and make them industry ready.
- 2.9 **Guest Lectures:** Second Party to extend the necessary support to deliver guest lecturers to the students of the First Party on the technology trends and in house requirements.
- 2.10 **Faculty Development Programs:** Second Party to train the Faculties of First Party for imparting industrial exposure/ training as per the industrial requirement considering the National Occupational Standards in concerned sector, if available.
- 2.11 Both Parties to obtain all internal approvals, consents, permissions, and licenses of whatsoever nature required for offering the Programs on the terms specified herein
- 2.12 There is no financial commitment on the part of the **Guru Nanak Institute of Engineering & Technology, the First Party** to take up any program mentioned in the MoU. If there is any financial consideration, it will be dealt separately.

### **CLAUSE 3: INTELLECTUAL PROPERTY**

- 3.1 Nothing contained in this MOU shall, by express grant, implication, Estoppel or otherwise, create in either Party any right, title, interest, or license in or to the intellectual property (including but not limited to know-how, inventions, patents, copy rights and designs) of the other Party.

### **CLAUSE 4: VALIDITY AND TERMINATION**

#### **VALIDITY:**

This MOU shall be valid for three years from the date of sign.

#### **TERMINATION:**

- 1) This MOU shall take effect immediately on the execution of these presents. This MOU can be terminated by any party by a written letter giving 2 months' notice period.
- 2) Upon expiry of the notice period, this MOU will be terminated & both the parties will settle their accounts within one month of termination of this MOU.



3) Any notice request or other communication required or permitted to be given or made under this agreement to GNIET & Maximum Solution shall be in writing.

**CLAUSE 5: RELATIONSHIP BETWEEN THE PARTIES**

5.1 It is expressly agreed that **First Party** and **Second Party** are acting under this MOU as independent contractors, and the relationship established under this MOU shall not be construed as a partnership. Neither Party is authorized to use the other Party's name in any way, to make any representations or create any obligation or liability, expressed or implied, on behalf of the other Party, without the prior written consent of the other Party. Neither Party shall have, nor represent itself as having, any authority under the terms of this MOU to make agreements of any kind in the name of or binding upon the other Party, to pledge the other Party's credit, or to extend credit on behalf of the other Party.

Any divergence or difference derived from the interpretation or application of the MoU shall be resolved by arbitration between the parties as per the Arbitration Act, 1996. The place of the arbitration shall be at District Head Quarters of the First Party. This undertaking is to be construed in accordance with Indian Law with exclusive jurisdiction in the Courts of Nagpur.

**AGREED:**

For  
Guru Nanak Institute  
of Engineering & Technology



*[Signature]*  
Authorized Signatory

For  
Anishree Technologies LLP, Nagpur



*[Signature]*  
Authorized Signatory

<b>Guru Nanak Institute of Engineering &amp; Technology, Nagpur</b>	<b>Anishree Technologies LLP, Nagpur</b>
<b>Address :</b> Dahegaon, Kalmeshwar Road, Near Radha Swami Satsang, Nagpur 444501	<b>Address:</b> Plot No.9, Fci Housing Society, 120 Railway Crossing Above Medplus Medical, Manish Nagar, Nagpur-440015
<b>Contact Details :</b> 8788917434	<b>Contact Details:</b> 9657959184/9146906649
<b>Emails :</b> sushma.telrandhe@gmail.com	<b>Emails:</b> admin@anishreotech.com
<b>Web site :</b> www.gniet.edu.in	<b>Web:</b> www.anishreotech.com

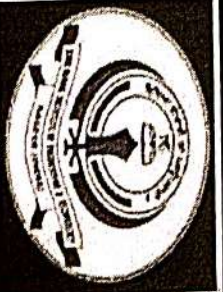
*[Signature]*  
Witness 1: Dr. Sushama Telrandhe

*[Signature]*  
Witness 2: Prof. Deepak Deshpande

Witness 1: *[Signature]*  
(Mr. Santosh Sewan)

Witness 2: *[Signature]*  
(Mr. Sayash Chakote)





# GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY



Workshop on

## "PCB Design and Fabrication & Simulation"

- Organized by Department of Electronics and Telecommunication
- Engg. in association with
  - R&D Cell, GNIET
  - IIC, GNIET

Venue: T12 Auditorium

Date: 22/5/2023  
Time: 10.00 am

<p>Prof. Sadaf Gauhar President, IIC, GNIET</p>	<p>Prof. Sushama Telrandhe HOD, ETC, GNIET</p>	<p>Prof. Rajendra Bhombhe Vice Principal GNIET</p>	<p>Dr. Hemant Hajare Principal GNIET</p>
---	--	--	--

Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441151





Guru Nanak Educational Society's  
**GURU NANAK INSTITUTE OF ENGINEERING &  
TECHNOLOGY**

AFFILIATED TO R.T.M.N.U | AICTE, DTE APPROVED | ISO 9001: 2008  
NAAC ACCREDITED

**INSTITUTION'S INNOVATION COUNCIL (IIC)**  
(Established under aegis of MHRD 's Innovation Cell, Government of India)

in association with  
**Industry Institution Interaction Cell**

Invitation Letter

To,  
**Mr. Santosh Selokar**  
Director  
**Anishree Technologies**

19/05/2023

Dear sir

GNIET, Nagpur established in 2007 by GNES society is one of the top Engineering Colleges affiliated to RTMNU, Nagpur, approved by All India Council for Technical Education, New Delhi and Director of Technical Education, Maharashtra, accredited by National Assessment and Accreditation Council and certified by ISO. GNIET offers best quality technical education to produced skilled technical manpower meeting international standards and global requirements ensuring optimal utilization of available resources. We are committed to nurture the required expertise under one roof by creating competent and motivated technocrats and managers of tomorrow, who will use their technical knowledge and managerial qualities to confidently and successfully lead the organization.

It's my pleasure to inform you that, Electronics and Telecommunication Department in collaboration with , Research and Development Cell and Institute Innovation Council, GNIET is organizing One Day workshop on PCB Design, Fabrication & Simulation. We are delighted to invite you to conduct technical session during this visit to enlighten our faculty members and students with your valuable knowledge of simulation in Matlab.

We hope you will accept our request for this invitation and help our faculties and students to gather knowledge about PCB Design & Simulation for different application in Electronics Engineering. Following are the details of your schedule.

Topic of workshop: PCB design & fabrication


Date: -22/05/2023

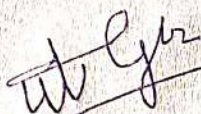
Venue: ETC workshop Lab

, GNIET

Time: -10.00 am onwards

Thank & Regards,

  
Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501

  
Dr Hemant Hajare  
Principal,





Guru Nanak Educational Society's  
**GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY**  
AFFILIATED TO R.T.M.N.U | AICTE, DTE APPROVED | ISO 9001: 2008  
NAAC ACCREDITED  
**INSTITUTION'S INNOVATION COUNCIL (IIC)**  
(Established under aegis of MHRD 's Innovation Cell, Government of India)

in association with  
**Industry Institution Interaction Cell**

**Department of Electronics and Telecommunication Engineering**

Session 2022-23

Date:-21/05/2023

**NOTICE**

All the Students of GNIET (ETC, CSE, EE) are here by informed that the Department of Electronics and Telecommunication Engineering is organizing a one day seminar on "PCB Design & Fabrication" on 22/05/2023 at 10.00 a.m. at SASKM Auditorium. GNIET.

So it is mandatory for students and staff to present over there.

Prof. Sandip Buradkar  
Co ordinator

Dr.. Sushama Telrandhe  
HOD,ETC

Venue: Sardar Avtar Singh Kohli Memorial Auditorium, T2 – Building GNIET

Time 10.00 am

Copy to (information):-

1. Hon. Chairman, GNES
2. Hon. Principal, GNIET
3. Vice. Principal, GNIET
4. All HOD'S in various Department of GNIET

Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501.





Guru Nanak Educational Society's  
**GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY**  
AFFILIATED TO R.T.M.N.U | AICTE, DTE APPROVED | ISO 9001: 2008  
NAAC ACCREDITED  
**INSTITUTION'S INNOVATION COUNCIL (IIC)**  
(Established under aegis of MHRD 's Innovation Cell, Government of India)

in association with  
**Industry Institution Interaction Cell**

A Report on workshop  
**PCB DESIGN , FABRICATION & SIMULATION**

**Date:22/05/2023**

**Duration : One Day**

**Activity under IIC,GNIET & MOU signed between GNIET and Anushree Technologies Pvt Ltd. Nagpur.**

**Resource person: Mr. Santosh Selokar,Director,Anushree Technologies,Nagpur.**

**Objective & outcomes of a PCB (Printed Circuit Board) design workshop:**

**Understanding of PCB design principles:** Participants should grasp the fundamental concepts of PCB design, including layout, routing, component placement, signal integrity, and manufacturability.

**Proficiency in PCB design tools:** Participants should gain hands-on experience with PCB design software such as Altium Designer, Eagle, KiCad, or similar tools, and be able to use them proficiently to create schematics and design PCB layouts.

**Ability to create schematics:** Participants should be able to create clear and accurate schematic diagrams that represent the electronic components and their interconnections on the PCB.

**Skills in PCB layout and routing:** Participants should develop the skills to effectively place components on the PCB layout, route traces to connect components, manage signal integrity, and optimize the layout for manufacturability and performance.

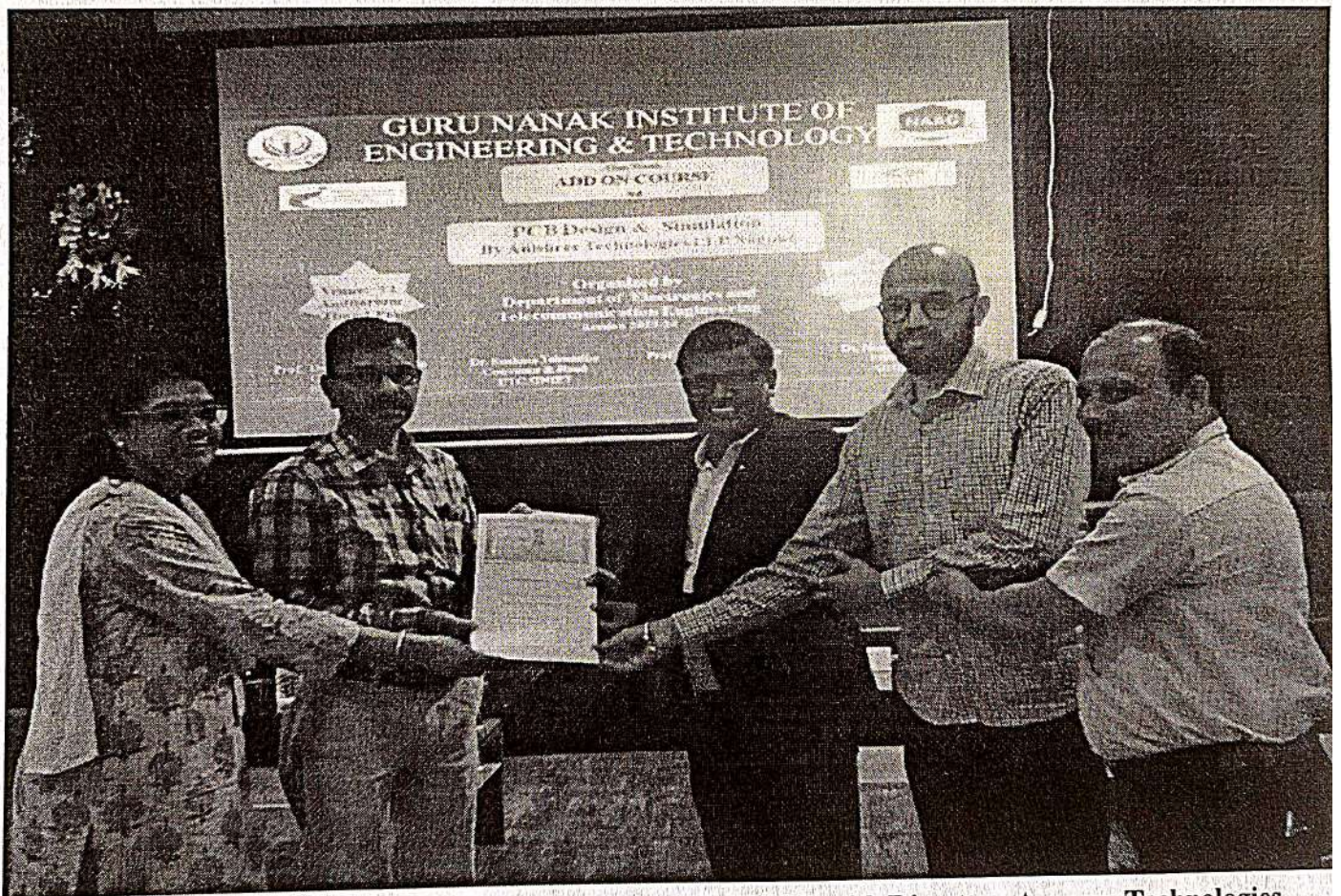
**Confidence in designing PCBs:** By the end of the workshop, participants should feel confident in their ability to design PCBs for a variety of electronic devices, from simple prototypes to complex systems, and be ready to apply their skills in real-world projects.

  
Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501

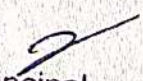


Department of Electronics & Telecommunication Engineering, in association with Research & Development cell ,GNIET have successfully organized a One day workshop on “PCB DESIGN FABRICATION & SIMULATION ” on 22-05-2023. This workshop is organized under IIC(Institution Innovation Council) GNIET..ETC department organizes such type of events on continuous basis to enhance student’s practical & technical skill and to make them compatible with industry.

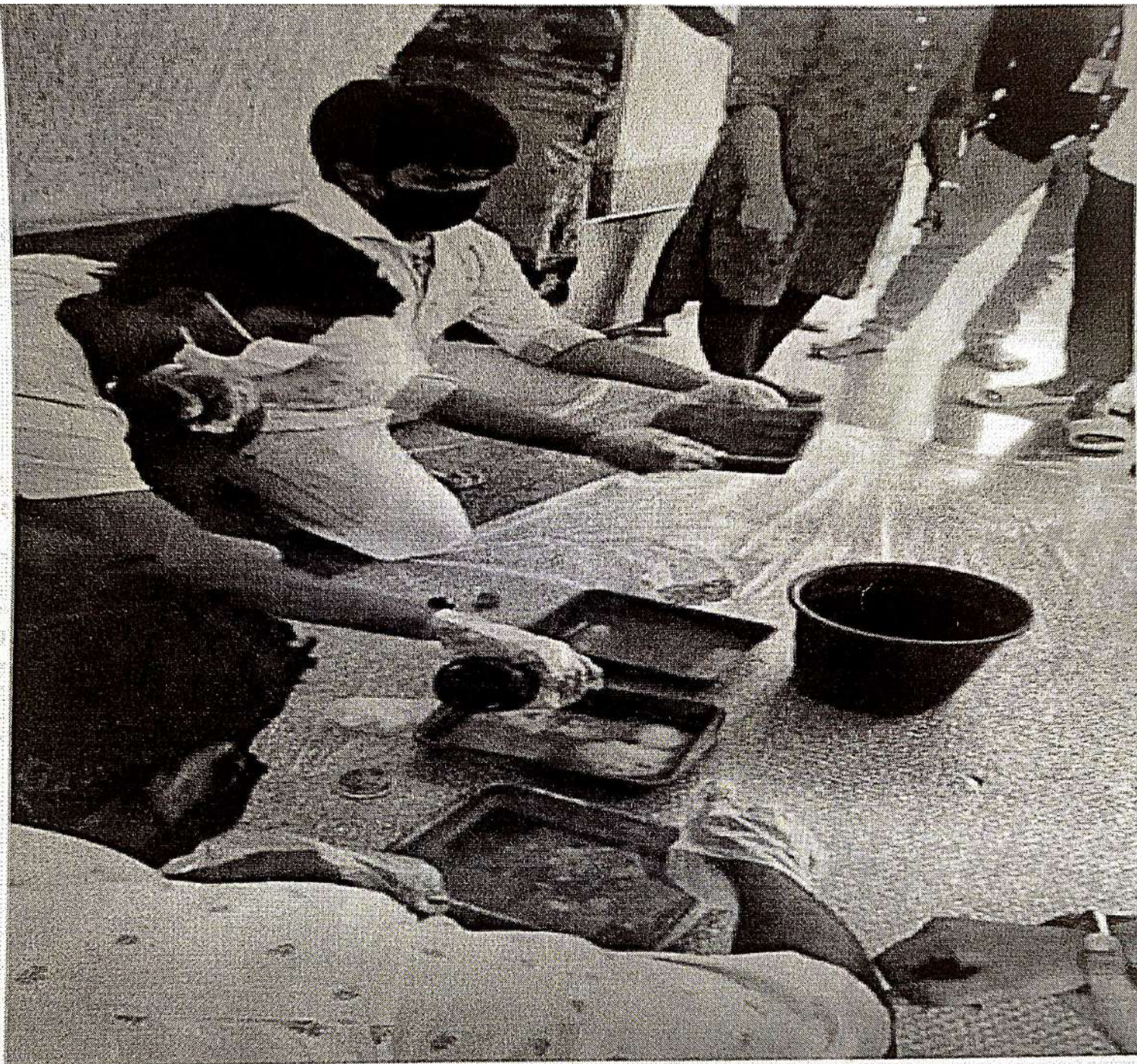
Mr.Santosh Selokar felicitated by Dr. Sushama Telrandhe ,HOD,ETC



**Prof. Rajendra Bhombe ,Vice Principal, GNIT signed MOU with Director Anusree Technologies Event -PCB Design ,Fabrication & Simulation on 22/05/2023 at GNIET,Nagpur**


  
Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501





**Etching process for PCB Design,Event: Workshop on PCB Design on 22/05/2023 at GNIET,Nagpur**

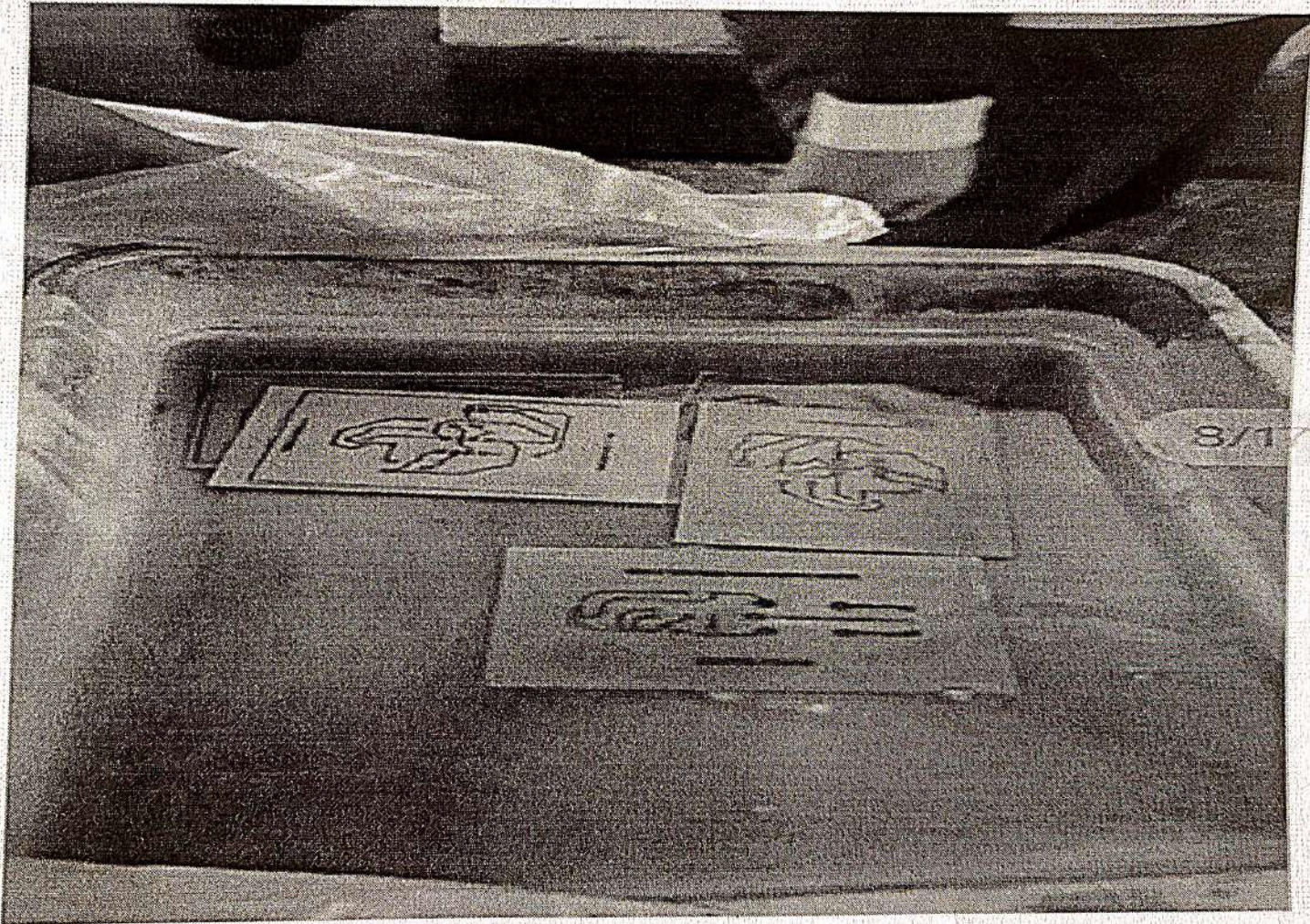
Workshop was aimed to provide knowledge about Complete PCB Designing using simulation tool to test electronics & electrical circuit in software environment and also its Fabrication,PCB design software named Cad soft EAGLE & to make physical PCB at home so that any student can make project on his own. Almost 50 students from ETC & other departments have participated in this course.

  
Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501



Following content are covered in course.

- ✓ Schematic Design
- ✓ Component Footprint Design
- ✓ Integrated Library Design
- ✓ PCB Design & Routing
- ✓ Etching of PCB
- ✓ Soldering of Components on to PCB.
- ✓ Simulation to check the performance of PCB designe circuit.



**Manufacturing of PCBs in workshop PCB Design ,Fabrication & Simulation on 22/05/2023 at GNIET,Nagpur**

  
Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501




Mr. Santosh Selokar , M.D., Anishree Technologies LLP along with his team have coordinated & executed this PCB Design workshop & simulation course appreciably to make it successful to enhance student's skill level. After attending this course students have expressed about their capability of designing special purpose PCB Of any electronics project. This workshop will be helpful for students for circuit development of their final year project. Total 50 students have Participated in this workshop , where everyone has got a chance to work individually on a separate PCB.

On 22nd May, the first session of expert talk started at 10:00 A.M by Mr. Santosh Selokar. He started with introduction of PCB Designing, Benefits of the course and its applications. He explained Schematic Design of Circuit on software Easy EDA, benefits of using Easy EDA and pointers to make precise schematic design. The afternoon session of Hands-on started at 1:30. P.M., where the participants are provided with laboratory sessions on Schematic design & PCB layout design on Easy EDA. The participants also tried various demo circuits provided.

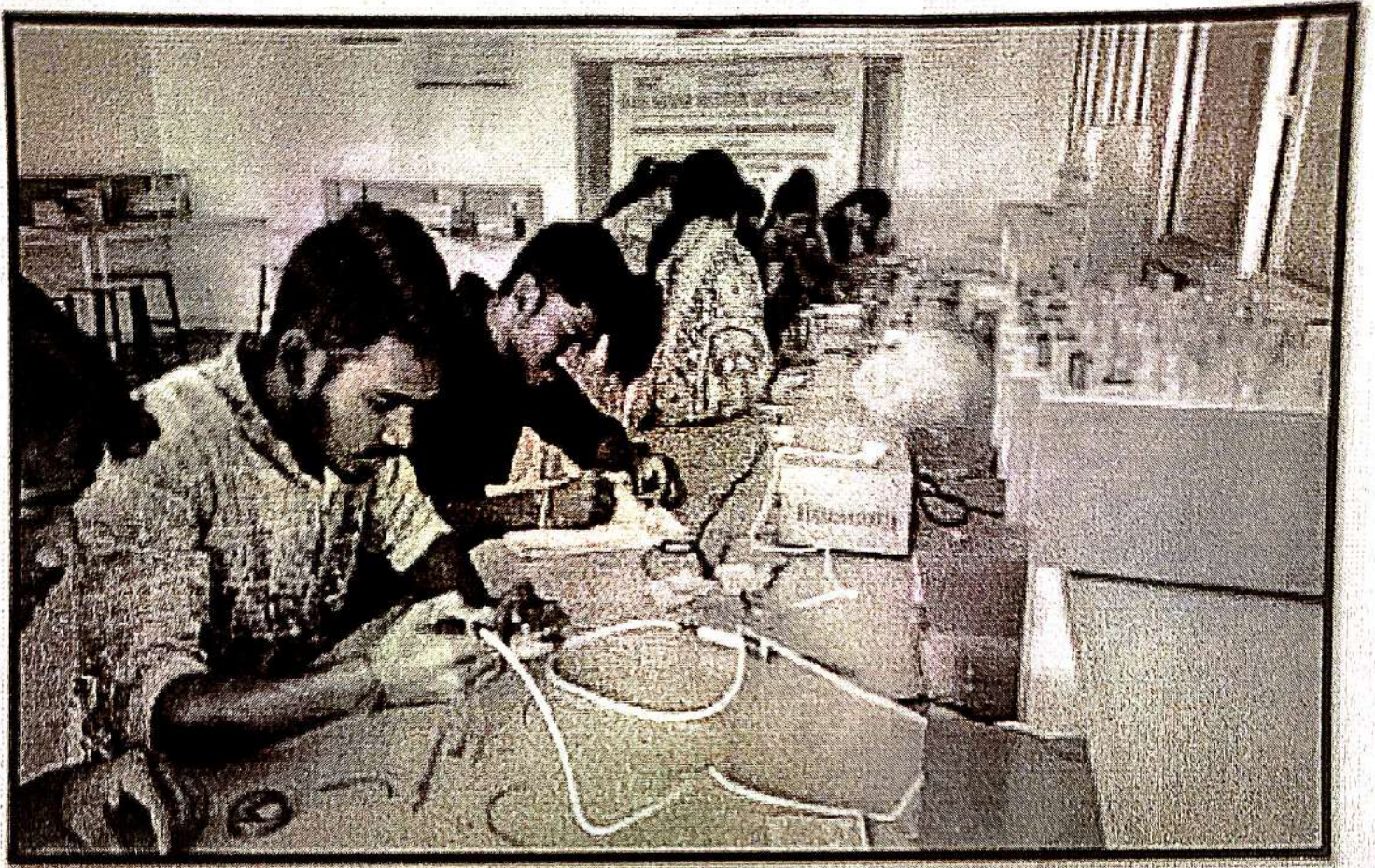
Mr. Santosh Selokar gave introduction of Fabrication Environment. He also explained about the process converting from File to Film, Printing the Inner layers, Removing the Unwanted Copper, Layer Alignment and Optical Inspection, Lay-up and Bond.

Mr. Saurabh Chakole of Aneeshree Technologies . explained about PCB Soldering procedure & do's and don'ts of making PCB. He also explained various circuits which can be used often by students and motivated students to keep designing such circuits.

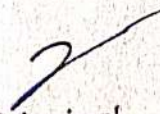
All the students responded that they have learned and had hands on experience in designing a PCB. They were very excited about participation in this workshop and requested for more workshops in similar way so that they can simultaneously gain the practical knowledge.

  
Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501

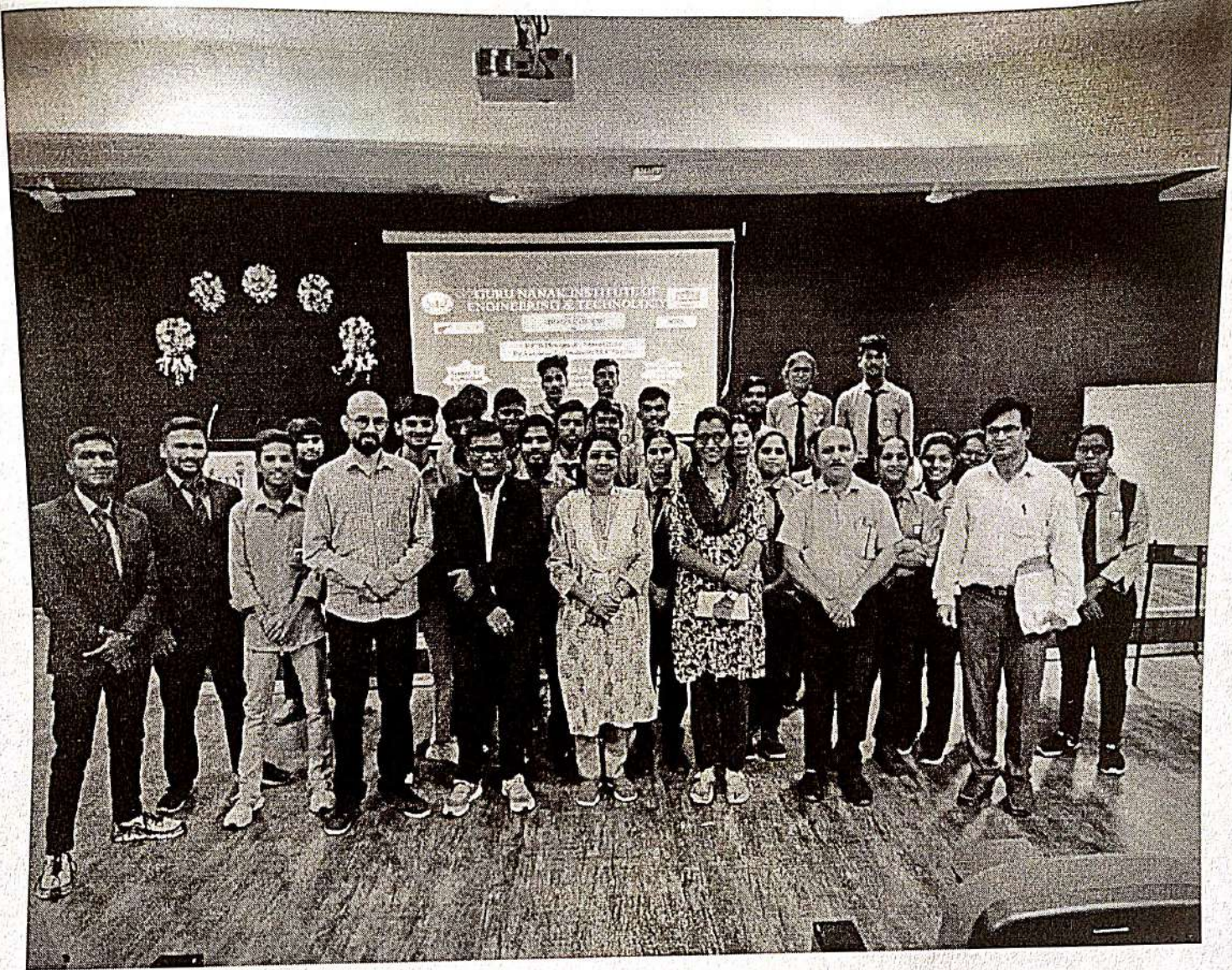




Esting process in workshop PCB Design ,Fabrication & Simulation on 22/05/2023 at GNIET,Nagpur

  
Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501





Mr Selokar with participants of workshop PCB Design ,Fabrication & Simulation on 22/05/2023 at GNIET,Nagpur

The vote of thanks & concluding speech to motivate the student to participate in this programme was given by Prof. Soniya Milmlile. She thanked the management of GNES, Sardar Navneet Singh Tuli, Chairman, GNES, Sardarni Tanpreet Singh Kaur Tuli, Secretary, GNES, Dr, Hazare, Principal, GNIET, Prof. Bhombe, Vice Principal, Prof. Sadaf Gaur, President ,IIC, GNIET, Dr. Telrandhe, Dean R&D, Prof. Sandip Buradkar , Event coordinator and entire teaching and non-teaching staff of GNIET for extending their continuous and valuable support in organizing and conducting these kind of workshops conducted in the college by the department. She thanked Anishree Technologies LLP Pvt. Ltd. for their technical support.

Total no. of participants of the event is 52

Sandip Buradkar  
Co-ordinator

Prof. Sadaf Gauhar  
President IIC, GNIET

Dr. Hemant Hajare  
Principal, GNIET

Copy to :  
Vice Principal, GNIET

Principal  
Guru Nanak Institute of  
Engineering & Technology  
Nagpur-441501



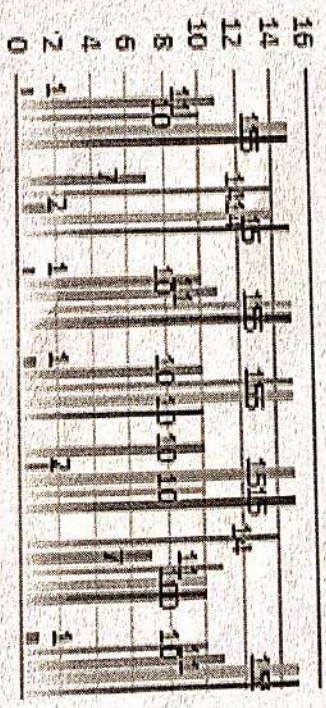
Title: PCB Design & Simulation  
Date: 22-05-2023

Year: 2022-23

52

Enter the number between 1-5 where 5 Means Strongly Agree and 1 Means Strongly Dissagree

	1	2	3	4	5
The venue was comfortable	1	2	3	4	5
The content was relevant	7	14	2	14	15
The event content was easy to understood	1	10	11	15	15
The content was comprehensive	1	10	15	15	10
The event handouts supported presentation	10	2	15	10	15
Event provided usefull additional information	14	7	11	10	10
The activites were usefull learning experiences	1	10	11	15	15



The venue was comfortable  
 The content was relevant  
 The event content was easy to...  
 The content was compreh...  
 The event handouts supported...  
 Event provided usefull...  
 The activites were usefull

1 2 3 4 5

Principal  
 Guru Nanak Institute of  
 Engineering & Technology  
 Nagpur-441501



B C D E F G H I J K L M

Title: PCB DESIGN & SIMULATION

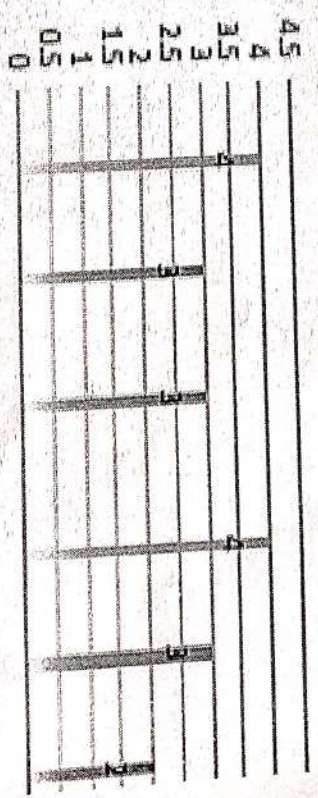
Date 22/05/2023

Participant: POOJA DILIP SHENDE

52

Rate the number between 1-5 where 5 Means Strongly Agree and 1 Means Strongly Dissagree

is comfortable	1	2	3	4	5
is relevant			3		
content was easy			3		
was comprehensive				4	
identifications			3		
presentation					
was useful		2			
information					
were useful					4
indices					



The venue was comfortable  
 The content was relevant  
 The event content was easy to understand  
 The content was comprehensive  
 The event handouts were provided useful  
 The overall event was useful

1 2 3 4 5

*[Signature]*  
 Principal  
 Guru Nanak Institute of Engineering & Technology  
 Nagpur-441501





In association with  
**Industry Institution Interaction Cell**

ATTENDANCE SHEET 2022-23 Date: 22/05/2023

Name of seminar/workshop:

PCB Design & simulation.

Sr no.	Name of student	Signature
1	ACHAL RUPCHAND WADBUDHE	Achal
2	ARTI DINESH KHAMBALKAR	Arti
3	BHAVIKA NILKANTH SHENDE	Bhavika
4	OJASVI SANJAY BURANDE	O Burande
5	PALLAVI DEORAO GHONGE	P Ghonge
6	POOJA DILIPRAO SHENDE	Pooja
7	PUNAM CHANDRABHAN MAHURE	Punam
8	RAKHIBAI KESHORAO PATLE	
9	SADHANA SOVINDAS BISEN	Sadhana
10	SHREYA SANGAM KAPSE	
11	SHWETA BAPURAO RANDKHE	Shweta
12	SHWETA KIRANRAO GHATOLE	Shweta
13	SUSHMA RAJENDRA MENDHE	Sushma
14	VAISHNAVI RAJU MADANKAR	V. Madankar
15	VANDANA SHOBHARAM MOHANKAR	V Mohankar
16	ACHAL ANANDRAO VIGHNE	Achal
17	ADITI SANTOSH SONULE	Aditi
18	ASHWINI HARIDAS TARWARE	Ashwini

*Handwritten signature/initials*





INSTITUTION'S  
INNOVATION  
COUNCIL

(Ministry of Education Government of India)



Guru Nanak Educational Society's  
GURU NANAK INSTITUTE OF ENGINEERING &  
TECHNOLOGY

AFFILIATED TO R.T.M.N.U | AICTE, DTE APPROVED | ISO 9001: 2008  
NAAC ACCREDITED

INSTITUTION'S INNOVATION COUNCIL (IIC)

(Established under aegis of MHRD 's Innovation Cell, Government of India)

in association with  
**Industry Institution Interaction Cell**

ATTENDANCE SHEET

Date: 22/05/2023

Name of seminar/workshop:

PCB Design & Simulation

19	AYUSHI VINOD MESHAM	Ayushi
20	BHAWANA VINOD IKHAR	
21	BHAWANA VINOD IKHAR	Bhuvani
22	DARSHANA SUBHASHRAO NASARE	D Nasare
23	DEEPALI TEJLAL MANE	Mane
24	DEVYANI CHANDRASHKHAR DHOLE	Dhole
25	JAYASHRI HARICHANDRA KHOPE	Jkn
26	JAYSHRI CHINDHUJI CHIKHALE	Jchikale
27	KARISHMA NIRMAL MANEKAR	Karishma Manekar
28	KARISHMA WASUDEO GHONGE	K Ghonge
29	KOMAL CHANDRASHEKAR HERODE	Komal
30	MAYURI TIKARAM SONKUSARE	MD
31	ADIBA SADAF MOHD AZHAR	Azar
32	BHAVIKA RAJENDRA TANDEKAR	B Rajendrar
33	DARSHANA PURUSHOTTAM VAIDHYA	Vaidya
34	NILIMA GAJANAN JARAD	Nilima
35	POOJA NILKANTHRAO SAYAM	Pooja
36	POONAM OMKAR BARGAT	Poonam

May 22





INSTITUTION'S  
INNOVATION  
COUNCIL  
(Ministry of Education Initiative)



Guru Nanak Educational Society's  
GURU NANAK INSTITUTE OF ENGINEERING &  
TECHNOLOGY

AFFILIATED TO R.T.M.N.U | AICTE, DTE APPROVED | ISO 9001: 2008  
NAAC ACCREDITED

INSTITUTION'S INNOVATION COUNCIL (IIC)

(Established under aegis of MHRD 's Innovation Cell, Government of India)

in association with  
**Industry Institution Interaction Cell**

ATTENDANCE SHEET

Date: 22/05/2023

Name of seminar/workshop:

PCB Design & simulation

37	PRACHI DAMODHAR TIBOLE	
38	PRADNYA RAJU MADANKAR	

Sr no.	Name of student	Signature
39	RAFAT TASNEEM MOHAMMAD RIYAZ	
40	SHIFA TAHA MOHAMMAD ZAFAR	
41	NILESH ANAND RAUT	
42	PRATIK PUNDLIK MOYAJE	
43	RAJKAMAL DAMODHAR BAGDE	
44	VAIBHAV GAJENDRA GIRADKAR	
45	HARSHAL RAJU PATIL	
46	KARTIKEYA SINGH THAKUR	
47	KRUNAL VINAYAK DEVTALE	

1/1/2023





Guru Nanak Educational Society's  
**GURU NANAK INSTITUTE OF ENGINEERING &  
TECHNOLOGY**

AFFILIATED TO R.T.M.N.U | AICTE, DTE APPROVED | ISO 9001: 2008  
NAAC ACCREDITED

**INSTITUTION'S INNOVATION COUNCIL (IIC)**  
(Established under aegis of MHRD 's Innovation Cell, Government of India)

In association with  
**Industry Institution Interaction Cell**

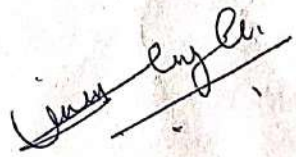
ATTENDANCE SHEET

Date: 26/10/2023

Name of seminar/workshop: PCB Design & Simulation

48	KRUNAL VINAYAK DEVTALE	
49	KRUSHNA ARUNRAO GANJARE	
50	LILADHAR TULARAM ITOLE	Kashai
51	NIRBHAY DADADRAM BHOYAR	Liladhar
52	PRATEEK SADANAND BHELAVE	Bhojar
53	PRAVIN JUNGILAL UIKE	Belave
Sr.no	Name of student	Signature
54	LILADHAR TULARAM ITOLE	Litole
55	NIRBHAY DADADRAM BHOYAR	NBhojar
Sr no	Name of student	Signature
56	PRATEEK SADANAND BHELAVE	PB
57	ROHIT RAMSING THAKRE	Thakre
58	SAGAR PRABHAKAR NIMKAR	S Nimkar
59	SUVHENDU DIPENDU SAHA	
60	SUVHENDU DIPENDU SAHA	Saha

  
HOD, ETC Head of Department  
Electronics & Telecommunication Eng.  
Griet Dahgaon Nagpur

  
Principal  
GNIET, Nagpur