Guru Nanak Educational Society's
GURU NANAK INSTITUTE
OF ENGINEERING & TECHNOLOGY
APPROVED BY AICTE, DTE & AFFILIATED TO RTM NAGPUR UNIVERSITY, NAGPUR
Dahegaon, Opp IOC Petrol pump, Kalmeshwar Road, Nagpur- 441501 Ph. 07118-661400
Website: www.gniet.ac.in E-mail: gnietnagpur@gmail.com

NAAC ACCREDITED

Report

on

Add-on Course

Android development Programming

Organized By: Department of Computer Science and Engineering

(2019-2020)

Dates from: 11-03-2020 to 16-03-2020

(06 Days, 05 Hrs per day, total 30 Hrs.)

(Timing: 10:00 am to 1:00 pm & 2:30 pm to 4:30 pm)

Submitted to

IQAC, GNIET, NAGPUR

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Guru Nanak Institute of Engineering & Technology Nagpur- 441501

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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY
Dahegaon, Kalmeshwar Road, Nagpur-441 501
(NAAC Accredited)

Department of Computer Science & Engineering

GNIET."CSE."21-22/.' Date-

-:Notice:-

As per the guidelines of higher authorities and IQAC cell, Department of Computer Science and Engineering is organizing 30 hrs. (One week) add on course "Android development Programming" from date 11-03-2019 to 16-03-2019. Timing for the classes and Hands on will be 10:00 inn to 1:00 pm & 2:30 pm to 4:30 pm. (05 hours per day; total hours 30 Hrs). All the students of 6rd and 8th semesters having a good attendance record in current as well iis previous semester are eligible to participate. All the interestedstudents are requested to register their names to Department Head before date of commencement of course. The Add-on
course is fully free of cost

HOD, CSE
Computer Science & Engineering
GNIET Dahegaon, Nagpur

Copy to:

. Hon. Chairperson (For Information)
2. Principal GNIET
3. Vice-Principal GNIET
4. Notice board & Office copy.

Course content:

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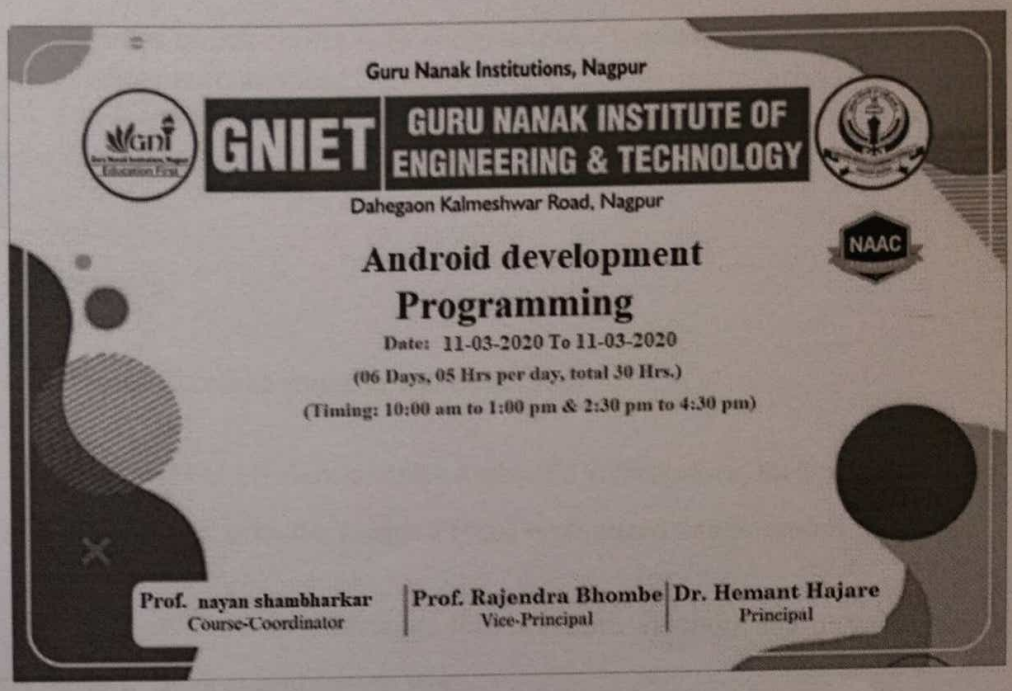

Brief Report On

Add-on Course: Android development Programming

A Add-on course on **Android development Programming,** was organized by **Department of Computer Science and Engineering** for Students of B. Tech. 6th (CBCS) and B.E.8th (CBS) CSE. The Add-on course was organized for the period of 30 hours starting from date: **11-03-2020 to 26-03-2020.** Timing for the classes and Hands on was 10:00 am to 1:00 pm & 2:30 pm to 4:30 pm. 05 hours per day (Total Course hours = 30 Hrs). The Add-on course was fully free of cost. Total 78 students have participated and completed Add-on course successf

Course Objective and Outcomes:

Course Objectives The main objective of the Add-on courses was

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The primary objective of this course is to equip learners with advanced skills and knowledge in Android app development, enabling them to build, test, and deploy professional-quality mobile applications. This includes understanding the full app development lifecycle, from design and development through testing and deployment, with a focus on practical, hands-on learning.

Course Outcomes:

After completion of the course students will be able to;

CO-1 Create apps that efficiently utilize Android's architecture, including activities, services, receivers, and content providers, with a focus on modern development practices.

CO-2 Craft compelling user interfaces that are both aesthetically pleasing and functional across a range of devices and screen sizes.

CO-3 Utilize various storage options and databases to store, retrieve, and manage data within Android applications securely.

CO-4. Develop apps that can consume web services, interact with APIs, and handle network tasks with efficiency and reliability.

CO-5 Understand the complete process of preparing an app for release, including signing the app, optimizing its performance, and publishing it to the Google Play Store.

This course is designed to transform intermediate learners into proficient Android developers who are ready to tackle real-world app development challenges and pursue professional opportunities in this dynamic field.

Course Mapping with POs and PSOs:

PO & PSO->	PO-1	PO-2	PO-3	PO-4	PO-5	PO-6	PO-7	PO-8	PO-9	PO-10	PO-11	PO-12	PSO-1	PSO-2

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CO-1	2	2	1	1	0	1	1	0	2	3	0	2	2	2
CO-2	2	3	2	3	0	1	0	0	2	2	0	2	3	2
CO-3	2	3	2	3	3	1	0	0	3	2	0	2	3	2
CO-4	2	1	3	3	0	3	3	3	2	3	3	2	2	3
CO-5	2	2	3	2	3	3	3	3	3	3	3	2	2	3
Avg POs	2	2.2	2.2	2.4	1.2	1.8	1.4	1.2	2.4	2.6	1.2	2	2.4	2.4
% PO/PSO attainment	66.7 %	73.3 %	73.3 %	80.0 %	40.0 %	60.0 %	46.7 %	40.0%	80.0%	86.7%	40.0%	66.7%	80.0%	80.0%

PO-1	**Engineering knowledge**	Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
PO-2	**Problem analysis**	Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
PO-3	**Design/ development of solutions**	Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
PO-4	**Conduct investigations of complex problems**	Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
PO-5	**Modern tool usage**	Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
PO-6	**The engineer and society**	Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.

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PO-7	**Environment and sustainability**	Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
PO-8	**Ethics**	Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
PO-9	**Individual and teamwork**	Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
PO-10	**Communication**	Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
PO-11	**Project management and finance**	Demonstrateknowledgeandunderstandingoftheengineeringandmanagement principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
PO-12	**Life-long learning**	Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

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Module 1: Advanced Android Fundamentals Android Architecture Review: Deep dive into Android components and lifecycle Advanced Intents and Broadcast Receivers: Effective communication and task scheduling

Module 2: Sophisticated UI/UX Design Material Design Components: Implementing advanced UI elements and navigation patternsCustom Views and Animations: Creating engaging visuals and transitionsAdaptive Layouts: Designing for different devices and screen sizes using ConstraintLayout and responsive design principles

Module 3: Data Storage and Management SQLite and Room Database: Advanced data persistence techniques, including complex queries and migrations SharedPreferences for Lightweight Data Storage: Best practices and use cases Content Providers and Contracts: Sharing data between apps securely

Module 4: Advanced Networking Retrofit and OkHttp: Handling RESTful API communication effectively GSON and Moshi for JSON Parsing: Converting between JSON and Java/Kotlin objects Network Security and Best Practices: Implementing secure networking practices

Module 5: Working with Android APIs and Services Google Maps and Location Services: Integrating maps and retrieving location information Push Notifications with Firebase Cloud Messaging (FCM): Setting up and sending notifications Using Sensors and Camera: Accessing and managing device sensors and camera for richer app functionalities

Module 6: Advanced Features Implementation Background Processing: Using WorkManager for deferred and asynchronous tasks Jetpack Compose: Understanding and applying the modern toolkit for native UI development Dependency Injection with Dagger/Hilt: Managing dependencies for scalable app development

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Module 7: Testing and Debugging Unit Testing: Writing and running unit tests using JUnit and Mockito UI Testing: Automating UI tests with Espresso Debugging Techniques: Advanced debugging techniques and tools in Android Studio

Module 8: App Optimization and Deployment Performance Tuning: Profiling app performance and memory usage APK Optimization: Reducing app size and improving launch times Publishing to the Google Play Store: Preparing for release, managing app signing, and post-launch monitoring

Daily Schedule:

From Date: 16-08-2022 to 21-08-2022

Day 1: March 11, 2019 - Introduction and Android Fundamentals

Morning Session:

Course Introduction: Overview, expectations, tools setup (Android Studio, SDKs)

Android Architecture Overview: Understanding of Android stack, components, and lifecycle

Afternoon Session:

Hands-on: Setting up the first Android project

Deep Dive: Activities and Intents - Creating user interfaces and navigating between screens

Day 2: March 12, 2019 - Sophisticated UI/UX Design

Morning Session:

Theory: Material Design principles, Responsive layouts using ConstraintLayout

Hands-on: Implementing RecyclerView for list displays

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Afternoon Session:

Workshop: Designing adaptive UIs for different screen sizes and orientations

Group Activity: Critique and redesign session for sample apps

Day 3: March 13, 2019 - Data Storage and Management

Morning Session:

Lecture: Data persistence options in Android - SharedPreferences, SQLite, and Room Database

Hands-on: Creating and querying a Room Database

Afternoon Session:

Group Project: Developing an app feature that utilizes local data storage

Q&A Session: Best practices for data management in Android

Day 4: March 14, 2019 - Advanced Networking

Morning Session:

Workshop: Consuming RESTful APIs using Retrofit, handling JSON with GSON

Hands-on: Building a network layer for an Android app

Afternoon Session:

Group Activity: Adding network call retries and error handling

Discussion: Security considerations in networking

Day 5: March 15, 2019 - Implementing Advanced Features

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Morning Session:

Lecture: Integrating Google Maps and accessing location

Workshop: Implementing push notifications with Firebase Cloud Messaging

Afternoon Session:

Hands-on: Adding sensor-based features to an app

Discussion: Best practices for using external services and APIs

Day 6: March 16, 2019 - Testing, Debugging, and Deployment

Morning Session:

Lecture: Introduction to unit testing in Android with JUnit and UI testing with Espresso

Hands-on: Writing and running tests for an existing feature

Afternoon Session:

Workshop: App optimization and profiling

Final Review: Preparing an app for deployment, Google Play Store submission process

Course Wrap-up: Feedback session and discussion on continuing development education

Glimpses:

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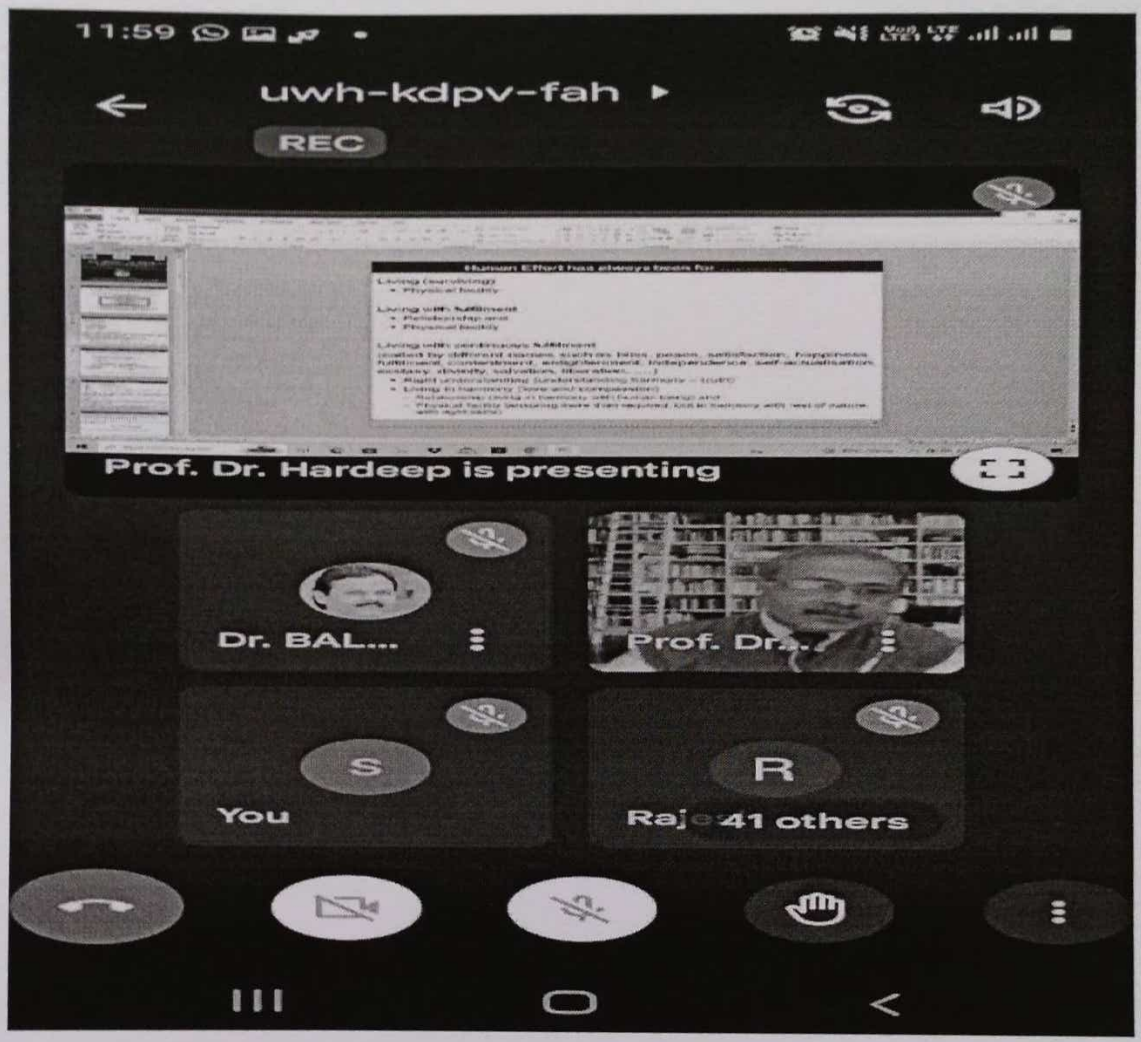
Android development Programming, Date-11-03-2020



Number of Participants/ Beneficiaries year wise

S.No	Semester / Branch	Number of Participants
1	6th year	43
2	8th year	35
	Total	**78**

Students Feed Back:

From the overall responses received from the students at the end of the course, it has been observed that a majority of students are satisfied and have recommended for similar type of Add-on Courses to be arranged in future as it was very useful to them. Course material of Add-on course has been distributed to all participants.

MCQ TEST ON
Android development Programming
Question Paper

Note: Attempt all 30 Questions. Each Question carry 01 Mark (MAX 30 Marks). Max Time – 01 Hr. Tick the correct answer. No negative marking.

1)What is the primary language(s) used for Android app development?

A) Python

B) Java and Kotlin

C) C++

D) Swift

Answer: B) Java and Kotlin

2)Which component is NOT part of Android's architecture?

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A) Activities

B) Intents

C) ViewControllers

D) Services

Answer: C) ViewControllers

3)What is an Intent in Android?

A) Serialization library

B) Messaging object

C) Database

D) Design pattern

Answer: B) Messaging object

4)Which of the following is used for persistent data storage in Android?

A) SharedPreferences

B) Intent

C) View

D) Toast

Answer: A) SharedPreferences

5)What does APK stand for?

A) Android Phone Kit

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Website: www.gniet.ac.in E-mail: gnietnagpur@gmail.com

MCQ Test Result:

S.N	Name of Participant	scor
1	ADITYA VILAS MADEKAR	18
2	AISHWARYA SURESH KAMBLE	19
3	AJAY UMAJI WANJARI	18
4	AKASH WASUDEO NAGALWADE	18
5	AKSHY REDDY MAHESH REDDY	17
6	ALFIYA MAHVISH AADIB KHAN	16
7	AMIT YOGRAJ BISEN	18
8	AMOL ARUN DOIFODE	19
9	ANKITA PARMAAND KALE	19
10	ANMOL SANJAY GHAI	17
11	ANTARA ANIL PATIL	18
12	ANTHONY PAUL JOSEP	14
13	ARTI VIJAY KHOKALE	17
14	ASHISH RAJESH KOCHE	18
15	ASHISH RAJKUMAR VAISHYA	19
16	ASHUTOSH ASHOK JAISWAL	17
17	ASHVINI KAMLAKAR WANJARI	18
18	AWDHOOT PRAKASH BHAWAR	19
19	BHAGWAT DIGAMBAR VIDHOLE	19
20	CHETNA KRUSHNKUMAR MADAVI	18
21	DHIRAJ DNYANESHWAR SAWARKAR	19
22	DIKSHA KHEMRAJ GAJBHIYE	18
23	DIPIKA ASHOK SHAHARE	18
24	DIVYANSHUKUMAR KEDAR PANDEY	17
25	KIRAN NADLAL SHAHARE	16

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26	KUNAL JIVANRAM TAGDE	18
27	MADHURA LANKESHWAR NARNAWARE	19
28	MANGESH MANOJ SONAWANE	19
29	MAYURI SHISHUPAL MESHRAM	17
30	MAYURI TUKARAM SHRUGARE	18
31	MOSAM ISARAM DAMHE	14
32	NEENU SHAJI NADAR	17
33	NEHA ISHWAR RAIKWAD	18
34	NIDHI PUROSHOTTAM JOSHI	19
35	PAYAL MADHUKAR SONEKAR	17
36	PAYAL SHALIKRAM AWATHARE	18
37	PRAJKTA VIJAY KAKDE	19
38	PRANALI GOPAL HELWATE	19
39	PRATIKSHA MANGAL PATIL	18
40	PRAVIN GANGADHAR WADHANKAR	19
41	PRIYANKA SUBHASH JUNGHARE	18
42	PRAJWAL DILIP JAGTAP	18
43	REVTI CHANDRASHEKHAR BURDE	17
	8th(sem)	16
44	ROHIT RAJESHRAO THAKRE	18
45	RUPLAL GENDLAL DAMHE	19
46	SAHIL SANJAY GAJBHIYE	19
47	SAHIL VIJAY DHORE	17
48	SAKSHI NARENDRA ATKAR	18
49	SAMIR HARIDASJI MASKE	14
50	SANJAY ISHWARPRASAD SONDHIYA	17

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ETC

GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY
Dahegaon, Kalmeshwar Road, Nagpur-441 501
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

Session 2019-2020

Date: 11/10/2019

NOTICE

All the Students of B.E. of Electronics & Telecommunication Engineering are hereby informed that department is organizing a short term course on **"ADD ON COURSE ON BASIC OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS"** from 15/10/2019 to 21/10/2019. The schedule along with all other details of this course is given in the brochure. All the interested students must register for the same from 11th to 14th Oct 2019. For registration of the course contact the co-ordinator in Electronics & Telecommunication Department.

Raut.

Prof. Sucheta Raut
HOD ETC

Head of Department
Electronics & Telecommunication Eng
Gniet Dahegaon Nagpur

Copy to:

1) Display on Notice Board
2) Circulation among the Students on Whatsaap group
3) Head T&P
4) Principal for Information

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Six Day Workshop on

"BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS"

REGISTRATION FORM

Name:

Branch:

Roll No. :

Contact No. :

Email Id:

Amount (Rs): _____

Signature of Applicant:

Date & Place:

Signature of Co-Orinator

Signature & Seal of HoD ETC

PATRONS

Sardar Navneet Singh Tuli,CMD, GNI, Nagpur

Mrs.Tanpreet Kaur Tuli, MD, GNI, Nagpur

ADVISORY COMMITTEE

Dr. Sanjay Shrivastava , Principal, GNIET, Nagpur

Prof. R .M. Bhombe, Vice Principal GNIET, Nagpur

CO-ORDINATOR

Prof. NehaChourasia, Asst. Prof. ETC

Email Id:-nehaetc@gmail.com

ORGANIZING COMMITTEE

Prof. SuchetaRautHOD, ETC

Prof. NehaChourasia, Asst. Prof. ETC

ADDRESS FOR CORRESPONDENCE:

Department of Electronics and Telecommunication Engineering Guru Nanak Institute of Engg. & Tech. KalmeshwarRoad,NearRadha Swami Satsang,Dahegaon, Nagpur, Maharashtra 441501

GURU NANAK INSTITUE OF ENGINEERING & TECHNOLOGY, NAGPUR

Add on Course on

"BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS"

15/10/2019 TO 21/10/2019

Organized by

DEPARTMENT OF ELECTRONICS and TELECOMMUNICATION ENGINEERING, GNIET, NAGPUR

REGISTRATION:

Registration can be made in advance by remitting the registration fee as indicated below along with the registration form. For registration contact to Mr. Deepak Deshpande,Asst.Prof. ETC

REGISTRATION FEE:

Registration fees for students of GNIET is 50/-.

IMPORTANT DATES:

Registration starts : 11/10/2019

Last Date of Registration : 14/10/2019

SCHEDULE:

Duration of course is 30 hrs, which will be covered in one week from **15/10/2019to 21/10/2019**. The schedule during the course is divided into Three sessions per day as follow:

Session 1 : 9:00 am To 1:30 am

Lunch Break :1:30 pm To 2:00 pm

Session 2 :2:00 pm To 4:00 pm

Mode :

Seminar HALL& ETC Lab

ELIGIBILITY

Students of ETC eligible to attend the training.

ABOUT THE COURSE

It is an Add on course which helps the students to understand the concepts through hands-on lab sessions on INTRODUCTION TO BASICS OF ELECTRONICS.

OBJECTIVE

The objectives of course are:

1. To make students familiar with Basics Of Electronics
2. To teach Students Analog and Digital circuits
3. The course will also teach the students about the designing of digital circuit

OUR TRAINER

Mr. RAVISH JAIN

Email Id:ravishjain8@gmail.com

IMPORTANT NOTE

✓All interested students should register before the last date of registration.

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Department of Electronics & Telecommunication Engineering

Session 2019-2020

ADD COURSE ON BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS

COURSE OBJECTIVE:

The objectives of workshop are:

1. To study the basic characteristic, construction, open loop & close loop operations of Op-Amp.
2. To study linear and non linear applications of Op-Amp.
3. To study the design of Electronic Circuits for Oscillator, Multivibrator and Active Filters
4. To enable students to design regulated power supply using regulated ICs

COURSE OUTCOME:

After completing this Introduction to Basics of Electronics and Analog Design of circuit's students will able to

1. Describe the basic differential Amplifier using transistor and its operation & characteristic.
2. Design linear Op-Amp circuits such as Voltage follower, Summing amplifier, scaling and averaging amplifier, Instrumentation amplifier circuits for various practical applications.
3. Design non-linear Op-Amp such as Comparators, Comparator IC such as LM 339, Schmitt trigger, multivibrator circuits for various practical applications using IC555.
5. Analyze and design amplifier circuits, oscillators, Filter, regulated power supply

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SYLLABUS:

DURATION (30HRS)

1. **Circuit Designing Analog and Digital (Duration-15 Hrs)**

- Decide the regulator to be used and its input voltage.
- Basic types of regulators, Decide the regulator to be used and its input voltage.
- Basic types of capacitor, Decide the value of the filter capacitor.
- Decide the PIV (peak inverse voltage) of the diodes to be used.
- Circuit Drawing and Simulation.
- Analog Circuits - Schematic Circuit Diagrams.
- Digital Circuits - Schematic Circuit Diagrams.

2. **Operational Amplifier Fundamentals (Duration-15 Hrs)**

- Blocks of OP-Amp (Basic Building Blocks)
- Basic differential Amplifier using transistor and its operation.
- OP-Amp parameters, characteristic and Definition, Ideal OP-Amp, Equivalent circuit, and Voltage Transfer curve.
- Inverting and Non-inverting configurations and design, concepts of virtual short and ground.

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DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION

Session 2019-2020

Attendance For Add On Course on Basis of Electronics and Analog Design Of Circuits

S.No.	Name of Students	Attendance Sign
1	Papiha Ravindrarao Ajmire	
2	Shubham Suresh Bhiwapure	
3	Shobhit Yashwant Bixen	
4	Tahalramani Divya Gopichand	
5	Khopbragade Shradha Rajesh	
6	Band Prajakta Umakant	
7	Dahat Pragati Narendra	
8	Saurabh Ajay Mishra	
9	Chaudhari Ajay Prabhulal	
10	Sharma Anshu Mitlesh	
11	Walde Sneha Shivnarayan	
12	Tiwari Ankita Jaiprakash	
13	Rohit Pramod Babde	
14	Shambharkar Saurabh Rajkumar	
15	Meshram Vrushali Rajendra	
16	Mahalle Gaytri Bhaskarrao	
17	Bute Nitin Devidasrao	
18	Sapkane Sonali Bharat	

Head of Department
Electronics & Telecommunication Engg
Guru Nanak Dahegaon Nagpur

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DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION

Session 2019-2020

Attendance For Add On Course on Basis of Electronics and Analog Design of Circuits

Sr. No.	Name of Students	Day 1	Day 2	Day 3	Day 4	Day 5	Day 6
1	Papiha Ravindrarao Ajmire	P	P	P	P	P	P
2	Shubham Suresh Bhiwapure	A	P	P	P	A	P
3	Shobhit Yashwant Bisen	P	P	P	P	P	P
4	Tahalramani Divya Gopichand	P	P	P	P	A	P
5	Khopbragade Shradha Rajesh	A	P	P	P	P	A
6	Band Prajakta Umakant	P	P	P	P	P	P
7	Dahat Pragati Narendra	P	P	P	A	P	P
8	Saurabh Ajay Mishra	P	P	P	A	P	P
9	Chaudhari Ajay Prabhulal	P	P	P	P	P	P
10	Sharma Anshu Mitlesh	P	P	P	P	P	P
11	Walde Sneha Shivnarayan	P	P	A	P	P	P
12	Tiwari AnkitaJaiprakash	P	P	P	P	A	P
13	Rohit Pramod Babde	A	P	P	P	P	P
14	Shambharkar Saurabh Rajkumar	A	P	P	P	P	P
15	Meshram Vrushali Rajendra	A	P	P	P	P	P
16	Mahalle Gaytri Bhaskarrao	A	P	P	P	P	P
17	Bute Nitin Devidasrao	P	P	P	P	P	P
18	Sapkane Sonali Bharat	P	A	P	P	P	P

P Raut

Head of Department
Electronics & Telecommunication Engg.
Gniet Dahegaon Nagpur

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MCQ OF ADD COURSE ON BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS

Name of Student:_____

Q1. Which of the following options are correct for a 4×1 multiplexer?
a) It has four 3 – input AND gates
b) It has four 2 – input AND gates
c) It has one 3 – input AND gate
d) It has one 3 – input AND gate

Q2. Which of the following is correct for Digital Circuits?
a) Less susceptible to noise or degradation in quality
b) Use transistors to create logic gates to perform Boolean logic
c) Easier to perform error detection and correction with digital signal
d) All of the mentioned

Q3. What is a Circuit?
a) Open-loop through which electrons can pass
b) Closed-loop through which electrons can pass
c) Closed-loop through which Neutrons can pass
d) None of the mentioned

Q4. Which of the following is an example of a digital Electronic?
a) Computers
b) Information appliances
c) Digital cameras
d) All of the mentioned

Q5. Which of the following is a type of digital logic circuit?
a) Combinational logic circuits
b) Sequential logic circuits
c) Both Combinational & Sequential logic circuits
d) None of the mentioned

Q6. What is a switching function that has more than one output called in Digital Electronics?
a) Multi-gate function
b) Multi-output function
c) Multiple-gate function
d) Multiple-output function

Q.7 If the B.J.T. is used as a follower, which capacitor experiences Miller multiplication?
a) C_π
b) C_μ
c) C_{cs}
d) C_b

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Q8. The maximum efficiency of Half wave rectifier is
a) 33.6%
b) 40.6%
c) 66.6%
d) 70.9%

Q9. Which of the following is a trivalent doping Element?
a) Arsenic
b) Antimony
c) Boron
d) Phosphorous

Q10. Which characteristic of IC in Digital Circuits represents a function of the switching time of a particular transistor?
a) Fan – out
b) Fan – in
c) Power dissipation
d) Propagation delay

ANSWERS: 1:a, 2:d, 3:b, 4:d, 5:c, 6:b, 7: 8:b, 9:c, 10:d

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Dahegaon, Kalmeshwar Road, Nagpur-441 501.
Department of Electronics & Telecommunication Engineering
Session 2019-2020

MCQ OF ADD COURSE ON BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS

Name of Student: Saurabh Shambharkar.

Q1. Which of the following options are correct for a 4×1 multiplexer?
a) It has four 3 – input AND gates
b) It has four 2 – input AND gates
c) It has one 3 – input AND gate
d) It has one 3 – input AND gate

Q2. Which of the following is correct for Digital Circuits?
a) Less susceptible to noise or degradation in quality
b) Use transistors to create logic gates to perform Boolean logic
c) Easier to perform error detection and correction with digital signal
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a) C_e
b) C_μ
c) C_{cs}
d) C_b

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FEEDBACK FORM ON BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS

Add on Course evaluation Form

Please submit feedback regarding the Add on course you have just completed, including feedback on course structure, content, and instructor.

Sign in to Google to save your progress. Learn more

* Indicates required question

Student Name *

Your answer

Contact Number *

Email Id

Your answer

Level of effort you put into the course *

- Poor
- Fair
- Satisfactory
- Very Good

Contribution of the course to your skill and knowledge *

- Poor
- Fair
- Satisfactory
- Very Good

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Skill and responsiveness of the instructor *

- Poor
- Fair
- Satisfactory
- Very Good

Course content was organized and well planned *

- Poor
- Fair
- Satisfactory
- Very Good

What aspects of this course were most useful or valuable? *

Your answer

Any other comments or suggestions? Please share them below

Your answer

Submit

Clear form

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Department of Electronics & Telecommunication Engineering

Session 2019-2020

FEEDBACK ANALYSIS OF ADD ON COURSES ON BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS

Total No. of Students: 18

1. **Level of effort you put into the course**

2. **Contribution of the course to your skill and knowledge**

3. **Skill and responsiveness of the instructor**

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Department of Electronics & Telecommunication Engineering

Session 2019-2020

REPORT ON BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS

1	**Course Title**	**BASIS OF ELECTRONICS AND ANALOG DESIGN OF CIRCUITS**
2	**Course Schedule**	**15/10/2019 to 21/10/2019**
3	**Course Venue**	**Seminar room**
4	**Name of Coordinator**	**Prof. Neha Choursia**
5	**No. Of students Participated**	18
6	**Course Objective**	To study the basic characteristic, construction, open loop & close loop operations of Op-Amp.To study linear and non linear applications of Op-Amp.To study the design of Electronic Circuits for Oscillator, Multivibrator and Active Filters. To enable students to design regulated power supply using regulated ICs
7	**Course Outcome**	Describe the basic differential Amplifier using transistor and its operation &characteristic. Design linear Op-Amp circuits such as Voltage follower, Summing amplifier, scaling andaveraging amplifier, Instrumentation amplifier circuits for various practical applications. Design non-linear Op-Amp such as Comparators, Comparator IC such as LM 339, Schmitt trigger, multivibrator circuits for various practical applications using IC555.Analyze and design amplifier circuits, oscillators, Filter, regulated power supply

Students attended Add on Course on Basics of Electronics and Analog Design of Circuits From 15th Oct to 19th Oct 2019.

Prof. Neha Choursia
Program Coordinaor

Prof. SuchetaRaut
HOD, ETC

Head of Department
Electronics & Telecommunication Engg.
Gniet Dahegaon Nagpur

Dr. Sanjay Shriastav
Principal GNIET

Principal
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APPROVED BY AICTE, DTE & AFFILIATED TO RTM NAGPUR UNIVERSITY, NAGPUR
Dahegaon, Opp IOC Petrol pump, Kalmeshwar Road, Nagpur- 441501 Ph. 07118-661400
Website: www.gniet.ac.in E-mail: gnietnagpur@gmail.com

DEPARTMENT ELECTRICAL ENGINEERING

Session 2019-2020

Date:15/02/2020

NOTICE

All the Students of VII semester B.E. of Electrical Engineering are hereby informed that department is organizing a short term course on **"EMBEDDED C FOR ELECTRICAL ENGINEERING"** from 20/02/2020 to 25/02/2020. The schedule along with all other details of this course is given in the brochure. All the interested students must register for the same from 15th to 20th feb , 2020. For registration of the course contact the co-ordinator in Electrical Department.

Prof. Rajendra Bhombe
HOD EE

Copy to:

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2) Circulation among the Students on Whatsaap group
3) Head T&P
4) Principal for Information

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Six Day Workshop on

"ADD ON COURSE ON EMBEDDED C FOR ELECTRICAL ENGINEERING"

REGISTRATION FORM

Name:

Branch:

Roll No. :

Contact No. : _____

Email Id:

Signature of Applicant:

Date & Place:

Signature of Co-Orinator

Signature& Seal of HoD EE

PATRONS

Sardar Navneet SinghTuli,CMD, GNI, Nagpur

Mrs.Tanpreet Kaur Tuli, MD, GNI, Nagpur

ADVISORY COMMITTEE

Dr.Shrivastava , Principal, GNIET, Nagpur

Mr. R .M. Bhombe, Vice Principal GNIET, Nagpur

CO-ORDINATOR

Mr. Akshay Pillewan, Asst. Prof. EE

Email Id:-akshu1712@gmail.com

ORGANIZING COMMITTEE

Mr. Akshay Pillewan, Asst. Prof. EE

Email Id: akshu1712@gmail.com

Ms.Diksha Khare Asst. Prof. EE Email Id:gnietee@gmail.com

Prof. Rajendra Bhombe HOD, EE

ADDRESS FOR CORRESPONDENCE:

Department of Electrical Engineering Guru Nanak Institute of Engg. & Tech. Kalmeshwar Road,Near Radha Swami Satsang,Dahegaon, Nagpur, Maharashtra 441501

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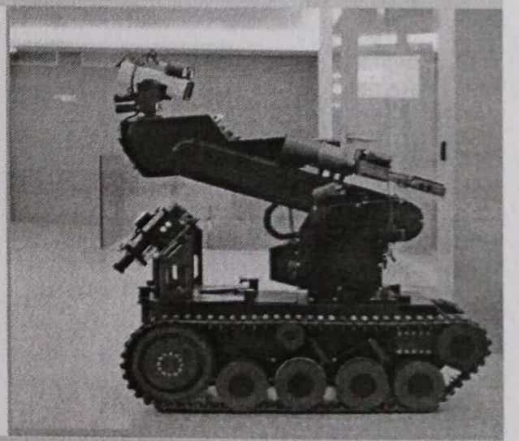
Course on

"ADD ON COURSE ON EMBEDDED C FOR ELECTRICAL ENGINEERING"

20/02/2020 TO 25/02/2020



Organized by

DEPARTMENT OF ELECTRICAL ENGINEERING, GNIET, NAGPUR

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REGISTRATION:
Registration can be made in advance by remitting the registration fee as indicated below along with the registration form. For registration contact to Mr. Akshay Pillewan, Asst. Prof. EE .

REGISTRATION FEE:
Registration fees for students of GNIET are FREE.

IMPORTANT DATES:
Registration starts : 15/02/2020
Last Date of Registration : 20/02/2020

SCHEDULE:
Duration of course is 30 hrs, which will be covered in one week from 29/12/2018 to 02/01/2018. The schedule during the course is divided into Three sessions per day as follow:
Session 1 : 9:00 am To 1:30 am
Lunch Break :1:30 pm To 2:00 pm
Session 2 :2:00 pm To 4:00 pm

Mode :
Seminar HALL

ELIGIBILITY
Students of **EE** are eligible to attend the training.

ABOUT THE COURSE

It is an add on course which helps the students to understand the concepts through hands-on lab sessions, examples on **EMBEDDED C FOR ELECTRICAL ENGINEERING.**

OBJECTIVE

The objectives of course are:

1. Understand the basics of an embedded system.
2. Understand the typical components of an embedded system.
3. To understand different communication interfaces
4. To learn the design process of embedded system applications.
5. To understands the RTOS and inter-process communication.

OUR TRAINER

Prof. N.Suresh

Email Id: nsuresh25@gmail.com

IMPORTANT NOTE

✔All interested students should register before the last date of registration .

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Department of Electrical Engineering

SESSION 19-20

ADD ON COURSE ON EMBEDDED C FOR ELECTRICAL ENGINEERING

COURSE OBJECTIVES

The objectives of workshop are:

1. Understand the basics of an embedded system.
2. Understand the typical components of an embedded system.
3. To understand different communication interfaces
4. To learn the design process of embedded system applications.
5. To understands the RTOS and inter-process communication.

COURSE OUTCOME

Upon completion of this course, the students will be able to:

1. Understand the design process of an embedded system
2. Understand typical embedded System & its components
3. Understand embedded firmware design approaches
4. Learn the basics of OS and RTOS

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Department of Electrical Engineering

SESSION 19-20

SYLLABUS

DURATION: 30 HOURS

Sr. No	Syllabus	No. of Hours
1	INTRODUCTION TO EMBEDDED SYSTEMS	6 hours
2	TYPICAL EMBEDDED SYSTEM.	6 hours
3	COMMUNICATION INTERFACE Onboard communication interfaces, External communication interfaces	6 hours
4	EMBEDDED FIRMWARE DESIGN AND DEVELOPMENT	6 hours
5	RTOS BASED EMBEDDED SYSTEM DESIGN	6 hours
	Total	**30 Hours**

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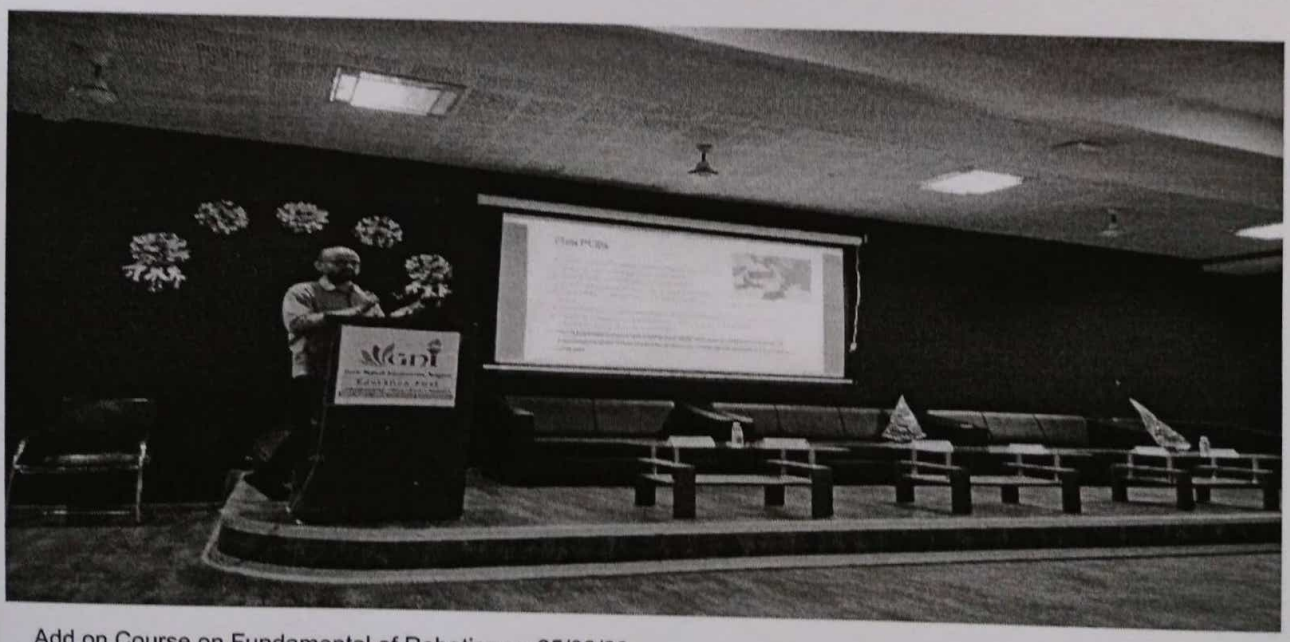
Department of Electrical Engineering

SESSION 19-20

REPORT ON "ADD ON COURSE ON FUNDAMETALS OF ROBOTICS & AUTOMATION"

1	**Course Title**	**"EMBEDDED C FOR ELECTRICAL ENGINEERING"**
2	**Course Schedule**	20/02/2020 to 25/02/2020
3	**Course Venue**	**Seminar room and Department of EE**
4	**Name of Coordinator**	**Prof. Akshay Pillewan**
5	**No. Of students Participated**	32
6	**Course Objective**	Understand the basics of an embedded system. Understand the typical components of an embedded system. To understand different communication interfaces To learn the design process of embedded system applications. To understands the RTOS and inter-process communication
7	**Course Outcome**	Upon completion of this course, the students will be able to: Understand the design process of an embedded system Understand typical embedded System & its components Understand embedded firmware design approaches Learn the basics of OS and RTOS



Add on Course on Fundamental of Robotics on 25/02/20

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DEPARTMENT ELECTRICAL ENGINEERING

Session 2019-2020

Date:26/02/2020

EMBEDDED C FOR ELECTRICAL ENGINEERING

MCQ

Name of Student:-.....

1.Which memory storage is widely used in PCs and Embedded Systems?

a) EEPROM

b) Flash memory

c) SRAM

d) DRAM

2. How is the protection and security for an embedded system made?

a) Security chips

b) Memory disk security

c) IPR

d) OTP

3. Which of the following task swapping method is a better choice in the embedded systems design?

a) time slice

b) RMS

c) cooperative multitasking

d) pre-emptive

4. Which type of memory is suitable for low volume production of embedded systems?

a) Non-volatile

b) RAM

c) Volatile

d) ROM

5. Which activity is concerned with identifying the task at the final embedded systems?

a) scheduling

b) task-level concurrency management

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c) high-level transformation
d) compilation

6. Which level simulates the algorithms that are used within the embedded systems?
a) algorithmic level
b) switch level
c) gate level
d) circuit level

7. How an embedded system communicate with the outside world?
a) Memory
b) Output
c) Peripherals
d) Input

8. Which of the following helps in reducing the energy consumption of the embedded system?
a) emulator
b) debugger
c) simulator
d) compilers

9. What is the purpose of memory refresh register of Z80?
a) To control on-chip SRAM
b) To control on-chip DRAM
c) To clear cache
d) To control ROM

10. What does MESI stand for?
a) modified exclusive system input
b) modifies embedded shared invalid
c) modified exclusive shared invalid
d) modified exclusive stale invalid

Add on Course evaluation Form

Please submit feedback regarding the Add on course you have just completed, including feedback on course structure, content, and instructor.

Sign in to Google to save your progress. Learn more

* Indicates required question

Student Name *

Your answer

Contact Number *

Contact Number *

Your answer

Email Id

Your answer

Level of effort you put into the course *

- ○ Poor
- ○ Fair
- ○ Satisfactory
- ○ Very Good

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Contribution of the course to your skill and knowledge *

- ○ Poor
- ○ Fair
- ○ Satisfactory
- ○ Very Good

Skill and responsiveness of the instructor *

- ○ Poor
- ○ Fair
- ○ Satisfactory
- ○ Very Good

Course content was organized and well planned *

- ○ Poor
- ○ Fair
- ○ Satisfactory
- ○ Very Good

What aspects of this course were most useful or valuable? *

Your answer

Any other comments or suggestions? Please share them below

Your answer

Feedback Analysis of Add on Courses on Exploring the EMBEDDED C FOR ELECTRICAL ENGINEERING

Total No. of Students 32

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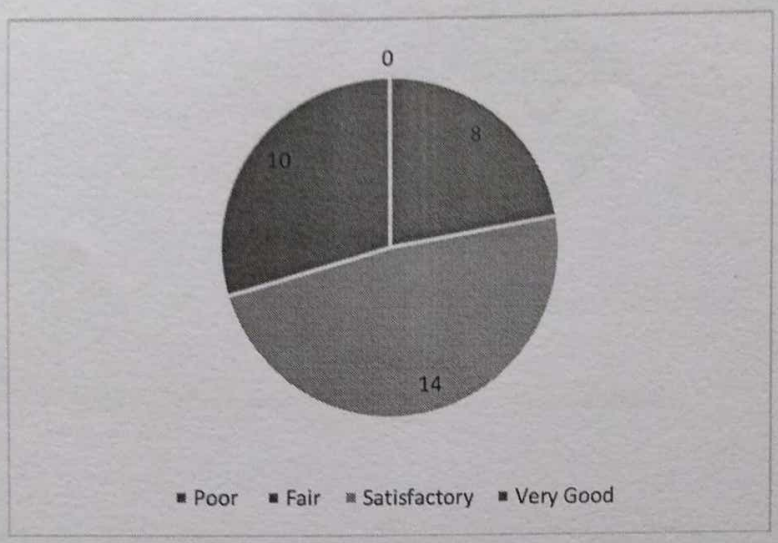
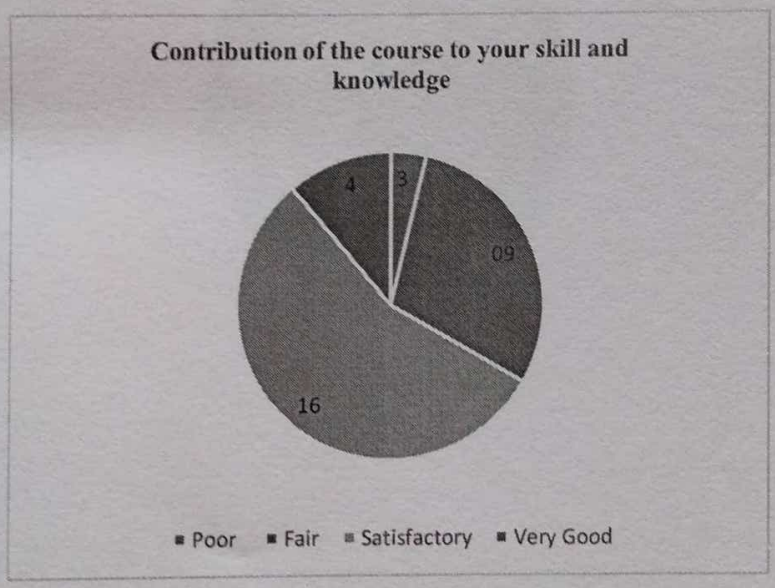
1. **Level of effort you put into the course**



2. Contribution of the course to your skill and knowledge



3. Skill and responsiveness of the instructor

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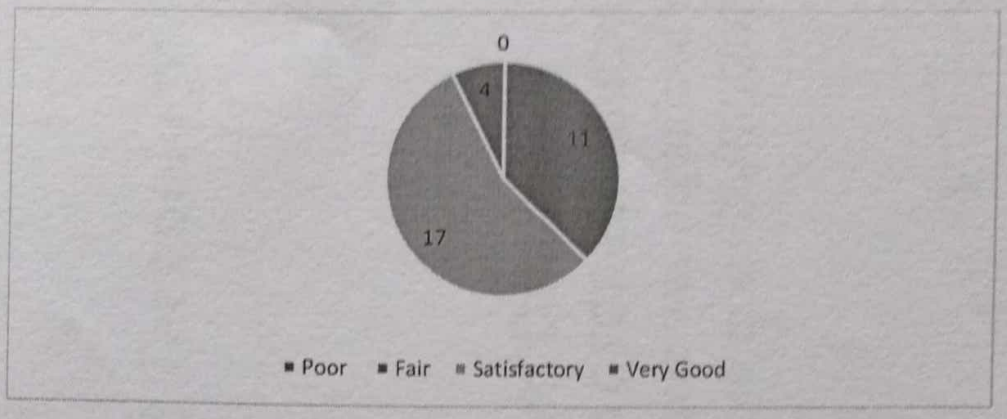
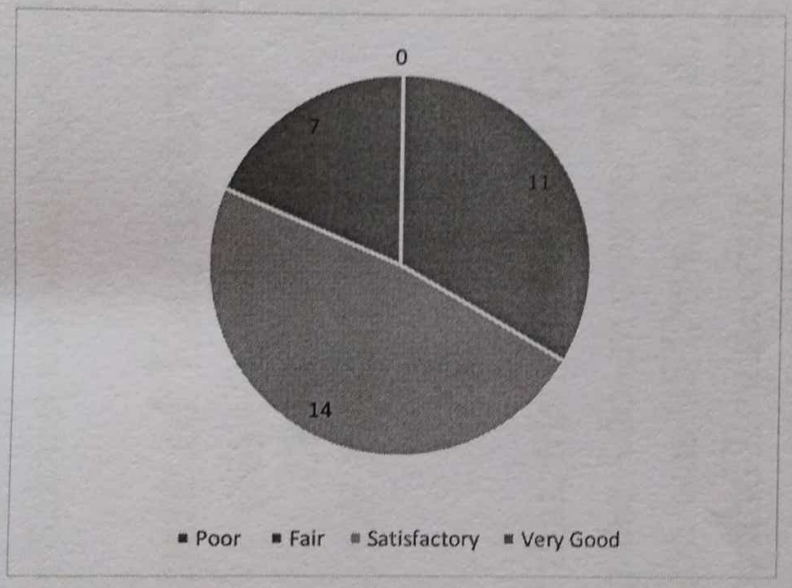


4. Course content was organized and well planned



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Department of Management Studies

Guru Nanak Institutions
EDUCATION FIRST

Date:27/06/2019

MBA

NOTICE

All the Students of Management are here by informed that, Department of Management is organising Ten days programme on **"Leadership SKILLs for Managers"** from 01/07/2019 to 10/07/2019 from 10:00 A.M to 4:00 P.M.

All the interested students of Management must register for the same before27/06/2019.

For Registration contact Dr**. Jonathan Joseph** Coordinator, Department of Management studies.

J. gidwani

Dr. Jaspal Gidwani
HOD, DMS

Copyto:

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10DaysProgrammeon
"LeadershipSKILLsforManagers"
from 01/07/2019 to10/07/2019"

RegistrationForm

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Mob: _____

mail: _____

mount (Cash) :_____

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ignatureof Participant _____

ORGANIZINGCOMMITTEE

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- *Mrs.TanpreetKaurTuli,M.D,GNI*

ADVISORS

- **Dr.SanjeevShrivastava,Principal ,GNIET**
- **Dr.JaspalGidwaniHOD,GNIET**

CONVENER

- **Dr.JonathanJoseph**

CO-ORDINATIONCOMMITTEE

- **Dr.JaspalGidwani**
- **Dr.PravinBhise**
- **Mr.RajendraKatole**

Guru Nanak Institutions
EDUCATION FIRST

GuruNanakInstituteofEngineeringand Management, Nagpur

10DaysProgrammeon
"Certificatecoursein"Leadership SKILLs for Managers""

(1st July to 10th July2019)

Organized by

DEPARTMENTOFMANAGEMENT STUDIES

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Aboutcollege:

Guru Nanak Institute of Engineering & Technology (GNIET), Nagpur was stablished in the year 2007 and is ffiliated to Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur, pproved by All India Council for Technical Education, New Delhi and Directorate of Technical Education, Maharashtra. Experienced and ledicated staff is an asset of the nstitute. GNIET focuses on the core ngineering field which makes it an deal place for the growth of technical ducation. GNIET has the state of the rt laboratories, digital library, Wi-Fi nd otherfacilities toenhancequality of eaching learning process.

AboutCertificateProgram:

The Objectives of the course are:
Exposure to environments under which lifferent organizations work

Providingon-jobexperienceofpractical spects of leadership skills

Developing disciplined attitude required to become an Accountant.

Highlights:

- To learn Communication &Problem Solving Skills
- Tolearn DecisionMaking &Collaborative Team Building

ResourcePersons:

□Dr.JonathanJoseph

ForWhom:

- Management & Engineering Students

Schedule:-

(1st Julyto 10th July2019)

10/07/2019Certificatepresentation& Valedictory Program

RegistrationFees:Rs.1,500per participant

Venue
SardarKohliAuditorium,GNIET

GNICampus
Dahegaon,KalmeshwarRoad,Nagpur.
441501 Maharashtra India
Ph:07118-661450

Foranyquerypleasecontact:

- Dr.JaspalGidwani
- Dr.PravinBhise
- Mr.RajendraKatole

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"LEADERSHIPSKILLSFOR MANAGERS"

from01/07/2019to10/07/2019from10:00A.M to4:00P.M.

COURSE OBJECTIVES:

Thiscourseofferscomprehensiveobjectivesdesignedto:

1. Buildleadershipskillsformanagersindifferentareas
2. Developstrategicthinkingtomanagingaglobal,mobileworkforce
3. Learnmoreabouthowcollaborationteamworks
4. ImproveCommunicationskillstosupportorganizations

COURSECONTENTS:

Syllabus
Duration:30Hours

Module1:WhyLeadershipSkillsforManagersAreImportant?

Onacompany-by-companybasis, manyofthecostsofineffective leadersare intangible. For instance, how can you measure the revenue your company might have achieved if onlyyour managers were better strategic thinkers, orthe kind of mentors who drew out hidden talent?

Othercostsareeasierto measure. Insurveysofemployeeengagement, employees'perceptionsof their managersrisetothetopasamajor influencer, responsible for upto 70%ofthedifference in engagement rates between business units. And since engagement influences everything from productivity to turnover rates.

Few companies give their management practices the attention theydeserve. Grovo found that a shocking98%ofmanagers feelmanagersattheircompaniesneed moretrainingoneverything from strategic thinking to conflict resolution; 87% of them reportedtheywished they had received more leadership training themselves. Perhaps most significantly, 98% of respondents also said they believed KPIs like employee retention and revenue would improve with increased training.

Module2:ImportantLeadershipSkillsforManagers
Whiletherearemanywaysto improve yourpeople management skills,thefirst stepistoidentify which areas need improvement. Which skills will have the biggest impact onthe bottom line? Whiletechnicalskillsarekeyfor individualcontributors,successasa leadertendstorequireasolid set of soft skills as well.

Herearesevenkeyleadershipskills for managers intoday'sworkforce. Youcanusethis manager skills list as you weigh upskilling needs for your organization or advance professionally on your own.

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GURUNANAKINSTITUTEOFENGINEERING&TECHNOLOGY
Dahegaon,KalmeshwarRoad,Nagpur-441 501.
Department of Management Studies

Date:13/07/2019

Reporton

"Certificate Course in Leadership Skills for Managers"

The Department of Management Studies had organized ten days programme on Certificate Course in Leadership Skills for Managers from1/07/2019to10/07/2019 The objectives of the course were successfully met :

This course offers comprehensive objectives designed to:

1. Buildleadershipskillsformanagersindifferentareas
2. Developstrategicthinkingtomanagingaglobal,mobileworkforce
3. Learnmoreabouthowcollaborationteamworks
4. ImproveCommunicationskillstosupportorganizations

Students came to learn why Leadership Skills for Managers are so important

Students participated enthusiastically and were benefitted by the contents which included communication skills, collaborative skills, decision making, problem solving, delegation of authority , integrity, strategic thinking and much more

Courseoutcomeafter attendingthiscourseprogram,studentswerebeableto:

1. Understandandbuildleadershipskillsformanagersindifferentareas
2. Developstrategicthinkingtomanageemployees
3. Applydecisionmakingincollaborativeteam
4. ExecuteDelegationofauthorityandCommunicationskillstosupportorganizations

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Total29studentshaveparticipatedinthisprogrammeandhaveexpressedtheirkeeninterestin attending more such courses in future.

All the students really appreciated the contents that were discussed, they realized that interactionslike these can help them improve their leadership skills.

J. Gidwani

Dr.JaspalGidwani
HOD,DMS

Principal
Guru Nanak Institute of
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engagement should look first at howtheytrain their managers. If theyaren't providing thorough leadershiptrainingandsupporttonewandexisting managers,they're missingakeyopportunityto improve company culture across the board.

COURSE OUTCOMES:

After completionofthecoursestudentswillbeableto :

1. Understandandbuildleadershipskillsformanagersindifferentareas
2. Developstrategicthinkingtomanageemployees
3. Applydecisionmakingincollaborativeteam
4. ExecuteDelegationofauthorityandCommunicationskillstosupportorganizations

J. gidwani

Dr.JaspalGidwani
HOD,DMS

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Module3:LeadershipSkills:

1. EmotionalIntelligence
Emotional intelligence—the ability to understand and controlone's own emotions, read and react appropriatelytoothers'emotions, and managerelationships—maysoundsimple. But accordingto Harvard Business Review, it accounts for nearly90% of what makes high performers stand out.

2. CommunicationSkills
Strong leaders can effectively communicate their vision to boththeir teamand those above them. But morethanthat,theymust beableto adjust andtailortheircommunicationsappropriatelyfora wide array of situations and people, often across cultural and generational lines.

3. Delegation
Noonecando it all. Managerswho struggletodelegatearelikelytohaveadifficult time meeting their department's productivitygoals and deliveringthe highest-qualitywork. But morethanthat, theycan also discourage their employees. Effective managers identify the right team member to handleaspecifictaskandensuretheyhavetheknowledgeandtoolstheyneedtogetthejobdone.

Module4:LeadershipSkills:

4. Motivating Others
Amotivated teamisaproductiveteam. Butthedriversofmotivationarehighlypersonal. Capable managers are able to identify what motivates each member of their team—whether that's opportunities forgrowthorsimplerecognition—andusethat knowledgetokeepthementhusiastic about that work. Theyalso understand that treating employees with respect and fairness is a powerful motivator in and of itself.

5. StrategicThinking
Today's marketplace is shifting rapidly, fromconsumer demand totechnology. Managers need to be able to seethe big picture and think strategicallyto lead their teams to success. Inthis context, strategicthinking includestheabilitytoassessasituation,set realisticgoals,anddevelopaplanto reach them. Strategic thinkers can adapt to changing circumstances and communicate their importance to their team and their own leadership.

6. Integrity
Noonelikesbeingtreatedunfairly,andemployeeswho feeltheyare not respectedbytheir manager or that theycannot trust their manager are likely to quickly disengage. Leaders who act with integrity, on the other hand, prioritize fairness and honesty, creating an environment oftrust within their team.

7. Flexibility
As the pandemic has demonstrated, unexpected conditions can change the way we work at anytime. Strong leaders can quicklyassess new challenges and pivot as needed, even when it's uncomfortable.Flexiblemanagersunderstandthatworkandprocessescanalwaysbeimproved,and don't hang onto certain ways of doing things just for the sake of it.

Great leadershipdependsonacomplexinterplayofmanagerskillsandcircumstances,but it does not emerge on its own. Companies looking to improve their bottom line and build employe

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