

Dahegaon, Kalmeshwar road, Nagpur

NAAC Accredited

Session 2022-23 (EVEN)

ACADEMIC CALENDAR

(For B.E./ B. Tech. – VI Sem / VIII Sem)

Sr.	Months	Teaching Learning	Date
No. 1		Faculty Subject Choice	12 th Dec 2022
2	Dec-	Load Calculation, Departmental Planning and Faculty Requirement	15 th Dec 2022
3	2022	Verification of Classrooms and Laboratories	19 th Dec 2022
4	2022	Stationary/ Equipment Requirement	30 th Dec 2022
5		Display of Time- Table (6 th and 8 th Sem)	5 th Jan 2023
6		Preparation of Student List and Attendance Register (6 th and 8 th Sem)	9 th Jan 2023
7		Preparation of Lesson Plan/ Teaching Plan	11 th Jan 2023
8		Commencement of Classes (6 th and 8 th Sem)	16 th Jan 2023
9	Jan-	Student Registration to the Department (6 th and 8 th Sem)	16 th to 30 th Jan 2023
10	2023	Finalization of Elective (6 th and 8 th Sem)	18 th Jan 2023
11		Teacher Guardian Allotment (6 th and 8 th Sem)	20 th Jan 2023
12		Preparation of Course File, Lab Manual etc.	23 rd Jan 2023
13		Republic Day	26 th Jan 2023
14		Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting	23 rd to 27 th Jan 2023
15		Attendance Review	30 th Jan 2023
16		Monthly Audit/ Student Feedback	31 st Jan 2023
17		Display of Assignment-I & II, Submission: 06-02-2023	1 st Feb 2023
18		Project Presentation-I	6 th to 11 th 2023
19	Feb-	Display of Question Bank (6 th and 8 th Sem)	13 th Feb 2023
20	2023	Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting	13 th to 17 ^h Feb 2023
21		Sessional-I Exam (6 th and 8 th Sem)	20st to 24th Feb 2023
22		Paper Publication in Journal/Conference (8 th sem)	24 th Feb 2023
23		Attendance Review/ Parents Teacher Meeting	27 th Feb 2023
24		Monthly Audit/ Student Feedback	28 th Feb 2023
25	March-	Display of Assignment-III & IV, Submission: 06-03-23	1 st March 2023
26	2023	Display of Question Bank (6 th and 8 th Sem)	2 nd March 2023
27		Project Presentation-II	6 th to 10 th March 2023
28		Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting/ Forum	13 th to 17 th March 2023
		Installation	th th
29		Sessional-II Exam (6 th and 8 th Sem)	20 th to 24 th March 2023
30		Attendance Review/ Provisional Student Detention List	29 th March 2023
31		Monthly Audit/ Student Feedback	31 st March 2023
32	A *1	Display of Assignment-V & VI, Submission: 11-03-23	4 th April 2023
33	April-	Display of Question Bank (6 th and 8 th Sem)	7 th April 2023

34	2023	Project Presentation-III	10 th to 14 th April 2023
35 36		Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting	17 th to 21 st April 2023
36		Attendance Review/ Final Student Detention List	25 th April 2023
37		Monthly Audit/ Student Feedback	28 th April 2023
38	May-	Internal Practical (6 th and 8 th Sem)	1 st to 5 th May 2023
39	2023	PUT Exam (6 th and 8 th Sem)	8 th to 12 th May 2023

Note: - Above mentioned date are subject to change due to unavailable circumstances.

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Prof. NehaChourasia Prof. RajendraBhombe

Dr. HemantHajare

Associate Dean (Academics)

Vice- Principal

Principal



${\bf Dahegaon, Kalmeshwar\ Road, Nagpur}$

NAAC Accredited

Session 2023-24 (ODD)

Date: 26.06.2023

ACADEMIC CALENDAR

(For B.E. / B. Tech. –VII Sem)

Original

		Originai
Sr.No.	Teaching Learning	Date
1	Faculty Subject Choice	12/06/2023
2	Load Calculation, Department Planning and Faculty Requirment	13/06/2023
3	Verification of Classrooms and Laboratories	14/06/2023
4	Stationary/ Equipment Requirement	15/06/2023
5	Finalization of 7 th Sem project group & Allotment of Project Guide	16/06/2023
6	Display of Time- Table Preparation	19/06/2023
7	Student List and Attendance Register (7 th sem)	21/06/2023
8	Preparation of lesson Plan/ Teaching plan	22/06/2023
9	Commencement of Classes	26/06/2023
10	Student Registration to the department	26/6/2023 to10/7/2023
11	Finalization of Electives (7 th Sem) + Project	26/06/2023to 30/6/2023
12	Teacher Gordian Allotment	28/06/2023
13	Preparation of Course File, Lab Manual etc.	3/7/2023
14	Industrial Visit/ III Cell Activity/ T & P Activity/ TGM	5/7/2023-12/7/2023
15	Attendance Review	21/7/2023
16	Student Feedback	24/7/2023
17	Monthly Audit/Finalization of 7 th sem Project with Title & Industry Tie up	26/7/2023
18	Display of Assignment-I & II, Submission date: 3/8/23	27/7/2023
19	Display of Question Bank	28/7/2023
20	Sessional-I Exam	3/8/2023 to 9/8/2023
21	Project Presentation-I	10/8/2023-11/8/2023
22	Display of Result (Sessional-I) &Send Letter to Parents	14/8/2023
23	Synopsis Submission	17/8/2023
24	Attendance Review/ Parents Teacher Meeting	18/8/2023
25	Monthly Audit/ Students Feedback	24/8/2023
26	Display of Assign-III & IV, Submission date: 1/9/23	25/8/2023
27	Industrial Visit/ III Cell Activity/ T & P Activity/ TGM	21/8/2023-31/8/2023
28	Display of Question Bank	1/9/2023
29	Project Presentation-II	11/9/2023-12/9/2023
30	Sessional-II Exam	4/9/2023-8/9/2023
31	Attendance Review/Display of Provisional Detention List	6/9/2023
32	Display of Result (Sess II) & Send Letter to Parents	11/9/2023
33	Parents Teacher Meeting	12/9/2023
34	Monthly Audit/ Students Feedback	26/9/2023
35	Conductional of Remedial Classes for Slow Learner Students	11/9/2023-13/9/2023
36	Industrial Visit/ III Cell Activity/ TGM	25/9/2023-29/9/2023
37	Submission of Final Students Detention List	22/9/2023
38	Last Teaching Day & Display of Question Bank	29/9/2023
39	PUT Exam./ Internal Practical Exam	3/10/2023 to 9/10/2023
40	Student feedback by HoD	9/10/23
41	Display of Result (PUT) & Send Letter to Parents	12/10/23

Note: - Above mentioned date are subject to change due to unviable circumstances.

Dr. Satichandra RagitDean Academics (Associate)

Prof. Rajendra Bhombe

Dr. Hemant Hajare

Vice-Principal

Principal



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(Even) SESSIONAL 2 Time Table

EXAM VI SEM

Date: 20/03/2023

Day	Date	Time	Subject
Mon.	20/03/2023	11.45am-12.45pm	Compiler Design
Tue.	21/03/2023	11.45am-12.45pm	IOT
Thu.	23/03/2023	10.00am-11.00am	DOS
Fri.	24/03/2023	10.45am-11.45am	Economic in IT
Mon.	27/03/2023	1.30pm-02.30pm	Open Elective

Note:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 20, Time: 1Hr.

Prof. M. shrungare

Sessional In-charge

Dr. B Timande
Head of Department



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23 (Even) SESSIONAL 2 Time Table

EXAM VIII SEM

Date: 20/03/2023

Day	Date	Time	Subject
Mon.	20/03/2023	11.45am-12.45pm	CCC
Tue.	21/03/2023	11.45am-12.45pm	DF
Thu.	23/03/2023	10.45am-11.45am	ICS
Fri.	24/03/2023	10.45am-11.45am	DOS

Note:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 20, Time: 1Hr.

TAX -

Prof. M. shrungare Sessional In-charge Dr. B. Timamle

Head of Department



Pre-University Test (PUT) EXAM

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INTERNAL PRACTICAL EXAM Time Table

Date: 17/04/2023

Day	Date	Time	Theory/	VIII
100			Practical	
Monday	24/04/2023	10.00am-1.00pm	Theory	Distributed Operating System
Wionday	24/04/2023	1:30pm- 3:30pm	Practical	Distributed Operating System
			Theory	Information & Cyber
		10.00am-1.00pm		Security
Tuesday	25/04/2023	1:30pm- 3:30pm	Practical	Information & Cyber Security
Wednesday	26/04/2023	10.00am-1.00pm	Theory	Clustering & Cloud Computing
		1:30pm- 3:30pm	Practical	Project/Seminar
Thursday	27/04/2023	10.00am-1.00pm	Theory	Digital Forensic

Note:

Instruction of PUT Examination Summer 2023

- Theory Exam time will be 3 hour.
- Theory paper will be 80 marks.
- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.
- For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrungare
Exam In-charge

Dr. B Tomande Head of Department

Head of Department Computer Science & Engineering GNIET, Dahegaon, Nagpur



Pre-University Test (PUT) EXAM &

INTERNAL PRACTICAL EXAM Time Table

Date: 17/04/2023

Day	Date	Time	Theory/ Practical	VI
Monday		10.00am-1.00pm	Theory	Compiler Design
Wionday	24/04/2023	1:30pm- 3:30pm	Practical	Compiler Design
		10.00am-1.00pm	Theory	IOT
Tuesday	25/04/2023	1:30pm- 3:30pm	Practical	Professional Skill-II
	2 6 10 4 12 0 2 2	10.00am-1.00pm	Theory	Distributed Operating System
Wednesday	26/04/2023	1:30pm- 3:30pm	Practical	Hardware
Thursday	27/04/2023	10.00am-1.00pm		Open Elective
, naiseay	27/04/2023	1:30pm- 3:30pm	Practical	Project/Seminar
Friday	28/04/2023	10.00am-1.00pm		Economic in IT

Note:

Instruction of PUT Examination Summer 2023

- Theory Exam time will be 3 hour.
- Theory paper will be 70 marks.
- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.
- For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrungare

Exam In-charge

Dr. B Tanande Head of Department

Gemputer Science & Engineering GNIET, Dahegaon, Nagpur

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N. o	Name of Day Branch		Wednesday	Friday	Monday	Wednesday
	Date	15-05-2023	17-05-2023	19-05-2023	22-05-2023	24-05-2023
ဖ	Mechanical Engineering	Automation in Production	Industrial Management	Energy Conversion – III		Elective -III
					1.Finite Element Method 2.Computer Integrated Manufacturing	1.Advanced Manufacturing Techniques 2. Machine Tool Design
					3.Industrial fluid Power	3. Renewable Energy Systems
					4. Management Information Systems	4. Mechanical Vibrations
					5.Refrigeration & Air Conditioning 6. Stress Analysis	5. Advance I.C. Engine 6. Tribology
7	Computer Technology	Data Warehousing &	Cyber & Information		Elective —III	Elective -1V
		Mining	Security		1.Pattern Recognition	1. Computational Geometry
					2. Parallel Computing	2.Cloud Computing
					3.Bio Informatics	3.Digital Forensic
					4. Web Data Management	4. Cognitive Science
					5. Human Computer Interface	Digital Image Processing
œ	Computer Engineering	UNIX & Shell	Distributed Systems &		Elective III	Elective -IV
)		Programming	Grid Computing		1.Wireless Communication & Mobile Computing	 Real Time Systems
)			2.Software Testing & Quality Assurance	Expert System Design
					3.Bio-Informatics & Cyber Security	3. Multimedia System
•	Information Technology	Gamina Architecture &	Distributed Systems		Elective III	Elective -IV
n	monination recimpage	Drogramming			1. Embedded Systems	1.Cyber Security
		Ziogiaiiii g			2. Digital Image Processing	2.Clould Computing
					3. Pattern Recognition	3.E- Commerce & Enterprise Resource Planning
					4. Machine Learning	4. Wireless Sensor Networks
	-	Suitchagar & Protection	Industrial Management	Power plant Erection &	Power Plant Maintenance Practices	Elective -
10	Power Engineering	Switchigeal & Hotechian		Commissioning		1.Renewable Energy Systems
				•		2. Operations Research
						 Material Handling System
		1	Vihration & Aero elasticity	Elective A	Elective -11	Elective —III
11	Aeronautical Engineering	Air Fransportation		1. Theory of Elasticity	1. Finite Element Method	1.Experimental Stress Analysis
				2 Optimization Techniques	2.Airframe Maintenance & Repair	2.Fatigue & Fracture
				3. Kellability Ceritered Maintanance	3. Aero-Engine Maintenance & Repair	 Computational Fluid Dynamics
				4. Aircraft Mechanism-	4.Flight Dynamics	
				Analysis & Synthesis		
			GENERAL INSTRUCTION	GENERAL INSTRUCTION S FOR SEATING ARKANGEMEN I	MGEMENI	

GENERAL INSTRUCTION S FOR SEATING ARRANGEMENT

The seating arrangement for the Centre will be notified separately seven days prior to the Commencement of the written examination at the respective College / Centre, The Candidates concerned College by University The name of the College Centre is already Printed on the Admission Card of each Candidate. However, it is notified for general information of the Candidates that: -

Rashtrasant Tukadoji Maharaj University, Nagpur Board of Examination & Evaluation

(Dr. Prafulla M. Sable)

Director

Date: 10-04-2023



B.E Eighth Sem. (CBS). SUMMER 2023 (153 TO 163)

RASHTRASANT TUKADOJI MAHARAJ NAGPUR UNIVERSITY

B. E. EIGHTH SEMESTER (CREDIT BASED SYSTEMS) EXAMINATION OF SUMMER 2023

PROGRAMME: WRITTEN

	Wednesday	24-05-2023	Elective – III 1. Advanced Geotechnical Engineering 2. Advanced Concrete Technology 3. Advanced Reinforced Cement Concrete Design 4. Advanced Steel Design 5. Water & Waste Water Treatment 6. Applied Remote Sensing & GIS	Elective —III 1.Artificial Intelligence 2.Robotics & Automation 3. Speech Processing 4. Data Compression & Encryption	Elective —III 1.Bio-Medical Engineering 2.Advanced Microprocessor Peripherals 3.power Semiconductor Based Electric Drives 4. Electrical Distribution System	Elective —III 1. Random Signal Theory 2. Robotics & Automation 3. Satellite Communication 4. CMOS VLSI Design	Elective -IV 1. Advance Wireless Sensor Network 2. Digital Image Processing 3. Natural Language Processing 4. Digital Forensic
.M.	Monday	22-05-2023	Elective – II 1. Advanced Sructural Analysis 2. Pre-Stressed Concrete 3. Pavement Analysis & Design 4. Watershed Management 5. Environment Management System 6. Water Transmission & Distribution System 7. Geotechnical Investigation & Ground Improvement Techniques 8. Advanced Engineering Geology 9. Water Power Engineering 10. Forensic Civil Engineering 11. Dissater Response & Management Techniques	Elective – II 1. Wireless Sensor Network 2. Nanotechnology 3. Fuzzy Logic & Neural Networks 4.Satellite Communication	Elective —II 1.Entrepreneurship Development 2.Digital Signal Processing 3.Power Quality 4.EHV AC & HVDC Transmission	Elective —I 1. Wireless Sensor Network 2.Embedded System 3.Digital Image Processing 4.Artificial Intelligence	Elective —III 1.Pattern Recognition 2.Soft Computing Tech. 3.Optimization Techniques 4.Clustering & Cloud Computing
Time: 02.30 P.M. To 05.30 P.M.	Friday	19-05-2023	Construction Economics & Finance	CMOS VLSI Design		Wireless & Mobile Communication	
14	Wednesday	17-05-2023		Communication Network	Computer Applications in Power System	Computer Communication Network	Information & Cyber Security
	Monday	15-05-2023	Irrigation Engineering	Microelectromechanical System & System on Chip	Switchgear & Protection	Microwave & Radar Engineering	Distributed Operating System
	Name of Day	Date	Civil Engineering	Electronics Engineering	Electrical Engineering (Electronics & Power)	Elect. & Telecommunication / Elect. & Communication Engineering	Computer Science & Engineering
٥	No.		-	7	m	4	w

List of the Examiners at the Practical Examinations, Summer-2022-23

Examination: - VI Sem (C.B.S.) B.E. Computer Science and Engineering (Summer-2022-23) Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

Form No.13

CONFIDENTIAL

Centre-312

Subject	External Examiner	Internal Examiner	Days & Dates	Time	Total No. of
					Students
Design Patterns		Ms. K. Malpe <u>Kmalpe@gmail.com</u> 562/C/139 8767312533	Wednesday 03 May 2023	9.30 am to 04.30 pm	02
		Ms. V. Kamble			Þ
Computer Networks		<u>sairamvijaya@gmail.com</u> 500/C/648 9850917133	Thursday 04 May 2023	9.30 am to 04.30 pm	05
Mini Project		Ms. K. Malpe Kmalpe@gmail.com 562/C/139	Saturday 06 May 2023	9.30 am to 04.30 pm	02
		8767312533			



Principal GNIET, Nagpur

Principal GNIET, Nagpur

RASHTRASANT TUKADOJI MAHARAJ NAGPUR UNIVERSITY

List of the Examiners at the Practical Examinations. Summer-2022-23

Form No.13

CONFIDENTIAL

Centre-312

Examination: - VI Sem (C.B.C.S.) B.Tech. Computer Science and Engineering (Summer-2022-23) Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

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Total No. of Students	1.	11.	17	۱۲
Time	9.30 am to 04.30 pm	9.30 am to 04.30 pm	9,30 am to 04,30 pm	9,30 am to 04,30 pm
Days & Dates	Wednesday 03 May 2023	Thursday 04 May 2023	Priday 05 May 2023	Saturday 06 May 2023
Internal Examiner	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Ms. V. Kamble sairamvijaya@gmail.com 500/C/648 9850917133	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Ms. K. Malpe Kmalpe@gunail.com 562/C/139 8767312533
External Examiner				
Subject	Compiler Design	Professional Skill	Hardware	Mini Project



Principal GNIET, Nagpur

RASHTRASANT TUKADOJI MAHARAJ NAGPUR UNIVERSITY

List of the Examiners at the Practical Examinations, Summer-2022-23

Centre-312

CONFIDENTIAL

Form No.13

Examination: - VIII Sem (C.B.S.) B.E. Information Technology (Summer-2022-23) Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

Subject	External Examiner	Internal Examiner	Days & Dates	Time	Total No. of Students
Distributed Systems	A	Ms. V. Kamble <u>sairamvijaya@gmail.com</u> 500/C/648 9850917133	Wednesday 03 May 2023	9.30 am to 04.30 pm	01
Gaming Architecture And Programming		Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Thursday 04 May 2023	9.30 am to 04.30 pm	01
Project		Ms. V. Kamble sairamvijaya@gmail.com 500/C/648 9850917133	Saturday 06 May 2023	9.30 am to 04.30 pm	01

GNIET, Nagpur



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(EVEN) VI SEM EXTERNAL PRACTICAL EXAM

Time Table

Date: 22/04/2023

Day	Date	Time	VI
Wed.	03/05/2023	10:00am- 4:00pm	Compiler Design
Thu.	04/05/2023	10:00am- 4:00pm	Professional Skills
Fri.	05/05/2023	10:00am- 4:00pm	Hardware
Sat.	06/05/2023	10:00am- 4:00pm	Mini Project & Seminar

Note:

Instruction of External Practical Examination Summer 2023

- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.
 For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrubgare Exam In-charge

Dr. B. diffiande Head of Department

Head of Department Computer Science & Engineering GNIET, Dahegeon, Nagpur



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(EVEN) VI SEM

EXTERNAL PRACTICAL EXAM Time Table

Date: 28/04/2023

Day	Date	Time	VI
Wed.	03/05/2023	10:00am- 4:00pm	Compiler Design
Thu.	04/05/2023	10:00am- 4:00pm	Professional Skills
Fri.	05/05/2023	10:00am- 4:00pm	Hardware
Sat.	06/05/2023	10:00am- 4:00pm	Mini Project & Seminar

Note:

Instruction of External Practical Examination Summer 2023

- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.

For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrubgare Exam In-charge

Dr. B. Timande Head of Department

Head of Department Computer Science & Engineering GNIET, Dahegson, Nagpur



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(EVEN) VI SEM

EXTERNAL PRACTICAL EXAM Time Table

Date: 28/04/2023

Day	Date	Time	VI
Wed.	03/05/2023	10:00am- 4:00pm	Compiler Design
Thu.	04/05/2023	10:00am- 4:00pm	Professional Skills
Fri.	05/05/2023	10:00am- 4:00pm	Hardware
Sat.	06/05/2023	10:00am- 4:00pm	Mini Project & Seminar

Note:

Instruction of External Practical Examination Summer 2023

- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.
- For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrubgare Exam In-charge

Dr. B. Timande Head of Department

Head of Department Computer Science & Engineering GNIET, Dahegson, Nagpur



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(EVEN) VI SEM

EXTERNAL PRACTICAL EXAM Time Table

Date: 28/04/2023

Day	Date	Time	VI
Wed.	03/05/2023	10:00am- 4:00pm	Compiler Design
Thu.	04/05/2023	10:00am- 4:00pm	Professional Skills
Fri.	05/05/2023	10:00am- 4:00pm	Hardware
Sat.	06/05/2023	10:00am- 4:00pm	Mini Project & Seminar

Note:

Instruction of External Practical Examination Summer 2023

- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.

 The students have to properly maintain their lab manual properly with index and certificates.

For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrubgare
Exam In-charge

Dr B. Timande Head of Department

Head of Department Computer Science & Engineering GNIET, Dahegson, Nagpur



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(EVEN) VIII SEM

EXTERNAL PRACTICAL EXAM Time Table

Date: 28/04/2023

Day	Date	Time	VIII
Wed.	03/05/2023	10:30am- 4:00pm	Distributed Operating System
Thu.	04/05/2023	10:30am- 4:00pm	Information & Cyber Security

Note:

Instruction of External Practical Examination Summer 2023

- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.

Hote: - Ext. prout. Exam of project will be held affer University recommendion.

Prof. Mayuri Shrubgare
Exam In-charge

Dr. B. Timande Head of Department

rlead of Department Computer Science & Engineering GNIET, Dahegaon, Nagpur

RASHTRASANT TUKADOJI MAHARAJ NAGPUR UNIVERSITY List of the Examiners at the Practical Examinations, Summer-2022-23

Examination: - VI Sem (C.B.S.) B.E. Computer Science and Engineering (Summer-2022-23) Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

CONFIDENTIAL Form No.13

Centre-312

Subject	External Examiner	Internal Examiner	Days & Dates	Time	Total No. of
Design Patterns	V. P. Yadav, P.C.E. Nogpur	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Wednesday 03 May 2023	9.30 am to 04.30 pm	02
Computer Networks	A.U. Gakanskari NIT Negpin	Ms. V. Kamble satramvijava@umail.com 500/C/648 9850917133	Thursday 04 May 2023	9.30 am to 04.30 pm	02
Mini Project	NiGaments paree Nagpire	Ms. K. Malpe Kmalpe@gmall.com \$62/C/139 8767312533	Saturday 06 May 2023	9.30 am to 04.30 pm	23



Reshinstant Tukadoji Maharaj Assit, Registrar (Comf. Negpur University, Negpar,

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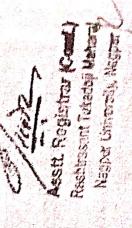
List of the Examiners at the Practical Examinations, Summer-2022-23

Examination: - VI Sem (C.B.C.S.) B.Tech. Computer Science and Engineering (Summer-2022-23)
Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

CONFIDENTIAL Form No.13

Centre-312

Total No. of Students	ш	U	T.	1 1
Time	9.30 am to 04.30 pm	9.30 am to 04.30 pm	9.30 am to 04.30 pm	9.30 am to 04.30 pm
Days & Dates	Wednesday 03 May 2023	Thursday 04 May 2023	Friday 05 May 2023	Saturday 06 May 2023
Internal Examiner	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Ms. V. Kamble sairanvijava@gmail.com 500/C/648 9850917133	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Ms. K. Malpe <u>Kmalpe@gmail.com</u> 562/C/139 8767312533
External Examiner	N. Aofe, Poe Nagpur S80600 2718	A-U. Gahaniost. NIT Nogport 9428525127	Atole chauban, Racek Asagrum— 9940468889	v. Ganerb Ped Negpyr 976466 930.
Subject	Compiler Design	Professional Skill	Hardware	Minal Projecti



RASHTRASANT TUKADOJI MAHARAJ NAGPUR UNIVERSITY List of the Examiners at the Practical Examinations, Summer-2022-23

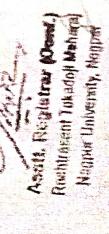
Form No.13

Examination: - VIII Sem (C.B.S.) B.E. Co.

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the Art is Computer Science and Engineering (Summer-2022-23)	tute of Engineering and Technolgy, Dahegaon, Nagnur-441501
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Total No. of	Students	23	62) 62
	Lime	9,30 am to 04.30 pm	9.30 am to 04.30 pm.	9.30-ant To 04,30 pm
	Days & Dates	Wednesday 03 May 2023	Thursday 04 May 2023	Saturday 06 May 2023
	Internal Examiner	Ms. V. Kamble sairamvijaya@gmail.com 500/C/648 9850917133	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Ms, V. Kamble sutramvijaya@gmail.com 500/C/648 9850917133
External Examiner	The state of the s	V.P.Yadav, Ree NDaypur 9aq6q24270	Atolo chauban, Karcek Sasparr 9970468839	
Subject		Distributed Operating Systems	Information & Cyber Security	Project



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RASHTRASANT TUKADOJI MAHARAJ NAGPUR UNIVERSITY List of the Examiners at the Practical Examinations, Summer-2022-23

Form No.13

CONFIDENTIAL

Centre-312

Examination: - VIII Sem (C.B.S.) B.E. Information Technology (Summer-2022-23)

Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

Suhiect	External Examiner	Internal Examiner	Days & Dates	Time	Students
Distributed Systems	R. Chaudhang.	Ms. V. Kamble sairamvijava@gmail.com 500/C/648 9850917133	Wednesday 03 May 2023	9.30 am to 04.30 pm	0
Gaming Architecture And	1993 S. 1893 S	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Thursday 04 May 2023	9.30 am to 04.30 pm	10
Programming	9922898175				
Project		Ms. V. Kamble sairamvijava@email.com 500LC/648	Saturday 06 May 2023	9.30 am to 04.30.pm	01

Rachtrasant Tukadoji Maharal Assit Registrar (Cost.) Negper University, Ne Joan

List of the Examiners at the Practical Examinations, Summer-2022-23

CONFIDENTIAL Form No.13

Centre-312

Examination: - IV Sem (C.B.C.S.) B.Tech. Computer Science and Engineering (Summer-2022-23) Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

ı tre: Guru Nan	ak Institute ot Engineeri	Centre: Guru Nanak Institute of Engineering and Technolgy, Dauegaon, 1996.	, independ		Total No of
Subject	External Examiner	Internal Examiner	Days & Dates	Time	Students
Data Structure and Program Design	A. Razeagye, AEET NoogPW	Ms. V. Kamble sairamvijaya@gmail.com 500/C/648 9850917133	Monday 29 May 2023	9.30 am to 04.30 pm	73
Database Management System	P. H. Gowandlan, Pee Noofpu 9923738489	Ms. V. Kamble sairamvijaya@gmail.com 500/C/648 9850917133	Tuesday 30 May 2023	9.30 am to 04.30 pm	73
Computer workshop-II	S.D. Zede, Nogram	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Wednesday 31 May 2023	9.30 am to 04.30 pm	73
	Contton.				

Rashtrasant Tukadoji Maharaj Asstt. Registrar (Conf.) Nagpur University, Negpek

List of the Examiners at the Practical Examinations, Summer-2022-23

Form No.13

CONFIDENTIAL

Examination: - VII Sem (C.B.S.) B.E. Computer Science and Engineering (Summer-2022-23) Centre: Guru Nanak Institute of Engineering and Technolgy, Dahegaon, Nagpur-441501

Centre-312

Total No. of Students	03	03	03
Time	9.30 am to 04.30 pm	9.30 am to 04.30 pm	9.30 am to 04.30 pm
Days & Dates	Tuesday 06 June 2023	Wednesday 07 June 2023	Thursday 08 June 2023
Internal Examiner	Ms. V. Kamble sairamvijaya@gmail.com 500/C/648 9850917133	Ms. K. Malpe Kmalpe@gmail.com 562/C/139 8767312533	Ms. V. Kamble <u>sairamvijaya@gmail.com</u> 500/C/648 9850917133
External Examiner	R. Chauobary, SBIIT Norgm	M. Makeepor, N. 17 Nogom	Dr. M.S.Khatibo, Acet Asograms 9823091923
Subject	Data Warehousing and Mining	Language processor	Project and seminar

Aastt Rogistrar (Conf.)

Rashtrasard Tukadoji Maharaj Nagpur University, Negous

List of the Examiners at the Practical Examinations, Summer-2022-23

Form No.13
CONFIDENTIAL

Centre-312

Centre: Guru Nanak Institute of Engineering and Technology, Dahegaon, Nagpur-441501 Examination: - VIII Sem (C.B.S.) B.E. Information Technology (Summer-2022-23)

Total No. of Students 0 9.30 am to 04.30 pm Time Days & Dates 08 June 2023 Thursday sairamvijaya@gmail.com Internal Examiner Ms. V. Kamble 500/C/648 9850917133 **External Examiner** 9096927270 V. Yadan, ReE Neagrous Subject Project

Asott. Registrar (Cont.) Reshtrasant Tokadoji Maharaj Nagpur University, Magner

List of the Examiners at the Practical Examinations, Summer-2022-23

UNIVERSITY Form No.13

222-23 CONFIDENTIAL

Centre-312

Examination: - VIII Sem (C.B.S.) B.E. Computer Science and Engineering (Summer-2022-23) Centre: Guru Nanak Institute of Engineering and Technology, Dahegaon, Nagpur-441501 Total No. of Students 62 9.30 am to 04.30 pm Time Days & Dates 08 June 2023 Thursday sairamvijaya@gmail.com Kmalpe@gmail.com 562/C/139 Internal Examiner Ms. V. Kamble Ms. K. Malpe 8767312533 9850917133 500/C/648 Dr. W. S. Kchatib, **External Examiner** 9823091923 Merpan Acer Subject Project





DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2023-24 (ODD)

SESSIONAL 1 Time Table

EXAM VII SEM

Date: 27 /08/2023

Day	Date	Time	Subject
Тни	03/08/2023	12.15pm-01.15pm	Cryptography & Network Security
FRI	04/08/2023	12.15pm-01.15pm	Optimization Techniques
MON	07/08/2023	12.15pm-01.15pm	Mobile Computing
TUE	08/08/2023	12.15pm-01.15pm	Power Transformer Engineer

Note:

1) It is compulsory for all students to solve paper in a proper format.

2) The question paper of each subject will be of descriptive pattern.

3) Maximum marks: 20, Time: 1Hr.

Prof. Mayuri Shrungare Sessional In-

charge

Dr. Balram Timande HoD CSE

Dr. Śatishchandra Ragit Academic Dean Prof. Rajendra Bhombe Vice-Principal Dr. Hemant Hajare Principal

Head of Department
Computer Science & Engineering
GMET, Dahegaon, Nagour



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2023-24(ODD) SESSIONAL-1 Time Table

EXAM V SEM

Date: 09/08/2023

10010

Day	Date	Time	Subject
Fri	11/08/2023	12.15pm-01.15pm	DAA
Mon.	14/08/2023	12.15pm-01.15pm	SEPM
Thu.	17/08/2023	12.15pm-01.15pm	DP
Fri	18/08/2023	12.15pm-01.15pm	ETC (Communication)
Mon	21/08/2023	12.15pm-01.15pm	AI

Note:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 30, Time: 1Hr.

Prof. Mayuri Shrungare Sessional Incharge Dr. Balraga Timande HoD CSE C. Satishchandra Ragit Academic Dean

Prof. Rajendra Bhombe Vice-Principal

Dr. Heman Hajare Principal



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2023-24(ODD) SESSIONAL 2 Time Table

EXAM V SEM

Date: 29/09/2023

Day	Date	Time	Subject
Mon	04/09/2023	12.15pm-01.15pm	Artificial Intelligence
Tue	05/09/2023	Teachers D	ay Celebration
Wed	06/09/2023	12.15pm-01.15pm	Design Analysis Algorithms
Thu	07/09/2023	12.15pm-01.15pm	Software Engineering & Project Management
Fri	08/09/2023	12.15pm-01.15pm	Design Pattern

Note:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 30, Time: 1Hr.

Prof. Mayuri Shrungare Sessional Incharge Dr. Balkam Timande HoD CSE

Dr. Satishchandra Ragit Academic Dean Prof. Rajendra Bhombe Vice-Principal Pr. Hemant Hajare Principal



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2023-24 (ODD) **SESSIONAL 2**

Time Table

EXAM VII SEM

Date: 29 /08/2023

Day	Date	Time	Subject
Mon	04/09/2023	12.15pm-01.15pm	Cryptography & Network Security
Tue	05/09/2023	Teachers Da	y Celebration
Wed	06/09/2023	12.15pm-01.15pm	Optimization Techniques
Thu	07/09/2023	12.15pm-01.15pm	Mobile Computing
Fri	08/09/2023	12.15pm-01.15pm	Power Plant Engineering

Note:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 30, Time: 1Hr.

Prof. Mayuri Shrungare Sessional Incharge

Balram Timande HoD CSE

Dr. Satishchandra Ragit **Academic Dean**

Prof. Rajendra Bhombe Vice-Principal

Dr. Hemant Hajare Principal



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Session 2023-24(ODD) SESSIONAL-1 Time Table

EXAM III SEM

Date: 18/09/2023

Day	Date	Time	Subject
Thu	27/09/2023	12.15pm-01.15pm	M-III
Fri	29/09/2023	12.15pm-01.15pm	OOPS
Tue	03/10/2023	12.15pm-01.15pm	OS
Wed	04/10/2023	12.15pm-01.15pm	CADS
Thu	05/10/2023	12.15pm-01.15pm	EIT
Fri	06/10/2023	12.15pm-01.15pm	UHV

Note:

1) It is compulsory for all students to solve paper in a proper format.

2) The question paper of each subject will be of descriptive pattern.

3) Maximum marks: 30, Time: 1Hr.

Prof. Mayuri Shrungare Sessional In-

charge

Timande HOD CSE Dr. Satishchandra Ragit Academic Dean

Prof. Rajendra **Bhombe** Vice-Principal

Dr. Hemant Hajare Principal



Pre-University Test (PUT) EXAM

INTERNAL PRACTICAL EXAM

Time Table

Date: 10/10/2023

Day	Date	Time	Theory/ Practical	VII
		10.00am-1.00pm	Theory	Cryptography & Network System
Monday	16/10/2023	2:00pm- 5:00pm	Practical	Cryptography & Network System
		10.00am-1.00pm	Theory	Mobile Computing
Tuesday	17/10/2023	17/10/2023 2:00pm- 5:00pm	Practical	Project (G1 to G5)
		10.00am-1.00pm	Theory	Optimization Technique
Wednesday	18/10/2023	2:00pm- 5:00pm	Practical	Project(G6 to G10)
	10/10/2022	10.00am-1.00pm	Theory	Power Plant Engineering
Thursday	19/10/2023	2:00pm- 5:00pm	Practical	Project(G11 to G15)

Note:

Instruction of PUT Examination Winter 2023

- Theory Exam time will be 3 hour.
- Theory paper will be 70 marks.
- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.
- For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrungare

Dr. Balram Fimande HoD CSE

Dr. Satishchandra Ragit Academic Dean

Prof. Rajendra **Bhombe** Vice-Principal

Principal

Sessional I/C

rlead of Department

Computer Science & Engineering GNIET, Dahegaon, Nagpur



Pre-University Test (PUT) EXAM

INTERNAL PRACTICAL EXAM Time Table

Date: 10/10/2023

1949	4 + 1	and the second s		Date: 10/10/2023
Day	Date	Time	Theory/ Practical	VII
		10.00am-1.00pm	Theory	Cryptography & Network System
Monday	16/10/2023	2:00pm- 5:00pm	Practical	Cryptography & Network System
Tuesday	lay 17710/2023	10.00am-1.00pm	Theory	Mobile Computing
		2:00pm- 5:00pm	Practical	Project (G1 to G5)
	18/10/2023	10.00am-1.00pm	Theory	Optimization Technique
	10/10/2023	2:00pm- 5:00pm	Practical	Project(G6 to G10)
	hursday 19/10/2023 10.00am-1.00pm	10.00am-1.00pm	Theory	Power Plant Engineering
	100	2:00pm- 5:00pm	Practical	Project(G11 to G15)

Note:

1. Instruction of PUT Examination Winter 2023

- Theory Exam time will be 3 hour.
- Theory paper will be 70 marks.
- Practical exam will be conducted.
- Practical exam will be 25 marks.
- Students have to properly maintain their lab manual properly with index and certificates.
- For project you have to come with PPT of your project and thesis and with complete Execution of Your Project

Prof. Mayuri Shrungare

Dr. Balram Timande HOD CSE

Dr. Satishchandra Ragit

Prof. Rajendra Bhombe Vice-Principal

Dr. Hemant Hajare Principal

Sessional I/C

riead of Department

Computer Science & Engineering Cademic Dean GNIET, Dahegaon, Nagpur



EXTERNAL PRACTICAL EXAM Time Table

Date: 05/12/2023

Day	Date	Time	VII
Wed	06/12/2023	10:3am- 5:00pm	Cryptography & Network System
Thu	07/12/2023	10:3am- 5:00pm	Project

Dr. Balram Timande HoD CSE Prof. Rajendra Bhombe Vice-Principal Dr. Hemant Hajare Principal



EXTERNAL PRACTICAL EXAM <u>Time Table</u>

Date: 05/12/2023

Day	Date	Time	V
Wed	06/12/2023	10:3am- 5:00pm	Artificial Intelligence
Thu	07/12/2023	10:3am- 5:00pm	Data Analysis & Algorithm
Fri	08/12/2023	10:3am- 5:00pm	Professional Skills

Dr. Balram Timande HoD CSE Prof. Rajendra Bhombe Vice-Principal Dr. Hemant Hajare Principal



EXTERNAL PRACTICAL EXAM <u>Time Table</u>

Date: 05/12/2023

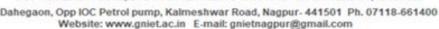
Day	Date	Time	III
Wed	06/12/2023	10:3am- 5:00pm	Object Oriented Programming with JAVA
Thu	07/12/2023	10:3am- 5:00pm	Operating System
Wed	08/12/2023	10:3am- 5:00pm	Computer workshop-1

Dr. Balram Timande HoD CSE

Prof. Rajendra Bhombe Vice-Principal Dr. Hemant Hajare Principal



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Department of Applied Sciences & Humanities

B.Tech First Year

(Session 2022-23)

Mechanism of internal assessment is transparent and robust in terms of frequency and mode

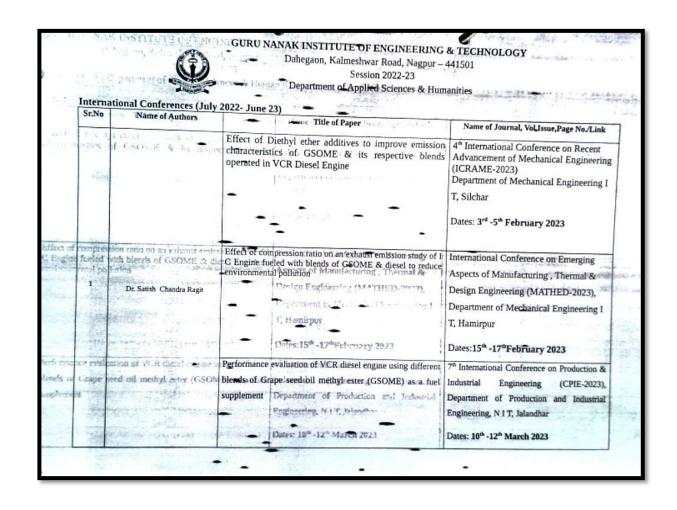
- Academic calenders
- Notices regarding internal examinations
- Exam Time tables



GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY





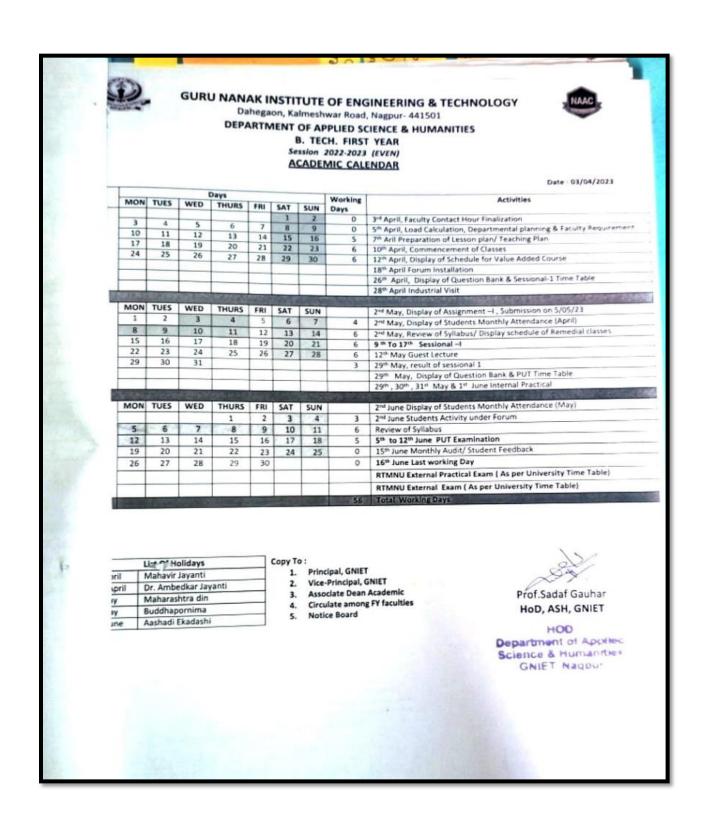




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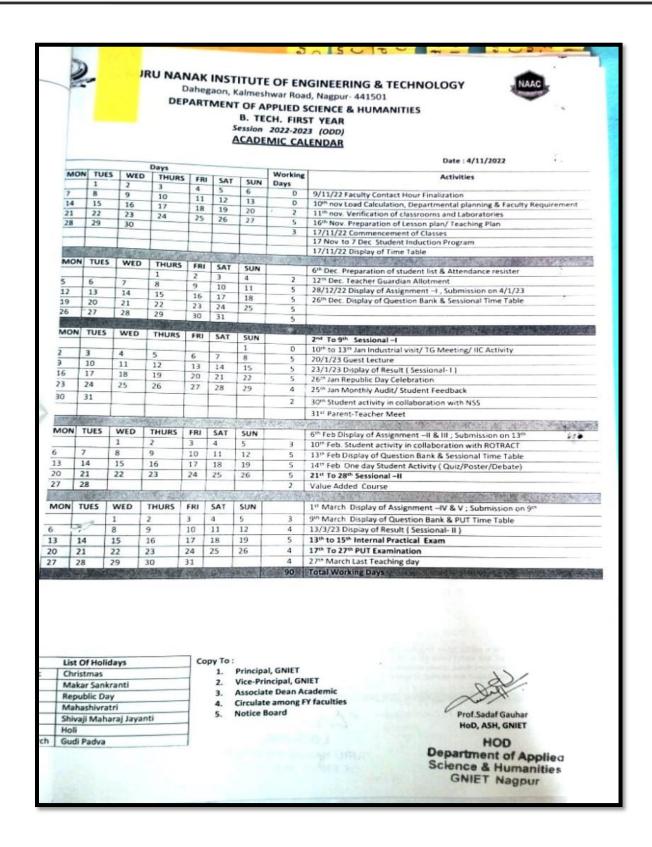




GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY









GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY





			DEP	ARTI	S	B. TE	PPLIED S CH. FIRS 2022-202 MIC CAL	
MOI	TUES	WED	Days				Working	Date 4/11/2022
7	1	2	THURS 3	FR1	SAT	SUN	Days	Activities
1-4	15	9	10	11	12	1.0	0	9/11/22 Faculty Contact Hour Finalization
21	22	23	17	18	14	20	0	10" nov Load Calculation, Departmental planning & Laurit, Ba-
28	29	30	- 24	25	20	2.7	5	
				-			3	16" Nov. Preparation of Lesson plan/ Teaching Plan 17/11/22 Commencement of Classes
								an rede to a Dec. Student Induction Program
MOR	TUES	-						17/11/22 Display of Time Table
14000	LOES	WED	THURS	FRI	SAT	SUN		6" One Property and a few last file.
5	6	7	8	- 0		-4	2	6th Dec. Preparation of student list & Attendance resister. 12th Dec. Teacher Guardian Allotment.
1.7	13	14	15	9	1.0	-4	5	28/12/22 Display of Assignment -I. Submission on 4/1/23
26	27	28	29	30	-	-2	. 5	26th Dec. Display of Question Bank & Sessional Time Table
2300	Pres	SBRE	CALLERY		CHES	0.000	5	
MON	TUES	WED	THURS	FRI	SAT	SUN		2 ⁻⁴ To 9 th Sessional -I
- 7	-						0	10° to 13° san industrial visit/ TG Meeting/ IIC Activity
9	3	4	5	6		4	5	20/1/23 Guest Lecture
16	17	11	12	13	- 1	10	5	23/1/23 Display of Result (Sessional-1)
23	24	25	261	27		7.5	5	26" Jan Republic Day Celebration
30	31			-	-2	-	4	25" Jan Monthly Audit/ Student Feedback
-		+	-	-		-	2	30" Student activity in collaboration with NSS 31" Parent-Teacher Meet
	N. V. D. P.	-	-	-		-		31" Parent-Teacher Meet
MON	TUES	WED	THURS	FRI	SAT	SUN		6" Feb Display of Assignment -II & III _ Submission on 13"
		1	2	3		-	3	10" Feb. Student activity in collaboration with 8018A."
6	7	8	9	10			5	13" Feb Display of Question Bank & Sessional Time Table
13	14	15	10	1.7	-	100	5	14" Feb. One day Student Activity (Quiz/Poster (Debate)
20	21	22	23	24			5 2	21" To 28th Sessional –II
27	28					_	-	Value Added Course
MON	TUES	WED	THURS	FRI	SAT	SUN		1" March Display of Assignment -IV & V. Submission on 9"
NION	TUES	WED	7	3	-	-	3	9" March Display of Question Bank & PUT Time Table
6	-	1 12	9	10	-	112	4	13/3/23 Display of Result Sessional-II
13	14	15	16	17	-	10	5	13 ^{cs} to 15 th Internal Practical Exam
20	21	2.2	2.3	24		12	4	17th To 27th PUT Examination
27	28	29	90	31			4	27th March Last Teaching day
F-8400	GE 40 15 E	Section	SAL VALL		100	C TIME	90	Total Working Days
n i	List Of Ho Christima Makar Sa Republic Mahashii Shivayi M	s inkranti Day	nyamti		1. 2. 3. 4. 5.	Princip Vice-F Associ	pal, GNIET Principal, G iate Dean i ate among e Board	NIET Academic FY faculties Prof. Sacal Garden HoD, ASH, GNET

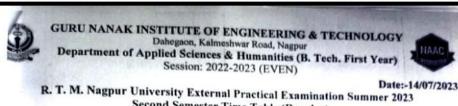


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APPROVED BY AICTE, DTE & AFFILIATED TO RTM NAGPUR UNIVERSITY, NAGPUR

Dahegaon, Opp IOC Petrol pump, Kalmeshwar Road, Nagpur- 441501 Ph. 07118-661400 Website: www.gniet.ac.in E-mail: gnietnagpur@gmail.com





r. o.	Date	Day	Time	Paper	University Roll No.	No. of Students
			10 am to 3 pm	Advanced Engineering Materials	170576 - 170630	55
	25-07-23	Tuesday	10 am to 3 pm	Applied Chemistry	170631 - 170685	55
			10 am to 3 pm	Computational Skills	170686 - 170740	55
			10 am to 3 pm	Workshop Practices	170741 - 170794	54
П			10 am to 3 pm	Applied Chemistry	170576 - 170630	55
	26-07-23	Wednesday	10 am to 3 pm	Advanced Engineering Materials	170631 - 170685	55
	20-07-23	Wednesday	10 am to 3 pm	Workshop Practices	170686 - 170740	55
			10 am to 3 pm	Computational Skills	170741 - 170794	54
			10 am to 3 pm	Computational Skills	170576 - 170630	55
	27-07-23	Thumday	10 am to 3 pm	Workshop Practices	170631 - 170685	55
	27-07-23	Thursday	10 am to 3 pm	Advanced Engineering Materials	170686 - 170740	55
			10 am to 3 pm	Applied Chemistry	170741 - 170794	54
7			10 am to 3 pm	Workshop Practices	170576 - 170630	55
		p.14	10 am to 3 pm	Computational Skills	170631 - 170685	55
	28-07-23	Friday	10 am to 3 pm	Applied Chemistry	170686 - 170740	55
	V. 1	1.0		Advanced Engineering Materials	170741 - 170794	54

Second Semester Time Table (Regular)

istruction of RTMNU Practical Examination Summer 2023

- Written exam time will be of 1 hr. Students need to submit the paper as per allotted time, Descriptive type
 questions will be asked in the examinations.
- Write the university roll number, if it is available.
- If any students fail to attend the exam as per allotted schedule, he/she will be consider as absent.
- Students must clear the balance fees, before attending the practical examination.
- Students must carry the practical record.
- If any change done by the University, College let you know before one day

Exam In charge m-Charge Sessional Exam

Sessional Exam
G.N.I.E.T., Nagpur
Science & Humanities
GNIET Nagpur

Vice-Principal
Principal
Principal
Guru Nanak Institute of
Engineering & Technology
Nagpur - 441501

ony Tor-

1. Associate Dean Academics

2. Circulate among Staff.

3. Circulate among Students.

4. Notice Board

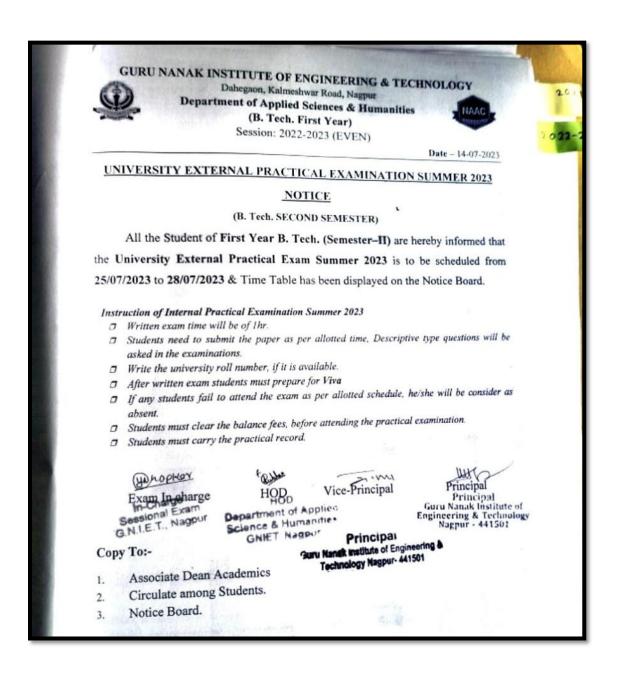
Guru Nanak institute of Engineering & Technology Nagpur. 441501



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Opp IOC Petrol pump, Kalmeshwar Road, Nagpur- 441501 Ph. 07118-661400 Website: www.gniet.ac.in E-mail: gnietnagpur@gmail.com



GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur

Department of Applied Sciences & Humanities (B. Tech. First Year)

Session: 2022-2023 (EVEN)

Date - 14-07-2023

rincipal

Guru Nanak Institute of

Engineering & Technology

UNIVERSITY EXTERNAL PRACTICAL EXAMINATION SUMMER 2023 (BACKLOG)

NOTICE

(B. Tech. FIRST SEMESTER)

All the faculties of First Year B. Tech. (Semester-I) (Backlog) are hereby informed that the University External Practical Exam Summer 2023 (Backlog) is to be scheduled on 24/07/2023 & Time Table has been displayed on the Notice Board.

Instruction of Internal Practical Examination Summer 2023 (Backlog)

- Written exam time will be of 1hr.
- Students need to submit the paper as per allotted time, Descriptive type questions will be asked in the examinations.
- Write the university roll number, if it is available.
- After written exam students must prepare for Viva
- If any students fail to attend the exam as per allotted schedule, he/she will be consider as
- Students must clear the balance fees, before attending the practical examination.
- Students must carry the practical record.

Supropicoy

Exam In charge Sessional Exam G.N.I.E.T., Nagpur

Vice-Principal

Department of Applied Science & Humanities GNIET Nagpur

Nagpur - 441501 Principai Guru Namek institute of Engineering &

Technology Nagpur. 441501

Copy To:-

Circulate among Students.

Notice Board.

1. Associate Dean Academics

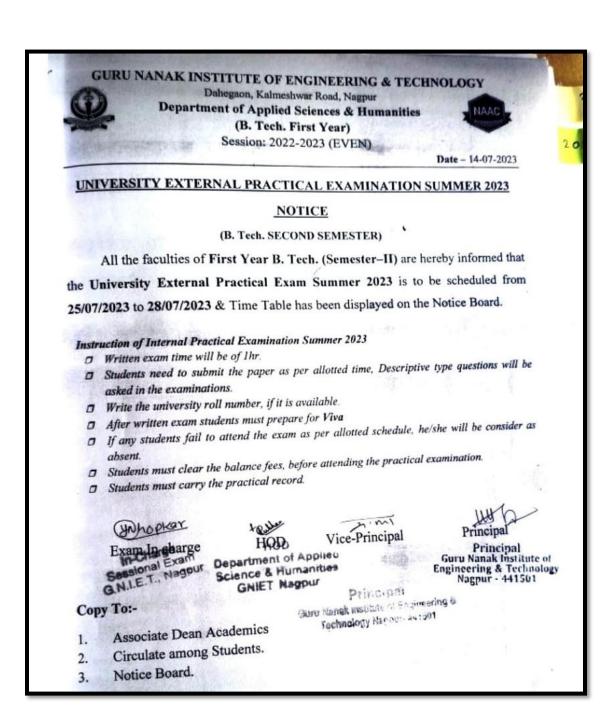
GURU NANAK INSTITUTE OF ENGINEERING AND TECHNOLOGY, NAGPUR



GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

PPROVED BY AICTE, DTE & AFFILIATED TO RTM NAGPUR UNIVERSITY, NAGPUR







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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar road, Nagpur

NAAC Accredited Session 2022-23 (ODD)

ACADEMIC CALENDAR

Sr.	Months	(For B.E./ B. Tech. – V Sem / VII Sem)	
No.		Teaching Learning	Date
1		Faculty Subject Choice	24th June2022
2	June-	Load Calculation, Departmental Planning and Faculty Requirement	27 th June2022
3	2022	verification of Classrooms and Laboratories	28 th June2022
4		Display of Project Guide With Specialization	29 th June2022
5	- de-11	Stationary/ Equipment Requirement	
6		Finalization of 7th Semester Project group & Allotment of Project Guide	30 th June 2022
		Display of Time- Table (7th Sem)	5 th July 2022
8		Preparation of Student List and Attendance Register (7th Sem)	5 th July 2022
9		Preparation of Lesson Plan/ Teaching Plan	7 th July 2022
10	-	Commencement of Classes (Final Year)	8 th July 2022
11	ģ	Student Registration to the Department (7th Sem)	11 th July 2022
12		Finalization of Elective-I (7 th Sem)	11 th to 30 th July 2022
13	July-	Teacher Guardian Allotment (7 th Sem)	12 th July 2022
14	2022	Preparation of Course File, Lab Manual etc.	14 th July 2022
15		Display of Time- Table (5th Sem)	15 th July 2022
16		Preparation of Student List and Attendance Register (5th Sem)	21st July 2022
17		Industrial Visit/III Coll Activity/The Activ	22 nd July 2022
18		Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting Attendance Review	18 th to 22 nd July 2022
	된 참	Student Feedback	27 th July 2022
19			28th July 2022
20		Monthly Audit/ Finalization of 7th Sem Projects with Title & Industry Tie-up	29 th July 2022
21		Commencement of Classes (Third Year)	1 st August 2022
22		Student Registration to the Department (5 th Sem)	1 st to 10 th August 2022
25		Display of Assignment-I & II, Submission: 10-08-22	2 nd August 2022
To the second second second		Teacher Guardian Allotment (5 th Sem)	5 th August 2022
26		Project Presentation-I	8 th to 12 th August 2022
27	Angiot	Display of Question Bank (7thSem)	12 th August 2022
28	August- 2022	Independence Day	15 th August 2022
29	2022	Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting	16 th to 19 th August 2022
30		Sessional-I Exam (7 th Sem)	22 nd to 26 th August 2022
31	X	Synopsis Submission	26 th August 2022
32		Attendance Review/ Parents Teacher Meeting	29 th August 2022
, 53		Monthly Audit/ Student Feedback	30th August 2022
34		Ganesh Utsav Festival 2022-23	31st August 2022

35	नागपुर	Display of Assignment-III & IV, Submission: 09-09-22	1 st September 2022
36	100 miles 2000	Display of Question Bank (5th Sem)	2 nd September 2022
37	1 10	Project Presentation-II	5th to 9th September 2022
38	Septem	Sessional-I Exam (5 th Sem) / Display of Question Bank (7 th Sem)	12 th to 16 th September 2022
39	ber- 2022	Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting/ Forum Installation	19 th to 23 rd September 2022
40		Sessional-II Exam (7 th Sem)	26 th to 30 th September 2022
41		Attendance Review/ Provisional Student Detention List	28th September 2022
42		Monthly Audit/ Student Feedback	29th September 2022
43		NavratriGarba Celebration-2022	3 rd October 2022
44	•	Display of Assignment-V & VI, Submission: 11-10-22	4 th October 2022
45	1837	Display of Question Bank (5 th Sem)	7 th October 2022
46	October	Project Presentation-III	10 th to 14 th October 2022
13	-2022	Sessional-II Exam (5 th Sem) / Display of Question Bank (7 th Sem)	17th to 21st October 2022
40	4.0	Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting	24th to 28th October 2022
49		Attendance Review/ Final Student Detention List	31st October 2022
50		Monthly Audit/ Student Feedback	31st October 2022
51	Novem	Internal Practical (5 th & 7 th Sem)	1 st to 4 th November 2022
52	ber- 2022	PUT Exam (5 th & 7 th Sem)	7 th to 11 th November 2022

Note: - Above mentioned date are subject to change due to unavailable circumstances.

Colomera

Prof. NehaChourasia

Associate Dean (Academics)

DIMP

Prof. RajendraBhombe

Vice- Principal

Militar

Dr. HemantHajare

Principal



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DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Session: 2022-23 (Even)

NOTICE SESSIONAL I EXAM

Date: 20/02/2023

All the students of 8th & 6th sem of ETC are hereby informed that the SESSIONAL I EXAM will be conducted from 27/02/2023 to 06/03/2023. The time table for the same is displayed on the departmental notice board. Exam will be conducted in college i.e., offline mode. Exam paper of each subject will be of 20 marks & duration to solve paper is 1 Hour.

Prof. Kajal Dhawale Exam In-charge Prof. Neha Chourasia HOD ETC, GNIET



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DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Session: 2022-23 (Even)

SESSIONAL I EXAM

Time Table

Date: 20/02/2023

Day	Date	Time	VI	VIII
Monday	27/02/2023	10:00-11:00 am	Computer Communication Netwrok	Microwave & Radar Engineering
Tuesday	28/02/2023	10:00-11:00 am	Internet of Things	Computer Communication Netwrok
Wednesday	01/03/2023	10:00-11:00 am	Wireless Sensor Network	Wireless Mobile Communication
Thursday	02/03/2023	10:00-11:00 am	Control System Engineering (PEC-II)	Embedded System (Ele-II)
Friday	03/03/2023	10:00-11:00 am	Solar PV system (OE-I)	CMOS VLSI Design (Ele-III)
Monday	06/03/2023	1:30-2:30 pm	Effective Technical Communication	•

Note:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 20, Time: 1Hr.
- 4) Clear balance fees dues if any

Prof. Kajal Dhawale Exam In-charge Prof. Neha Chourasia HOD ETC, GNIET



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Department of Electronics & Telecommunication

Year/Sem: II Yr/IV SEM Time: - 01 Hours Sessional - I

Subject: ASD Date:10/05/23 Maximum Marks: - 20

INSTRUCTIONS TO CANDIDATE.

- 1) All Questions carry marks as indicated.
 - 2) Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
 - 4) Assume suitable data whenever necessary.
- 5) Use of non programmable calculator is permitted.

Explain block diagram of Operational Amplifier

- Explain Inverting and non inverting Operational Amplifier with circuit diagram 7
- Explain Instrumentation amplifier with application. ä
- Explain Integrator and differentiator with circuit diagram 4.
- Explain monostable bistable and astable multivibrator. ۶.
- Explain 555 timer circuit with application.
- Explain unregulated DC power supply with rectifier circuit.
- Explain series voltage regulator.

Department of Electronics & Telecommunication Sessional - I

Year/Sem: II Yr/IV SEM

Subject: ASD Date:10/05/23 Maximum Marks: - 20

Time: - 01 Hours

1) All Questions carry marks as indicated. INSTRUCTIONS TO CANDIDATE.

- 2) Solve FIVE questions.
 3) Illustrate your answer wherever necessary with the help of neat sketches.
 - 4) Assume suitable data whenever necessary
- 5) Use of non programmable calculator is permitted.

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Department of Electronics And Telecommunication Engineering Session 2022-23 Even

Date: 17/04/2023

NOTICE

All the students of VI sem and VIII sem are hereby informed that their PUT Exam will be starting from 24/04/2023. Attendance is mandatory.

Dr. Sushma Telrandhe

HOD, ETC, GNIET

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- Associate Dean
- 3) Faculty members



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DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION **ENGINEERING**

Session 2022-23(EVEN)

PUT EXAM

Time Table

Date: 17/04/2023

Day	Date	Time	VI	VIII
Mon	24/04/2023	10.00 am - 1.00 pm	Computer Communication Network (Theory)	Microwave&Radar Engineering (Theory)
		2.00 am - 4.00 pm	Computer Communication Network (Practical)	Microwave&Radar Engineering (Practical)
<u> </u>		10.00am-1.00pm	Internet Of Things (Theory)	Computer Communication Network (Theory)
Tue	25/04/2023	2.00 am - 4.00 pm	Internet Of Things (Practical)	Computer Communication Network (Practical)
Wed	26/04/2023	10.00am-1.00pm	Wireless Sensor Network (Theory)	Wireless Mobile Communication
		2.00 am - 4.00 pm	Wireless Sensor Network (Practical)	19 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
Thu	27/04/2023	10.00am-1.00pm	Control System Engineering (PEC-II)	Embedded System
Fri	28/04/2023	10.00am-1.00pm	Solar PV System (Open Elective-I)	CMOS VLSLDesign
		2.00 am - 4.00 pm	Mini Project	Project
Sat	02/05/2023	10.00am-12.00pm	HSC:Effective technical communication	

- 1) It is compulsory for all students to solve paper in a proper format .
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 70 for 6th sem and Maximum marks: 80 for 8th sem
- 4) Sessional Exam will be conducted in the given slot, remaining classes will continue as per time table. Clear balance fees .dues if any

Prof. Minakshi Dhage Exam In-charge

Dr. Sushma Telrandhe Head of Department



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Department of Electronics & Telecommunication Engineering Pre-University Test

Subject: CCN

Maximum Marks: - 70

2. Solve two questions as follows: . All questions carry marks as indicated. INSTRUCTIONS TO CANDIDATE.

Date: 24/04/2023

Year/Sem: III Yr/VI Sem Time: - 03 Hours

Q. No. 7 OR Q. No. 8 Q. No. 5 OR Q. No. 6

Q. No. 3 OR Q. No. 4 Q. No. 1 OR Q. No. 2

Q. No. 9 OR Q. No. 10

Assume suitable data wherever necessary.

4. Illustrate your answer wherever necessary with the help of neat sketches.

4. a)		ь)	3. a)	b)	2. a)		(d	1. a)
Write short notes on selection of IEEE 802.11.a.b.g and n.	OR	What is packet switching? Explain the structure of Datagram switching and virtual switching.	Explain difference between guided and unguided media.	Explain protocols and standards.	Compare ATM and frame Relay.	OR	lain OSI Reference model and compare it with TCP/IP model.	What is the Importance of layering architecture. Discuss problems associated with it.
7	K	7	7	7	7	-	7	7

<u>b</u>

Explain Sonet N/T

06	Explain the concept of streaming Audio / video 6	Explain
	OR	
- 1 = -	ii) Simple mail transfer protocol iii) DNS.	ii) Simpl iii) DNS.
	Write short notes on 1) File transfer protocol	Write sh
ng & Dísi	Explain Routing algorithms shortest path routing & Distance vector routing.	Explain F routing.
	Explain 3 phase operation in TCP.	Explain 3
	OR	
	Explain IPv6 Addressing.	Explain I
trol in c	What do you mean by the term congestion control in communication network.	What do network.
	Explain Active Hubs and passive Hubs.	Explain A
ols,	Explain Go Back-N and selective repeats protocols.	Explain G
	OR	
	What is ERROR control and flow control.	What is E
HDLC fra	Write short note on point to point protocol and HDLC frames.	Write sho



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Department of Electronics and Telecommunication Engineering Session 2022-23 Even

Date: 16/03/2023

NOTICE

All the students of VI sem and VIII sem are hereby informed that their Sessional II Exam will be starting from 20/03/2023. Attendance is mandatory.

Dr. Sushama Telrandhe

HOD, ETC, GNIET

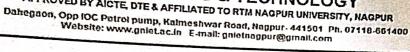
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- 2) Associate Dean
- 3) Faculty members



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DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING

Session 2022-23(EVEN)

SESSIONAL 2 EXAM

Time Table

Date: 15/03/2023

Day	Date	Time	VI	VIII
Mon	20/03/2023	10.00am-11.00am	Computer Communication Network	Microwave & Radar Engineering
Tue	21/03/2023	10.00am-11.00am	Internet Of Things	Computer Communication Network
Thu	23/03/2023	10.00am-11.00am	Wireless Sensor Network	Wireless Mobile Communication
Fri	24/03/2023	10.00am-11.00am	Control System Engineering (PEC-II)	Embedded System
Mon	27/03/2023	10.00am-11.00am	Open Elective-I	CMOS VLSI Design

1) It is compulsory for all students to solve paper in a proper format.

2) The question paper of each subject will be of descriptive pattern.

3) Maximum marks: 20, Time:1Hr.

4) Sessional Exam will be conducted in the given slot ,remaining classes will continue as per time table. Clear balance fees .dues if any

Prof. Minakshi Dhage Exam In-Charge

Dr. Sushma Telrandhe HOD, ETC



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Department of Electronics & Telecommunication

Sessional - II

Subject: WSN

Maximum Marks: - 20



Department of Electronics & Telecommunication Sessional - II

Year/Sem: III Yr/VI SEM Time: - 01 Hours

Maximum Marks: - 20

Year/Sem: III Yr/VI SEM Time: - 01 Hours Subject: WSN

INSTRUCTIONS TO CANDIDATE.

All Questions carry marks as indicated.

3) Illustrate your answer wherever necessary with the help of neat sketches.

4) Assume suitable data whenever necessary.

3) Illustrate your answer wherever necessary with the help of neat sketches.

 All Questions carry marks as indicated. INSTRUCTIONS TO CANDIDATE.

2) Solve FIVE questions.

5) Use of non programmable calculator is permitted.

4) Assume suitable data whenever necessary.

5) Use of non programmable calculator is permitted.

Explain Transport control protocol for Wireless Sensor Network? Explain Traditional Transport control protocol for Wireless Sensor Network. 7

Explain Transport control design issue Wireless Sensor

Explain existing Transport control protocol with example? 4

Explain performance of Transmission control protocol with S

Explain how TCP /Ip protocol work for WSN.

Explain routing challenges and design issue in WSN

Explain Transport control design issue Wireless Sensor Network.

Explain performance of Transmission control protocol with

S.

Explain routing challenges and design issue in WSN

Explain routing strategies in WSN.

∞

Explain how TCP /Ip protocol work for WSN.

9

Explain existing Transport control protocol with example?

Explain Transport control protocol for Wireless Sensor Network?

Explain Traditional Transport control protocol for Wireless

Sensor Network.

3

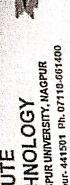
Network

Explain routing strategies in WSN.



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Department of Electronics & Telecommunication Dahegaon, Kalmeshwar Road, Nagpur-441 501.

Sessional - II

Year/Sem: II Yr/ IV SEM

Time: - 01 Hours

Subject: IoT

Maximum Marks: - 20

INSTRUCTIONS TO CANDIDATE.

- 1) All Questions carry marks as indicated.
- 2) Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
 - 4) Assume suitable data whenever necessary.
- 5) Use of non programmable calculator is permitted.

Introduce About IoT Architecture?

- Explain 5 Layer IoT Architecture? 7
- Explain Real life Model of IoT? with Example? ω.
- Explain Physical Design of IoT?

Explain Real life Model of IoT? with Example?

'n

Introduce About IoT Architecture?

Explain 5 Layer IoT Architecture?

Explain Functional Block Diagram of IoT?

Explain Physical Design of IoT?

4:

Explain 4 Layer IoT Architecture?

9

Explain Disadvantages of IoT?

7.

- Explain Functional Block Diagram of IoT? 5.
- Explain 4 Layer IoT Architecture? 9
- Explain Disadvantages of IoT?
- Explain real-world design constraints of IoT.

Explain real-world design constraints of IoT.

∞:

GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY Dahegaon, Kalmeshwar Road, Nagpur-441 501

Department of Electronics & Telecommunication Sessional - II

Year/Sem: II Yr/ IV SEM Time: - 01 Hours

Maximum Marks: - 20 Subject: IoT

INSTRUCTIONS TO CANDIDATE.

- 1) All Questions carry marks as indicated.
 - 2) Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
 - 5) Use of non programmable calculator is permitted. 4) Assume suitable data whenever necessary.



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Department: ETC

Room: B 203

Name of Lab: Advanced computer labortary

1. Object Oriented Programming & Data Structure (Sem : III)

2. Software Workshop (Sem : IV)

3. Digital Signal Processing (Sem : VI)

4. DSP Processor & Architecture (Sem: VII)

5. Computer Communication Network (Sem : VIII)

Sr. No	Equipment m/c/ Computers	Total Cost in Rs./-
1	Computer (No)Lenovo:- Intel Pentium Dual Core 2.7 Ghz, 1 GB DDR 2, RAM,320 GB hard disk, 18.5 tft monitor, multimedia keyboard optical mouse.	
2	DSP lab TMS 320C6713 DSK development card by VPL infotech, model: VPL Dsp 6713 with instruction manual, software, power supply, usb cable.	
3	DSP based embedded system lab based on ADSP 21xx evaluation kit plate form(Dsp trainer kit) by VPL infotech, model: Adsp 2181with power supply cable And Instruction manual.	7,82,310/-
4	DSP development board easy to operate expension by VPL infotech, model:DSP 50 with all accessories.	
5	DSP Single Signal license system.	
6	Panasonic PT-LX22EA LCD Projecter	

Name of Lab: 1. Object Oriented Programming & Data Structure

Sem: III

S No	Name of Experiment
1	Write a C++ program to implement the concept of class and object.
2	Write a C++ program to find the area of circle and rectangle by using default and parameterized constructer.
3	Write a C++ program using following inheritance path: Student -> Marks-> Result & to produce result of each student.
4	Write a C++ program, to implement operator overloading. Overload "+" operator so that two string can be concatenated.
5	Write a C++ program to implement a following sorting tech. to arrange elements in ascending order. 1) Bubble sort 2) Insertion sort

6	Write a C++ program to implement a stack in which push, pop and display can be performed.
7	Write a C++ program to implement a queue in which insertions, deletions and display can be performed
8	Write an interactive C++ program to create a singly linked list and perform following operation 1) Create 2) Insert 3) Delete
9	techniques. 1) Preorder 2) Inorder 3) Postorder
10	Write a C++ program to construct a binary search Tree and perform following Operation. 1) Insert 2) Delete 3) Print leaf n.
11	Write a C++ Program to implement quick sort
12	Write a C++ Program to implement "this" keyword

Name of Lab: 2. Software Workshop

Sem : IV List of Experiment

Sr. No. Name of the Experiment	
1 Introduction to MATLAB Environment 2 To study simple matrix and array manipulations using Matlab	
4	Calculus using MATLAB
5	To plot signals: discrete and continuous using MATLAB
6	Function programming and MATLAB
7	Signal Manipulation using MATLAB
8	Design and simulation of resistive circuit
9	Plotting of VI characteristics of diode0
10	Plotting of VI characteristics of BJT/FET
11	Plotting of VI characteristics of UJT/SCR
12	Design and simulation of half wave & full wave rectifier
13	Design and simulation of clipper and clamper circuits
	Simulation of frequency response of a transistorized RC coupledamplifier

ame of Lab: 3. Digital Signal Processing

Sem: VI

S.No	List of Experiment			
1	To study & generate different Name of the Experiment			
2	To study & generate different basic discrete time standard test signal in MATLAB. Write a program in MATLAB to evaluate second to the experiment.			
3.	Write a program in MATLAB to evaluate convolution of given sequence. To study & generate different operations that can be performed on discrete time signal in MATLAB			
4	To study and obtain the cross-correlation of two given sequences in MATLAB.			
5	To study and obtain the auto-correlation of given discrete sequence in MATLAB.			
6	Write a program for addition of two sine wave of same frequency.			
7	Write in MATLAB to find inverse z-transform of given Function. Compare the z-transform obtained in MATLAB with result obtained theoretically using partial fraction method. Also plot pole & zero of given signal x(n).			
8 (Write a program in MATLAB to find DFT of given signal using twiddle factor. Also plot its magnitude .			
9	Write a program in MATLAB to find DFT of given signal using FFT algorithm. Also plot its magnitude response.			
10	Write a program in MATLAB to compute circular convolution of given sequence.			
11	Write a program in MATLAB to design analog elliptic filter(Bessel), Order 2, cutoff 1 Hz.			
12	Study of Linear Convolution using DSP-TMS320C6713 Trainer kit.			
13	Study of Circular Convolution using DSP-TMS320C6713 Trainer kit.			
14	Study of Impulse Response using DSP-TMS320C6713 Trainer kit.			

Name of Lab: 4. Advanced Digital System Design

Sem : VII

Sr. No.	Name of the Experiment
1	Design of basic logic gates using VHDL.
2	Design of half and full Adder circuit using VHDL.
3	Design of Multiplexer and demultiplexer using VHDL.
4	Design of Priority encoder using VHDL.
5	Design of BCD to 7-segment encoder using VHDL.
6	Design of n-bit up-down counter using VHDL.
7	Design of n-bit shift register using VHDL.
8	Design of sequence detector using Mealy FSM.

9	Design of sequence detector using Moore FSM.	
10	Design of 4-bit ALU using VHDL.	

Name of Lab: 5. Computer Communication Network

Sem : VIII

No.	Name of the Experiment
1	To study Network simulator and get familiar with the NS2
2	To greate network topologies in NS2
3 .	To demonstrate data transmission using Ping protocol, tracert, IP configuration and hub.
4 .	To chidy the fundamental of socket programming.
5	To understand the IP address of the system, DHCP, network address translation.
6	To understand the domain name server.
7	To study protocol analyzer
8	To configure Router.
9	To study FTP, HTFT protocol.
10	To perform PC to PC communication using RS-232 port.
11	To understand wireless TCP and UDP protocols.
12	To demonstrate network security cryptography.



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Room : B 204

Department: ETC

Name of Lab: 6. Electronic Devices and Circuits

Name of Lab: 7. DSP Processor & Architecture

Sem: III

Sr. No.	Equipment m/c/ Computers	Total Cost in Rs./-
1	Computer (No)Lenovo & HCL:- Intel Pentium Dual Core 2.7 Ghz, 1 GB DDR 2, RAM,320 GB hard disk, 18.5 tft monitor, multimedia keyboard optical mouse.	
2	Electrical/ Electr. Ckt. Trainer (XPO-anadigi), Exp. Pan. Semiconductor & power semiconductor Rectifire, Filter, Zener, Transistor Amplifire, Transistor Class – A,B,C, Oscillator & Multivibrator C.R.O. 25 MHz, 2 chanal, 4 Trace Single O/p regulated D.C. Power supply Digital Multimeter Negative /Possitive feedback amplifier Function Generator 1 MHz Voltmeter DC (4 No.) Ammeter	5,35,970/-

Name of Lab: 6. Electronic Devices and Circuits

Sr. No.	Name of the Experiment	
1.	To Plot V-I .Characteristics of Si/Ge Diode.	
2.	To study Half Wave rectifier with and without Capacitor filter.	
3.	To study Full Wave rectifier with and without Capacitor filter.	
4.	To study Input-output characteristics of Common Base Configuration.	
5.	To study Input-output characteristics of Common Emitter Configuration.	
6.	To Determine the h-parameter of CE amplifiers.	
7.	To find the operating point of transistor.	
8.	To Study RC Oscillator (RC-Phase Shift and Wien Bridge Oscillator).	
9.	To Study LC Oscillators (Colpitt"s and Hartley Oscillator).	
10.	To study transistorized Astable Multivibrator.	
11.	To study transistor as an amplifier.	
12.	To study FET characteristics.	

Name of Lab: 7. DSP Processor & Architecture

Sem : VII

lo.	Name of the Experiment		
1.	To generate basic signals using TMS320C6713.		
2.	Write an ALP using instruction of TMS processors to add two numbers.		
3.	Write an ALP using instruction of TMS processors to subtract two numbers.		
4.	Write an ALP to multiply two numbers using instruction of TMS processors.		
5.	Write an ALP to division of two numbers using instruction of TMS processors.		
6.	Implementation of FFT using code Composer studio.		
7.	To implement Interpolation filter by Matlab.		
8.	To implement Decimation filter by Matlab.		
9.	To design FIR filter using MATLAB and find finite word length effect & cross verify using DSP processor.		
10	To design IIR filter using MATLAB and find finite word length effect & cross verify using DSP Processor.		



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Dahegaon, Opp IOC Petrol pump, Kalmeshwar Road, Nagpur-441501 Ph. 07118-661400
Website: www.gniet.ac.in E-mail: gniotnagpur@gmail.com



Room : B 204

Department: ETC

Name of Lab: 8. Communication Electronics (Sem: V)

9. Digital Communication (Sem: VI)

Sr. No	Equipment m/c/ Computers	
i	C.R.O 25 MHz 2 Channel 4 trace	Total Cost in Rs./-
2	Mixed signal Oscilloscope M . S O. 25 Mhz 16 channel	2,65,600/-
3	Demonstration Kit Amplitude Mod ,Freq .mod.,Pulse Width Mod.Pulse Position mod & Demod,Pulse Amplitude Mod. & Demod.	
4	Demonstration Kit FSK Mod ,ASK Mod ,PSK Mod ,Delta Mod TDM Mod,FDM Mod PCM Mod	

Name of Lab: 8. Communication Electronics

Sem: V

Sr. No.	Name of the Experiment	
1	To Perform & Verify Amplitude Modulation and Demodulation.	
· 2	To Perform & Verify Frequency Modulation and Demodulation.	
3	To Perform & Verify Pulse Amplitude Modulation.	
4	To Perform & Verify Pulse Width Modulation.	
5	To Perform & Verify Pulse Position Modulation.	
6	To Perform & Verify Time Division Multiplexing.	
7.	Simulation of Amplitude modulation techniques using MATLAB.	
- 8	Simulation of Frequency modulation techniques using MATLAB.	
9	To perform Pulse Code Modulation (PCM) using Simulation in MATLAB.	

Name of Lab: 9. Digital Communication

List of Experiment

Sem : VI

•	Name of the Experiment
	To study and verify the sampling theorem and reconstruction of sampled wave form.
	To analyze a ASK modulation system and interpret the modulated and demodulated waveforms.
	To analyze a FSK modulation system and interpret the modulated and demodulated waveforms.
	To analyze a PSK modulation system and interpret the modulated and demodulated waveforms.
	To study the Delta modulation process by comparing the present signal with the previous signal of the given modulating signal.
31	To perform generation of QPSK using MATLAB.
7	To perform Slow and Fast Frequency hopping: FH/MFSK using MATLAB.
8	To generate Hamming Code using MATLAB.
9.	To write and execute MATLAB code for Convolutional Encoding in Time Domain Approach.
10	Write and execute MATLAB code for generation of ASK.
11	Write and execute MATLAB code for generation of FSK.
12	A A A A A A A A A A A A A A A A A A A
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Room: B 209 A

Department: ETC

Name of Lab: 10. Television and Video Engineering Lab

Sem: VII

Sr.	Equipment m/c/ Computers	Total Cost in Rs./-
	Colour Tv Trainer (51 cm), Colour Pattern	1,25,000/-
	Generator, Colour Pattern Generator by signet	en sed attilized

List of Experiments

Name of the Experiment

To understand working of various stages of TV receiver

To analyse and verify SMPS Panel

To analyse and verify antenna cum tuner panel

To analyse & Verify I.F amplifier cum Detector panel

To study and observe colour composite video signal at the output of VIF Section

To analyse & Verify sync. Separator, Horizontal Oscillator & EHT

To analyse & Verify Vertical Oscillator and Vertical outputs

To understand the chroma section of TV receiver and observe the chroma outputs

To analyse & verify different faults in color TV trainer kit

To analyse and observe the patterns on TV Receiver

Total Investment in Laboratories:

Total Tire	THE PROPERTY OF THE PARTY OF THE PROPERTY OF THE PARTY OF	Cost in Rs/-
Sr. No.	Name of Laboratory	
1	Digital Signal Processing	7,82,310/-
2.	Electronic Devices and Circuits	5,35,970/-
We have	Communication Electronics	2,65,600/-
3.	Television and Video Engineering Lab	1,25,000/-
4.	Total =	17,08,880/-

To,
Dear Parent/Guardian
Shri. Tivan Lamse
Clo S.B. Chardrerry
Laxadi xd.
Laypum

Subject: - Regarding not attending regular classes.

This is to inform you that we have noticed an irregularity of your ward. As the classes already started, we have been observing that your ward is not attending his\her classes on regular basis. As per RTMNU regulation 75% attendance is compulsory. If his/her attendance is less than 75% then he/she will be unable to appear for RTMNU exam.

If she/he remain absent then strict action will be taken.

Thank you

Prof. Deepak Deshpande Class Incharge Dr. Sushama Telrandhe HOD, ETC, GNIET

Date:- 2/08/2023

To,
Dear Parent/Guardian

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Maneweda Magrux

Subject: - Regarding not attending regular classes.

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Thank you

Prof. Deepak Deshpande
Class Incharge

Dr. Sushama Telrandhe HOD, ETC, GNIET

To,		
Dear Pare	ent/Guardian	Rajput
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	U'	

Subject: - Regarding not attending regular classes.

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Thank you

Prof. Soniya Milmile Class Incharge Dr. Sushama Telrandhe HOD, ETC, GNIET

Date: - 31/07/2023

To,
Dear Parent/Guardian
Shar TKBAL SHETKH
Bezan Rogh, Saday
Nagpur

Subject: - Regarding not attending regular classes.

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Thank you

Prof. Soniya Milmile Class Incharge Dr. Sushama Telrandhe HOD, ETC, GNIET

Date: - 31/07/2023

To,

Dear Parent/Guardian

To DPILP Mendhe

Near pany Tank

Canear hospital

Tukdoj Sq. Magpur

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Thank you

Prof. Deepak Deshpande Class Incharge Dr. Sushama Telrandhe HOD, ETC, GNIET

To,

Dear Parent/Guardian

Show projeash BhajGrawere

Plate No. 27, Showwas.

Taitala Nogher

Subject: - Regarding not attending regular classes.

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Thank you

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Question Bank

2022-23

Branch	Electronics &
	Telecommunication Engineering
Semester	VII th Sem
Subject	Wireless Mobile Communication
Subject Code	BEETC701PE-T



Website: www.gnlet.ac.in E-mail: gnietnagour@gnai.com



<u>Department of Electronics & Telecommunication</u>

	UNIT-I		Blooms Level	COn
	What is frequency reuse concept? Explain the importance while designing cellular mobile system	5	AP	
	Explain cell splitting and cell sectoring used in cellular system. Explain the terms Umbrella cell approach and cell dragging related to	5	AN	
ST AT	hand off. Draw a view of cellular telecommunication system and explain in brief.	<u> 5</u>	UN	
4.		5	AN	
5	Define following terms used in Trunking theory) Holding time ii)Traffic Intensity iii)Grade of service iv)Request rate	5	UN	
6	What are the types of cellular interference	5	AN	CO-1
7	Explain second generation 2G network in detail.		UN	
8	Explain Public Switched Telephone Network(PSTN) What are the differences between wired and wireless communication	5	AP	
9	system in detail.	5	AN	
0	Explain how hand off concept work in the network.	5	AP	
	- UNIT-II	Marks	Blooms Level	COn
1	What do you understand by multi path fading.	5	AP	CO-2
1 2	Explain the difference between short term fading and long term fading.	5	AN	002
1 3	The state of the s	5	UN	
1 4	a a manig.	5	AN	
1 5		5	UN	
6		5	AN	
7	7	5	UN	
8	Explain the terms i)level crossing rule	5	AP	



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\$0 \$ \$	21 - 2008 Certified Institutions		AN	17 - 4 172 - 4
1 9	Write short notes on Coherence bandwidth	5	AP	
2	Explain the terms Delay spread	5	Blooms	Bloom
0	UNIT-III	Marks	Level	s Level
2	to Lef Equalization ?	5	AP	50°
2	Explain Fundamental of Equalization ? Define Space Polarization with application	5	AN	
2 /3	Explain frequency diversity technique with example	5	UN	
2,4	Explain time diversity technique with example	5	AN	
2 5	Explain space diversity technique with example	5	UN	
2 6	Explain polarization diversity with application	5	AN	CO-3
2 7	What is the difference between frequency and time diversity	5	UN	
2 8	Explain fundamentals of channel coding	5	AP	
2 9	Explain how channel coding works	5	AN	
0. 3	Write short note on equalization	5	. AP	



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Question Bank

2022-23

Branch	Electronics &
	Telecommunication Engineering
THE STATE OF THE S	V th Sem
Semester	v th sem
Subject	Sensor and System
Subject Code	
•	BEETC505PE







1 9.	Explain coolant and temperature sensor with example.	5	Analyzing	
2 0	Explain oxygen and engine speed sensor used in automobile.	5	Analyzing	
	UNIT-III	Marks	Blooms Level	Bloom s Level
2	What is Rotary transformation give one example	5	Analyzing	720
2 2	Explain torque transducer with application	5	Analyzing	
2	Explain importance of passive seed sensors	5	Analyzing	CO-3
2 4	Explain smart position sensor with application	5	Analyzing	
2 5	Explain non contact hall effect rotary position sensor.	5	Analyzing	
2 6	Explain with example current and voltage sensor	5	Analyzing	
7	What is hot metal detector give one example	5	Analyzing	
2 8	Explain proximity and displacement sensor	5	Analyzing	
2 9	Explain how sensor interface with microcontroller	5	Analyzing	
3	Write short note on Calibration	5	Analyzing	



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Department of Electronics & Telecommunication

	gineering UNIT-I	Marl	ks Blooms Level	s CO	n
N. San	What is Sensor? Explain the importance of sensor.	5	Understan		
	Define transducer and explain its working.	5	Understa d		
+	Explain classification of transducer with one example.	5	Understan	d	
	Explain characteristics and parameter s of transducer.	5	Understand	d	
200	Explain design procedure to choose sensor.	5	Understand	d	
	Explain types of sensor with one example.	5	Understand	d co-	1
	Explain difference between transducer and sensor	5	Understand	<u> </u>	
3	Explain inductive, capacitive and resistive sensor.	5	Understand		
)	Explain environmental parameters and of any two sensors.	5	Understand		
1.	Explain environmental parameters and of any two transducer.	5	Understand		
	UNIT-II	Marks	Blooms Level	COn	Mary Control of
L L	Explain sensors used in automobile industries.	5	Analyzing	CO-4	
T.	Explain the working of camshaft position sensor with example.	5	Analyzing		
PACE AND ADDRESS OF THE PACE A	Explain throttle position sensor with example	5	Analyzing		
No. of Control	Explain types of sensor .Give example of any one .	5	Analyzing		
年入り	Explain vehicle speed sensor in detail	5	Analyzing		
	Explain voltage sensor in detail with one example	5	Analyzing		
	Explain fuel temperature and manifold absolute pressure sensor		Analyzing		
3	Explain types of sensor and their interfacing with microcontroller.		Analyzing		



Guru Nanak Institute of Engineering and Technology, Nagpur Department Of Electronics & Telecommunication Engineering INTERNAL ASSESSMENT Marks III Sem

Session 2022-23

Subject :- DSP

AASTHA RAVINDRAKUMAR RAULKAR ACHAL GANGADHAR KALAMBE DHAMMADIP NILESH GONDANE HARSHAL GULABRAO NAGPURE KARAN DEEPAK PARATE KHUSHI DHARMARAJ KELWADE LAXMI AMAR MESHRAM MEGHA YOGIRAJ SONTAKKE NIKHIL NARESHRAO BALANSE POOJA CHANDRAKANT SHRIWAS PRACHI MOHAN BAHAD PRATIKSHA RAJU UNPANE RITIK KRISHNA LOKHANDE ROSHAN DHARAMLAL TURKAR SANKET PRAMOD MASKE		[6]		[6]	(10)	[30]
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HITESH SHIWAII BHOYAR						
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46	VISHAL PRADIP MADANKAAR		The second second second	and the design of the first of the second of
47	YASH SHIVAJ WANKHADE			
48	Shraddha Rajesh Pashine			



Prof. Soniya Milmile Class Incharge Dr. Sushama Telrand HOD, ETC

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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY DAHEGAON, KALMESHWAR ROAD, NAGPUR-441501

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

Session: 2022-2023(EVEN)

Sessional-II (Attendance Sheet)

VI SEMESTER (CBCS) (Regular)

Date: 20/03/2023

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2	AKASH KULDEEP SOMKUWAR					
3	APEKSHA SUNIL SAKHARE					
4	AMEYA DILIP MENDHE	7.				13 7 15 15
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16	GAURAV SANJAY BHAJNI		.,		,	
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Prof. Minakshi Dhage Exam In-charge Dr. Sushama Telrandhe HOD, ETC



GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY Dahegaon, Kalmeshwar road, Nagpur

NAAC Accredited Session 2022-23 (ODD)

ACADEMIC CALENDAR

Sr.	Months	(For B. Tech. – III Sem)	
$\frac{N_0}{1}$	Tonths	Activiting Scarring	Date
1 2 3 4 5 6 7 8 9		Faculty Subject Choice	24th June2022
3	June-	Load Calculation, Departmental Planning and Faculty Requirement	27th June2022
4	2022	Verification of Classrooms and Laboratories	28th June2022
5		Display of Project Guide With Specialization	29th June2022
6		Stationary/ Equipment Requirement	30th June 2022
7		Display of Time- Table (3 rd Sem)	1st September 2022
8		Preparation of Student List and Attendance Register (3rd Sem)	2 nd September 2022
9		Preparation of Lesson Plan/ Teaching Plan	5th September 2022
10		Commencement of Classes (3 rd Sem)	12th September 2022
11	Septem	Student Registration to the Department (3rd Sem)	12th to 16th September 2022
12	ber-	Teacher Guardian Allotment (3rd Scm)	19th September 2022
13	2022	Industrial Visit/ III Cell Activity/ TG Meeting/ Forum Installation	19th to 23rd September 2022
14		Preparation of Course File, Lab Manual etc.	23 rd September 2022
15		Display of Assignment-I & II, Submission: 30-09-22	26th September 2022
16		Attendance Review/ Provisional Student Detention List	28th September 2022
17		Monthly Audit/ Student Feedback ·	29th September 2022
18		Navratri Garba Celebration-2022	3rd October 2022
19		Display of Question Bank (3 ^{ru} Sem)	7th October 2022
20		Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting	10th to 14th October 2022
21	October	Sessional-I Exam (3 rd Sem)	17th to 21st October 2022
	-2022	Diwali Vacation	24th to 28th October 2022
22		Display of Assignment-III & IV, Submission: 07-11-22	31st October 2022
23		Attendance Review/ Final Student Detention List	31st October 2022
24		Monthly Audit/ Student Feedback	31st October 2022
5		Display of Question Bank (3rd Sem)	8 th November 2022
6	Novem	Industrial Visit/ III Cell Activity/ T&P Activity/ TG Meeting	14th to 18th November 2022
7	ber-	Sessional-II Exam (3 rd Sem)	21st to 25th November 2022
8	2022	Display of Assignment-V & VI, Submission: 05-12-22	28th November 2022
9	2022	Attendance Review/ Final Student Detention List	29th November 2022
0		Monthly Audit/ Student Feedback	30th November 2022
1	Decem	Internal Practical (3rd Sem)	5th to 8th December 2022
2	ber- 2022	PUT Exam (3 rd Sem)	12 th to 16 th December 2022
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Note: - Above mentioned date are subject to change due to unavailable circumstances.

Colomeia Prof. NehaChourasia

Prof. RajendraBhombe

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Dr. HemantHajare

Associate Dean (Academics)

Vice- Principal

Principal



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Question Bank

2022-23

Branch	Computer Science and
	Engineering
Semester	3RD
Subject	Computer Architecture and Digital Systems
Subject Code	BECSE304T



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Question Bank

Subject: Computer Architecture and Digital Systems

	UNIT-I	Marks	Blooms Level	COn
1	Classify logic gates and explain function of each in brief.	5	An	
2	Using Boolean Algebra show that: (any one) I. BC+ AC' +AB + BCD =BC + AC' II. AB'+ ABD + ABD' + A'C'D' +A'BC' = A+ BC'+ C'D'	5	Ap	
	Find Reduced Boolean expression using K-map (any one)			
3	1. F(A,B,C,D)= π M(5, 6, 7, 12, 13) + d (4, 9, 14, 15)	5	Ap	
	$2.F(A,B,C,D,E) = \sum_{i=1}^{n} m(0,2,3,8,9,12,13,15)$			CO-1
4	Using K map reduce to a minimum SOP form Y= A'B'C'D' + A'B'C'D + ABCD + ABCD'+A'BC'+AB'C	5	Ap	CO-1
5	Verify De Morgans Theorem.	5	An	
6	Discuss on Multiplexers.	5	Un/An	
7	Discuss on De-multiplexers.	5	Un/An	
8	Discuss on Encoders.	5	Un/An	
9	Discuss on Decoders.	5	Un/An	
10	Design 3:8 decoder circuits.	5	Ap	
	UNIT-II	Marks	Blooms Level	COn
11	Explain basic structure and its function in computer systems.	5	Un	
12	Explain Von-Neumans architecture of computer systems.	5	An	
13	Discuss on basic operational concepts of Computer systems.	5	Un	
14	Classify Bus structure explains each in brief.	5	An	
15	Classify various addressing modes and explain each with example.	5	An	CO-2
16	Explain how subroutine is working in computer system.	5	An	
17	Discuss on parameter passing.	5	Un	
18	Classify different instruction format and explain each in brief.	5	An	
19	Write short note on single bus structure.	5	Un	
20	Write short note on multiple bus structure.	5	Un	
	UNIT-III	Marks	Blooms Level	COn



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21	List various types of bus architecture and analyse function of any three buses.	5	An	
22	Explain with example, the steps of execution of complete Instruction.	5	An	
23	Explain sequencing of control signal for, fetch operation.	5	An	
24	Explain sequencing of control signal for, data read operation.	5	An	
25	Explain sequencing of control signal for, execution operation	_	An	
25	including arithmetic operation and saving the result in memory.	5		CO 2
26	Demonstrate hardwired control system.	5	Ap	CO-3
27	Demonstrate microprogrammed control system.	5	Ap	
20	Analyse function of each bit of micro-operation field F1, F2, and	_	An	
28	F3 of microinstruction format.	5		
20	Analyse function of each bit of Condition field CD of	_	An	
29	microinstruction format.	5		
20	Analyse function of each bit of Branching fieldBR of	5	An	
30	microinstruction format.	3		
	LINIT IV	Marks	Blooms	COn
	UNIT-IV		Level	
31	Illustrate with example Decimal to Binary and Binary to Decimal	5	An	
31	conversion.	3		
32	Illustrate with example Binary to Octal and Octal to Binary	5	An	
32	conversion.			
33	Illustrate with example Hexa-Decimal to Binary and Binary to	5	An	
33	Hexa-Decimal conversion.	<i>-</i>		
	Evaluate difference between two binary numbers using two's		Ev	
34	complement method 1) Case-1 Minuend > Subtrahend 2) Case-2	5		CO-4
	Minuend < Subtrahend			
35	Determine sign of final result of addition and subtraction of	5	Ev	
	signed magnitude using 8 different conditions.	3		
36	Explain addition algorithm and subtraction algorithm, also	5	An / Cr	
	implement the same using hardware.			
37	<u> </u>	5	Ev/Cr	
38	Design a full adder using XOR and AOI logic.	5	Cr	
	UNIT-V	Marks	Blooms	COn
		_	Level	
39	Discuss on various technologies used in memory design	5	Un	
40	Explain different types of memories used in computer	5	Ap	
	architecture.			
41	Distinguish between Memory space and Address space	5	An	CO-5
42	Explain Page Table	5	Un	
45	Distinguish between Memory mapped I/O and I/O mapped I/O	5	An	
46	Illustrate interrupts and interrupt handling mechanism	5	An	
47	Classify interrupts	5	An	



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48	Distinguish between synchronous and asynchronous data transfer.	5	An
49	Discuss on DMA data transfer	5	Un
50	Design higher order memory.	5	Cr

Start your Questions with following list of BT Verbs, to map the Blooms Level.

Remembering	Understanding	Applying	Analyzing	Evaluating	Creating
Define	Approximate	Apply	Analyze	Compare	Assemble
Describe	Characterize	Assign	Characterize	Conclude	Combine
<mark>Draw</mark>	Clarify	Construct	Compare	Criticize	Compile
Enumerate	Classify	Demonstrate	Confirm	Defend	Compose
Identify	Compare	Derive	Correlate	Determine	Construct
List	Compute	Determine	Detect	Discriminate	Create
Match	Convert	Draw	Diagnose	Estimate	Design
State	Describe	Employ	Diagram	Evaluate	Develop
Study	Differentiate	Examine	Differentiate	Calculate	Formulate
Write	Discuss	Express	Discriminate	Compute	Generate
	Distinguish	Modify	Distinguish	Explain	Incorporate
	Elaborate	Plot	Examine	Grade	Integrate
	Estimate	Predict	Explain	Justify	Interface
	Explain	Prepare	Figure out	Measure	Model
	Express	Produce	Illustrate	Predict	Modify
	Give	Show	Infer	Prescribe	Organize
	Predict	Depict	Investigate	Rate	Prepare
	Review	Portray	Maximize	Summarize	perform
	Rewrite	Simulate	Minimize	Test	Produce
	Subtract	Sketch	Optimize	Validate	Program
	Summarize	Solve	Point out	Verify	Rearrange
	Translate	Use	transform	•	Reconstruct



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Question Bank

2023-24

Branch	Electrical Engineering
Semester	5 th Semester
Subject	Microprocessors and Microcontrollers
Subject Code)



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Subject: Microprocessor and Microcontroller

	UNIT-I	Marks	Blooms Level	COn
1	Discuss briefly on Microprocessor based system.	5	Un	
2	Explain Internal architecture of 8 bit Microprocessor 8085.	5	Un	
3	Examine function of each bit of flag register.	5	Ap	
	Define Addressing mode and classify addressing mode according	_	An	
4	to their function.	5		
5	Classify instruction set of 8085 and explain function of any one	5	An	CO-1
	instruction under each instruction group.			CO 1
6	Compare function of between CALL and JMP instruction.	5	An	
7	Explain function of PCHL, SPHL, XTHL instructions.	5	An	
8	PCHL is known as one word JMP instruction, Justify.	5	Ev	
9	RSTn is known as one word CALL instruction, Justify .	5	Ev	
10	Explain the function of DAA instruction.	5	An	
	LINIT II	Marks	Blooms	COn
	UNIT-II		Level	
11	Explain Instruction cycle using timing diagram in 8085.	5	An	
12	Compare the function of PUSH and POP instruction in stack operation.	5	An	
13	Explain interrupt structure of 8085.	5	An	
14	Differentiate the role of CALL and RET instruction in execution of SUROUTINE.	5	An	
15	Summarize how ISR is executed after arrival of an Interrupt.	5	Ev	
16	Distinguish between linear and absolute decoding technique.	5	An	CO-2
17	Design a memory system for 8085 using linear decoding technique with 2KB RAM and 2KB ROM.	5	Cr	
18	Write an ALP to perform addition of 10 bytes in sequence.	5	An	
19	Write an ALP to perform two Multi-Byte hexadecimal numbers.	5	An	
20	Write an ALP to find square root of the number (81) residing at 1000H memory location and save the result at 1001H.	5	An	
	•	Marks	Blooms	CO-n
	UNIT-III	Iviaiks	Level	CO-II
	Portray the architecture of 8255 PPI and describe each functional		Ap	
21	block in brief.	5	Дþ	
22	Write control word format of 8255 PPI, and examine function of each bit.	5	An	
23	List different mode of 8255 PPI, and Explain simple IO mode.	5	An	
24	List different mode of 8255 PPI, and Explain BSR mode.	5	An	
25	Design an interface between 8255 and 8085 Microprocessors.	5	Cr	



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26	Design an interface between 8255, LED and 8085 Microprocessors.	5	Cr	
27	Design an interface between 8255, keyboard and 8085 Microprocessors.	5	Cr	
28	Design an interface between 8255, 7 segment display unit and 8085 Microprocessors.	5	Cr	CO-3
29	Portray the architecture of 8253/54 PIT and describe each functional block in brief.	5	Ap	
30	Write control word format of 8253/54 PIT, and examine function of each bit.	5	An	
31	Analyze various modes of 8253/54.	5	An	
32	Design an interface between 8253/54 and 8085 Microprocessors.	5	Cr	
33	Distinguish between 8085 and 8086 Microprocessors based on their features.	5	An	
	UNIT-IV	Marks	Blooms Level	CO-n
34	Distinguish between 8085 Microprocessors and 8051 Microcontrollers based on their features.	5	An	
35	Portray the architecture of 8051 Microcontroller and describe each functional block in brief.	5	Ap	
36	Explain programming model (Register organization) of 8051.	5	An	
37	Write PSW format of 8051, and examine function of each bit.	5	An	
38	List addressing modes of 8051. Explain each with one example.	5	An	
39	Write an ALP to display the message "GNIET, Nagpur" continuously on display interfaced on port 2 of 8051.	5	Ap	CO-4
40	Explain stack operation in 8051.	5	An	
41	Write Interrupt SFRs format of 8051, and examine function of each bit.	5	An	
42	Write Timer SFRs format of 8051, and examine function of each bit.	5	An	
43	Write Serial port SFRs format of 8051, and examine function of each bit.	5	An	
	UNIT-V	Marks	Blooms Level	CO-n
44	Design an interface between 8251 Microcontroller, 4KB ROM and 2KB RAM memory.	5	Cr	
45	Design an interface between 8251 Microcontroller, 7-segment LED display.	5	Cr	
46	Design an interface between 8251 Microcontroller, LCD display.	5	Cr	CO-5
47	Design an interface between 8251 Microcontroller, and 8x8 keyboard matrix.	5	Cr	
48	Design an interface between 8251 Microcontroller, Stepper motor	5	Cr	
49	Design an interface between 8251 Microcontroller, DC motor	5	Cr	



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50 Design an interface between 8251	Microcontroller, ADC 0809	5	Cr
51 Design an interface between 8251	Microcontroller, DAC 0800	5	Cr
52 Discuss briefly on features and app	olications of ARDUINO.	5	Un

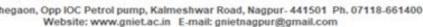
Start your Questions with following list of BT Verbs, to map the Blooms Level.

Remembering	Understanding	Applying	Analyzing	Evaluating	Creating
Define	Approximate	Apply	Analyze	Compare	Assemble
Describe	Characterize	Assign	Characterize	Conclude	Combine
Draw	Clarify	Construct	Compare	Criticize	Compile
Enumerate	Classify	Demonstrate	Confirm	Defend	Compose
Identify	Compare	Derive	Correlate	Determine	Construct
List	Compute	Determine	Detect	Discriminate	Create
Match	Convert	Draw	Diagnose	Estimate	Design
State	Describe	Employ	Diagram	Evaluate	Develop
Study	Differentiate	Examine	Differentiate	Calculate	Formulate
Write	Discuss	Express	Discriminate	Compute	Generate
	Distinguish	Modify	Distinguish	Explain	Incorporate
	Elaborate	Plot	Examine	Grade	Integrate
	Estimate	Predict	<mark>Explain</mark>	Justify	Interface
	<mark>Explain</mark>	Prepare	Figure out	Measure	Model
	Express	Produce	Illustrate	Predict	Modify
	Give	Show	Infer	Prescribe	Organize
	Predict	Depict	Investigate	Rate	Prepare
	Review	Portray	Maximize	Summarize	perform
	Rewrite	Simulate	Minimize	Test	Produce
	Subtract	Sketch	Optimize	Validate	Program
	Summarize	Solve	Point out	Verify	Rearrange
	Translate	Use	transform		Reconstruct



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Department of computer science and engineering

Subject: COMPUTER NETWORKS

UNIT I

PART A

- 1. What are the two types of line configuration?
- 2. What do you mean by error control?
- 3. Define flow control?
- 4. What is redundancy?
- 5. Write short notes on error correction?
- 6. Mention the types of error correcting methods.
- 7. What are the steps followed in checksum generator?
- 8. Define checksum.
- 9. Write short notes on CRC checker.
- 10. Write short notes on LRC.
- 11. List out the available detection methods.
- 12. Mention the types of errors and define the terms?
- 13. Distinguish between peer-to-peer relationship and a primary-secondary relationship.
- 14. What are the ways to address the framing problem?
- 15. What are the responsibilities of data link layer?
- 16. What are the functions of Application layer?
- 17. Define Bit stuffing.



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- 18. What are the features provided by layering?
- 19. What are header and trailers and how do they get added and removed?
- 20. Group the OSI layers by function?
- 21. What are the two types of parity checks? Define it.
- 22. Draw a neat diagram of Internet protocol graph.

PART B

- 1. Explain in detail the error detection and error corrections. (16)
- 2. With a neat diagram explain in detail about the Network architecture. (16)
- 3. Discuss in detail about HDLC. (16)
- 4. (i) What is the difference between Internet architecture and OSI architecture? (8)
- (ii) Explain about coaxial cable and fiber optics media for communication. (8)
- 5. Discuss about the links operated on the physical media in detail. (16) 6. Explain the different approaches of framing in detail. (16)
- 7. Discuss about different types of Issues in data link layer. (16)
- 8. (i)Write about channel access on links. (8)
- (ii)Explain about FDMA and TDMA in detail. (8)
- 9. (i) A block of 32 bits has to be transmitted. Discuss how the thirty two bit block is transmitted to the receiver using Longitudinal Redundancy Check. (4)
- (ii) Consider a 32 bit block of data 11100111 11011101 00111001 10101001 that has to be transmitted. If Longitudinal Redundancy Check is used what is the transmitted bit stream? (6)
- (iii)In the Hamming code, for a data unit of m bits how do you compute the number of redundant bits 'r' needed? (6)



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UNIT II

PART A

- 1. What is CSMA?
- 2. Explain CSMA/CD
- 3. Mention some of the physical properties of Ethernet.
- 4. What is the role of VCI?
- 5. What do you mean by error control?
- 6. What are the functions of bridges?
- 7. What is the size of Ethernet address?
- 8. What is the advantage of FDDI over a basic token ring?
- 9. List any two functions which a bridge cannot perform?
- 10. Mention the function of hub.
- 11. Mention different random access techniques?
- 12. List the two types of data frames in FDDI
- 13. What is the purpose of the NAV?
- 14. Name the four types of S frames.
- 15. What is the access method used by wireless LANs?

PART B

- 1. Explain the CSMA/CD algorthims of Ethernet. (8)
- 2. Explain in details about the access method and frame format used in Ethernet and token ring. (16)
- 3. Name the four basic network topologies and explain them giving all the relevant features (16)
- 4. (i) Explain the working of carrier sense multiple access protocol. (3)
- (ii) How does a Token Ring LAN operates? Discuss. (6)



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- (iii) List and briefly discuss the two different basic transmission technologies that can be used to set up wireless LAN's. (7)
- 5. Explain the frame format, operation and ring maintenance feature of IEEE 802.5 MAC protocol. (16)
- 6. Briefly define key requirements for wireless LANs. (16)
- 7. Describe the FDDI frame format and explain. (16)
- 8. Discuss the MAC layer functions of IEEE 802.11. (8)
- 9. Explain in details the types of bridges. (8)

UNIT III

PART - A

- 1. List the difference between circuit switching and Packet switching.
- 2. What are the different kinds of Multicast Routing?
- 3. Discuss the class field in IP address.
- 4. What is meant by circuit switching?
- 5. What is multicasting?
- 6. What is a hostid and netid?
- 7. How does a netid differ from a network address?
- 8. What is the purpose of subnetting?
- 9. Define Masking.
- 10. What is the difference between boundary level masking and non-boundary level masking.
- 11. What is the function of router?
- 12. How does a router differ from a bridge?
- 13. Find the class of each addresses.



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- 14. Why is adaptive routing superior to non adaptive routing?
- 15. What are the three main elements of distance vector algorithms.
- 16. What is address resolution?
- 17. What are the benefits of subnetting a network?
- 18. What are the metrics used by routing protocols?

PART B

- 1. Explain shortest path algorithm with a suitable illustration. (16)
- 2. Explain the distance vector routing algorithm.(16)
- 3. Mention the limitations of distance vector routing algorithm.(8)
- 4. Explain the building and distribution of link state packets in link state routing algorithm.(8)
- 5. Mention the limitations of link state routing algorithm.(8)
- 6. Explain link state routing and discuss its advantages over distance vector routing.(8)
- 7. Explain in details packet switching.(16)
- 8. Explain in details Datagram approach.(16)
- 9. Explain in details IP addressing methods.(16)
- 11. i. In classful addressing how is an IP address in class A, Class B and Class C divided? (4)
- ii. Given the address 23.56.7.91 and the default class A mask, find the beginning address (network address). (6)
- iii. Given the address 201.180.56.5 and the default class C mask, find the beginning

address (network address). (6)



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UNIT IV

PART A

- 1. What are the services provided by transport layer protocol?
- 2. What is the difference between network service and transport service?
- 3. List some of the Quality of service parameters of transport layer
- 4. What are the functions of transport layers?
- 5. What is transport entity?
- 6. What is segmentation?
- 7. What is the purpose of sequence control?
- 8. How transport layer performs Duplication control?
- 9. What are the service primitives in simple transport protocol?
- 10. What is the methods to improve Qos?
- 11. What is traffic shaping? Name two methods of shape traffic?
- 12. What is the use of sequence numbers?

PART B

- 1. Explain a congestion control algorithm. (16)
- 2. Explain the duties of Transport layer. (16)
- 3. Explain the TCP transmission policy, Congestion control. (8)
- 5. Explain the TCP header and working of the TCP protocol. (16)
- 6. Explain the various fields of TCP header with the help of a neat diagram.(16)
- 7. Explain the various steps that are followed in releasing a TCP connection. (16)
- 8. Explain the three way handshake protocol to establish the transport level connection. (8)
- 9. Discuss about congestion control in frame relay. (16)
- 10. Discuss the various issues of transport layer in details. (16)



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UNIT V

PART A

- 1. Discuss the three main divisions of the DNS.
- 2. What role does the DNS resolver play in the DNS system?
- 3. How does a DNS Resolver bootstrap the domain name lookup process?
- 4. Define SMTP.
- 5. Define the term domain.
- 6. What are the two parts of addressing system in SMTP?
- 7. Discuss MIME.
- 8. What are the services provided by user agent?
- 9. What are the four properties of HTTP?
- 10. What are the four groups of HTTP header?
- 11. What are categories of web documents?
- 12. What are basic functions of email system?
- 13. What is WWW?
- 14. What is the web browser?
- 15. What is a post office protocol?
- 16. Compare the HTTP and FTP.
- 17. What is mailing list?
- 18. What are the two main categories of DNS messages?
- 19. Define cryptography.
- 20. What are the two categories of cryptography methods? What is the main

difference

between the categories?



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21. What are the advantages of public key encryption/decryption?

PART B:

- 1. Explain DNS with reference to its components and working. (16)
- 2. Explain the message transfer using simple mail transfer protocol. (8)
- 3. Explain the final delivery of email to the end user using pop3. (8)
- 4. Write short notes on email services of the application layer. (8)
- 5. Explain in details WWW. (8)
- 6. Explain the architecture and services of e-mailing system. (16)
- 7. What are the two categories of encryption/decryption methods? What is the main
- difference between the categories. (8)
- 8. With a relevant example discuss how the domain space is divided. (6)
- ii. Distinguish between a fully qualified domain name and a partially qualified domain name. Give relevant example. (6)
- iii. List the various risks faced by messages that are transmitted over the internet.(4)
- 9. i. Discuss how simple mail transfer protocol (SMTP) works? Can multimedia messages be transmitted using SMTP? Discuss. (10)
- ii Is common gateway interface a languages. Discuss. (6)



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FINGINEERING & TECHNOLOGY







Question Bank

2023-24

Branch	Computer Science & Engineering
Semester	5th Semester
Subject	Effective Technical
	Communication
Subject Code	
	BTECH_CSE-506T

Department of Computer Science & Engineering

	Department of Computer Science & Engineering					
	UNIT- I, II, III & IV	Marks	Blooms	COn		
			Level			
1	Correct the following sentences.	5		CO-1		



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	i) Measles are an infections disease.ii) Are either of the twins alive?		
	ii) Are either of the twins alive?iii) Much of the countryside were flooded.		
	iv) We lodged a FIR against the thief.		
	v) Are you good in tennis?		
	,		
	Do as directed]
	i) The tsunami wave killed hundreds of people. (change the voice)		
	ii) Who opened the door? (change the voice)		
2	iii) I said to him, "I have been reading the novel since 2 pm". (change	5	
	the narration)		
	iv) Pinki regretted with sorrow that she had failed again. (change the		
	narration)		
	v) Shall we have fun? (change into assertive sentence)		-
	Rewrite as directed of the following.		
	i) Ram Said, "I am tired now" (change the narration)		
	ii) Pradeep does the duty very well. (Change the voice)		
3	iii) The Aeroplan flies faster than birds. (change in to Negative	5	
	sentence).		
	iv) Rahul is an intelligent student. (Add a question tag)		
	v) He earned his Ph. D. at the age of 77. (Make it a complex sentence).		
	Do as directed		-
	i) Make new words with given prefixes and write their meanings		
	i) Sub ii) Inter		
	ii) Give one word for the following.		
	i) A book giving instructions or information		
4	ii) One who is neither intelligent nor dull.	5	
	iii) Give two examples of words formed out of Clipping.		
	iv) Explain the Jargon - Spam.		
	v) Make new words with given suffix and write their meaning		
	i)-er ii) ian		
	Do as directed		
	i) Give one word for "one who copies from other writers".ii) Choose the		
_	word which can be substituted for "a thing which is no longer in use"	_	
5	iii) Find similar relationship from the options given - Pulse: Heart	5	
	a)pitch: music b) nerve: brain c) tick: clock d) motor: engine e) blood		
	: arteriesiv) Make a new word with prefix 'para' and explain its		
	meaning.v) Explain the term 'alma mater'.		
	Read the passage and answer the questions that follow:		
6	You are living in an age when there are great opportunities for women	5	
	in socia - work, public life and administration. Society requires women		
	of disciplined minds & restrained manners. Whatever line of work you		



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	undertake, you should bring to it an honest, disciplined mind. You will then succeed and have the joy of your work. Actually in our country, education, so for as girls education is concerned, is not widespread enough. So every institution which contribute to the education of girls is worthy of recognition and encouragement. But I am anxious that the kind of education that is imparted must not only be broad but should also be deep. We are lacking in depth, we may become learned and skilled but it we do not have some kind of purpose in our life, our lives themselves become blind, blundering and bitter. The Gita says, 'vyavasayatmikabuddhirekeha'. For a truly cultured mind, there is single-mindedness, a dedication to a single purpose. For the uncultured mind, the whole life is scattered in many directions; 'bahusakhohyanantascta'. Therefore it is essential that the education which you acquire in these institutions should give you not merely learning and skill but endow you with a definite purpose in life. What that purpose is, you have to define for yourselves. It is said that Vidya gives you Viveka, a sense of what is right and helps you to avoid what is wrong. You must try therefore to find out what is required of you in this generation. A purpose which hailed good centuries ago, may not hold good today in view of the rapidly changing conditions of our country and of the world. So the purpose which you adopt in your life must be adapted to the relevant needs of the present generation. Questions:i)What does education mean to people in general? ii)How do we know that our actions are right or wrong? iii)What kind of approach to work has been suggested? iv)Describe three objects of education.v)What is the significance of technical skills in life?		
7	Use the following idioms and phrases in sentences of your own i)keep your chin up ii) come across iii) an apple of discord iv) in the cards v)hang about	5	
8	Prepare a user manual of a 'mobile' under the following headings : a) Cover Page b) diagram c) Functions d) Operation e) Precautions f) Guarantee	5	
9	Write a note on the various components of which a research paper is made up of \ Write the format of writing Research paper.	5	
10	Explain the different types of Job Interviews	5	
11	Write an application in response to the following advertisement. Assume that you are Arjun Singh. You have completed your M. Tech. in computer science from IIT Delhi. You are a dynamic professional with excellent communication skills. Apply in a multinational company based in Mumbai along with your resume.	5	



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		Assume that as a project development officer you have been			
		asked by the NIT of computer education, OPERA Building 24/15			
	12	camp, Pune to set up a communication technology center at	5		
12	12	Nagpur for training professionals in the use of latest technological	3		
		aids for face to face and distance communication .Write a			
		proposal for the same			

Start your Questions with following list of BT Verbs, to map the Blooms Level.

Remembering	Understanding	Applying	Analyzing	Evaluating	Creating
Define	Approximate	Apply	Analyze	Compare	Assemble
Describe	Characterize	Assign	Characterize	Conclude	Combine
Draw	Clarify	Construct	Compare	Criticize	Compile
Enumerate	Classify	Demonstrate	Confirm	Defend	Compose
Identify	Compare	Derive	Correlate	Determine	Construct
List	Compute	Determine	Detect	Discriminate	Create
Match	Convert	Draw	Diagnose	Estimate	Design
State	Describe	Employ	Diagram	Evaluate	Develop
Study	Differentiate	Examine	Differentiate	Calculate	Formulate
Write	Discuss	Express	Discriminate	Compute	Generate
	Distinguish	Modify	Distinguish	Explain	Incorporate
	Elaborate	Plot	Examine	Grade	Integrate
	Estimate	Predict	<mark>Explain</mark>	Justify	Interface
	Explain	Prepare	Figure out	Measure	Model
	Express	Produce	Illustrate	Predict	Modify
	Give	Show	Infer	Prescribe	Organize
	Predict	Depict	Investigate	Rate	Prepare
	Review	Portray	Maximize	Summarize	perform
	Rewrite	Simulate	Minimize	Test	Produce
	Subtract	Sketch	Optimize	Validate	Program
	Summarize	Solve	Point out	Verify	Rearrange
	Translate	Use	transform		Reconstruct



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Question Bank

2023-24

Branch	Computer Science & Engineering
Semester	3rd Semester
Subject	Universal Human Values
Subject Code	BTECH_CSE-506T

Department of Computer Science & Engineering

UNIT-I& II	Marks	Blooms	COn				
		Level					



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1	What is value education? Why there is a need for value education?	5	Re	
2	What is the purpose for value education?	5	Re	
3	What is happiness and prosperity? What are the wrong notions about attaining happiness and prosperity? What are the problems faced due to these wrong notions about happiness and prosperity?	5	Re/Un	
4	Do you think that human beings are a sum-total of sentiments and physical aspects the 'self'and the 'body'? Explain your answer using examples.	5	Un/An	CO-1
5	Define the process of self-exploration with help of diagram and its benefits	5	Re	
6	Distinguish between the needs of the self and the needs of the body.	5	Un	
7	What is Sanyam? How is it necessary in ensuring svasthya?	5	Re	
8	What is your vision of Happy and Prosperous life?	5	Un	

Start your Questions with following list of BT Verbs, to map the Blooms Level.

Remembering	Understanding	Applying	Analyzing	Evaluating	Creating
Define	Approximate	Apply	Analyze	Compare	Assemble
Describe	Characterize	Assign	Characterize	Conclude	Combine
Draw	Clarify	Construct	Compare	Criticize	Compile
Enumerate	Classify	Demonstrate	Confirm	Defend	Compose
Identify	Compare	Derive	Correlate	Determine	Construct
List	Compute	Determine	Detect	Discriminate	Create
Match	Convert	Draw	Diagnose	Estimate	Design
State	Describe	Employ	Diagram	Evaluate	Develop
Study	Differentiate	Examine	Differentiate	Calculate	Formulate
Write	Discuss	Express	Discriminate	Compute	Generate
	Distinguish	Modify	Distinguish	Explain	Incorporate
	Elaborate	Plot	Examine	Grade	Integrate
	Estimate	Predict	<mark>Explain</mark>	Justify	Interface
	Explain	Prepare	Figure out	Measure	Model
	Express	Produce	Illustrate	Predict	Modify
	Give	Show	Infer	Prescribe	Organize
	Predict	Depict	Investigate	Rate	Prepare
	Review	Portray	Maximize	Summarize	perform
	Rewrite	Simulate	Minimize	Test	Produce
	Subtract	Sketch	Optimize	Validate	Program
	Summarize	Solve	Point out	Verify	Rearrange
	Translate	Use	transform	-	Reconstruct



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Department of computer science and engineering

Subject: COMPUTER NETWORKS

UNIT-1

- 1. a. Compare Connection oriented and connectionless service. 6M b. Explain the functions and protocols and services of each layer? 6M
- 2. What are the major components of an optical communication system? 12M
- 3. a) Explain about the Guided transmission Medias in computer networks? 6M b) Briefly explain about the public switched telephone networks 6M
- 4. Write about network software & hardware 12M
- 5. Discuss about a) Computer applications 6M b) Features of LAN, MAN, WAN 6M
- 6. With a neat diagram describe in detail about the Network architecture. 12M
- 7. Explain in detail about a) Data Link Layer and Network Layer 6M b) Physical Layer 6M
- 8. Distinguish between point to point links and multi point links. Give relevant diagrams 12M
- 9. Discuss various types of networks topologies in computer network. 12M
- 10. Explain the ISO-OSI model of computer network with a neat diagram 12M QUESTION BANK 2019 2



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UNIT-2

- 1. Explain the operation of the bit-oriented protocol HDLC with the required frames 12M
- 2. Explain the various error detection and correction Mechanisms used in computer network 12M
- 3. Discuss about a) GO BACK NARQ 6M b) Selective repeat ARQ 6M
- 4. Explain about the Elementary data link protocols?. 12M
- 5. a. Discuss stop and wait protocol 7M b. Explain about the Carrier Sense Multiple Access Protocols? 5M
- 6. Briefly discuss about data link layer design issues? 12M
- 7. Explain about the data Link Layer switching 12M
- 8. Explain MAC sub layer protocol and frame structure of IEEE 802.11. 12M
- 9. Describe sliding window protocol using Go back n. 12
- 10. Compare and distinguish in detail the concept of Virtual-Circuit and Data gram Networks. 12M

UNIT-3

- 1. a.)State the major difference between distance vector routing and link state routing. 7M b) How these routing techniques work 5M
- 2. Explain about the Routing algorithms. 12M



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- 3. Discuss about a) concept of leaky bucket algorithm? 7M b) token bucket algorithm with neat diagram 5M
- 4. Explain in detail congestion control algorithms 12M
- 5. Explain CSMA/CD protocol 12M
- 6. Classify the functions of Bridges & Switches in brief. 12M
- 7. Explain the function of ARP & RARP 12M
- 8. a. Explain distance vector routing in detail. 7M b. Discuss about Inter-network Routing? 5M
- 9. Write a detail note on Internet protocols 12M QUESTION BANK 2019 3
- 10. a) Explain about the Shortest Path Algorithm? 7M b) Explain packet switching in detail. 5M

UNIT-4

- 1. a) Explain the duties of transport layer. 6M b) Write short notes on performance issues of transport layer 6M
- 2. Show the different approaches in Packet Switching. Explain them in detail. 12M
- 3. Enumerate the mechanism of three way handshake protocol for TCP 12M
- 4. Explain in detailed about the Transport Layer. 12M
- 5. Describe about a) TCP connection management. 5M b) Avoidance of congestion in TCP 7M



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- 6. Explain the function of TCP/IP protocol. 12M
- 7. a) Explain about elements of transport protocols 7M b) Briefly explain the internet transport protocols 5M
- 8. a) Discuss TCP and its various operations 12M b) Write short notes on User Datagram Protocol (UDP). 12M
- 9. Explain the operation of TCP with neat sketch. 12M
- 10. Discuss the various timers used by TCP to perform its various operations. 12M UNIT-5
- 1. Explain how security is provided in interact operations in detail 12M
- 2. a) Explain the working of Electronic mail. How SMTP used in Email applications 6M b) List and discuss the types of DNS records. 6M
- 3. Discuss in detail about world wide web 12M
- 4. a) Explain the encryption and decryption methods. 6M b) Discuss Application layer in details 6M
- 5. Explain in detail about function and structure of e-mail protocol. 12M
- 6. a) Discuss the File transfer Protocol (FTP)with a neat diagram. 6M b) Explain briefly simple network management protocol 6M QUESTION BANK 2019 4
- 7. Write short notes on IMAP and MIME with an example 12M
- 8. a)Discuss the features of HTTP and also discuss how HTTP works. 6M b) Explain about Application layer and its services in detail? 6M



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- 9. Describe the role of a DNS on a computer network with reference to its components. 12M
- 10. a) Write briefly about World wide web 6M b) Describe about High speed LANs 6M



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Department of computer science and engineering

Subject: COMPUTER NETWORKS



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Sessional-I (Month/Year)

Subject:Computer Architecture and Digital systems
Code:----Branch/Semester:-CSE- 3RD Semester

Time: 12:15 PM to 01:15 PM **Date-** ------

Maximum Marks: 30

Note: Attempt any 03Questions from group CO-1 and CO-2 each. All Questions carry equal marks.

Ques.	Description	Marks	Blooms	CO-
			level	n
1	Using Boolean Algebra show that: (any one) I. BC+ AC' +AB + BCD =BC + AC'	5	Ap	
	II. $AB' + ABD + ABD' + A'C'D' + A'BC' = A + BC' + C'D'$			
	Find Reduced Boolean expression using K-map (any one)			CO-
2	1. F(A,B,C,D)= π M(5, 6, 7, 12, 13) + d (4, 9, 14, 15)	5	Ap	1
	2. $F(A,B,C,D,E) = \sum_{i=1}^{n} m(0,2,3,8,9,12,13,15)$			
3	Verify De Morgans Theorem.	5	An	
4	Discuss on Multiplexers.	5	Un/An	
5	Discuss on basic operational concepts of Computer systems.	5	Un	
6	Classify Bus structure explains each in brief.	5	An	CO-
7	Classify different instruction format and explain each in brief.	5	An	2
8	Write short note on single bus structure.	5	Un	



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Sessional-II (Month/Year)

Subject:Computer Architecture and Digital systems
Code:----Branch/Semester:-CSE- 3RD Semester

Time: 12:15 PM to01:15 PM **Date-** ------

Maximum Marks: 30

Note: Attempt any 03Questions from group CO-1 and CO-2 each. All Questions carry equal marks.

Ques.	Description	Marks	Blooms level	CO-n
1	List various types of bus architecture and analyse function of any three buses.	5	An	
2	Explain with example, the steps of execution of complete Instruction.	5	An	CO 2
3	Demonstrate microprogrammed control system.	5	Ap	CO-3
4	Analyze function of each bit of Branching field BR of micro instruction format.	5	An	
5	Illustrate with example Decimal to Binary and Binary to Decimal conversion.	5	An	
6	Evaluate difference between two binary numbers using two's complement method 1) Case-1 Minuend > Subtrahend 2) Case-2 Minuend < Subtrahend	5	Ev	CO-4
7	Determine sign of final result of addition and subtraction of signed magnitude using 8 different conditions.	5	Ev	
8	Explain addition algorithm and subtraction algorithm, also	5	An /	
o	implement the same using hardware.		Cr	



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Sessional-I (Month/Year)

Subject: Micropro	cessor and	Microcontrol	lers
--------------------------	------------	--------------	------

Code: -----

Branch/Semester: ----EE/5th Semester-----

Time: -12:15 PM-- to -1:15 PM- **Date-** ------

Maximum Marks: 30

Note: Attempt any 03 Questions from group CO-1 and CO-2 each. All Questions carry equal marks.

Ques.	Description	Marks	Blooms	CO-n
			level	
1	Explain Internal architecture of 8 bit Microprocessor	5	Un	
_	8085.			
2	Examine function of each bit of flag register.	5	Ap	GO 1
3	PCHL is known as one word JMP instruction,	5	Ev	CO-1
3	Justify.			
4	RSTn is known as one word CALL instruction,	5	Ev	
	Justify.			
5	Summarize how ISR is executed after arrival of an	5	Ev	
	Interrupt.			
6	Distinguish between linear and absolute decoding	5	An	
0	technique.			CO-2
7	Design a memory system for 8085 using linear	5	Cr	002
/	decoding technique with 2KB RAM and 2KB ROM.			
8	Write an ALP to perform addition of 10 bytes in	5	An	
_	sequence.			



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Sessional-II (Sept-2023)

Subject:	Microprocessor	and	Microcontrollers

Code: -----

Branch/Semester: ----EE/5th Semester-----

Time: -12:15 PM-- to -1:15 PM- **Date-** ------

Maximum Marks: 30

Note: Attempt any 03 Questions from group CO-3 and CO-4 each. All Questions carry equal marks.

Ques.	Description	Marks	Blooms level	CO-n
1	Portray the architecture of 8255 PPI and describe each functional block in brief	5	Ap	
2	List different mode of 8255 PPI, and Explain BSR mode.	5	An	CO-3
3	Design an interface between 8255 and 8085 Microprocessors.	5	Cr	
4	Design an i nterface between 8255, LED and 8085 Microprocessors.	5	Cr	
5	Distinguish between 8085 Microprocessors and 8051 Microcontrollers based on their features.	5	An	
6	Explain programming model (Register organization) of 8051.	5	An	
7	Write an ALP to display the message "GNIET, Nagpur" continuously on display interfaced on port 2 of 8051.	5	Ap	CO-4
8	Write Serial port SFRs format of 8051, and examine function of each bit.	5	An	



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SESSIONAL EXAMINATION [PUT-Winter 2023]

Subject: Microprocessor & Microcontroller

Code: -----

Branch/Semester:Electrical Engineering / 5th Semester **Time:** 10:15 AMto 01:15 PM **Date-** 12-10-2023

Maximum Marks: 70

Note: Attempt Questions as follows:

Q1 or Q2 - Unit-I

Q3 or Q4 – Unit-II

Q5 or Q6 - Unit-III

Q7 or Q8 – Unit-IV

Q9 or Q10 - Unit-V

Each Questions carries equal mark (14). Each part carries equal mark (7)

Ques.	Part	Description	Marks	Blooms level	CO-n
Q1	a	Explain Internal architecture of 8 bit Microprocessor 8085.	7	Un	
	b	Examine function of each bit of flag register.	7	Ap	
		OR			CO-1
Q2	a	RSTn is known as one word CALL instruction, Justify .	7	Ev	
~2	b	Explain the function of DAA instruction.	7	An	
Q3	a	Differentiate the role of CALL and RET instruction in execution of SUROUTINE.	7	An	
Q3	b	Summarize how ISR is executed after arrival of an Interrupt.	7	Ev	
		OR			CO-2
Q4	a	Write an ALP to find square root of the number (81) residing at 1000H memory location and save the result at 1001H.	7	An	
	b	Distinguish between linear and absolute decoding technique.	7	An	
Q5	a	Portray the architecture of 8253/54 PIT and describe each functional block in brief.	7	Cr	CO-3
Q3	b	Design an interface between 8255, LED and 8085 Microprocessors.	7	Cr	20-3



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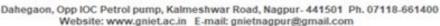
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		OR			
0.6	a	Design an interface between 8255 and 8085 Microprocessors.	7	Cr	
Q6	b	Write control word format of 8253/54 PIT, and examine function of each bit.	7	An	
Q7	a	Write PSW format of 8051, and examine function of each bit.	7	An	
	b	List addressing modes of 8051. Explain each with one example.	7	An	GG 4
	OR			CO-4	
	a	Explain stack operation in 8051.	7	An	
Q8	b	Write Interrupt SFRs format of 8051, and examine function of each bit.	7	An	
Q9	a	Design an interface between 8251 Microcontroller, 4KB ROM and 2KB RAM memory.	7	Cr	
	b	Design an interface between 8251 Microcontroller, 7-segment LED display.	7	Cr	
		OR			CO-5
010	a	Design an interface between 8251 Microcontroller, DC motor	7	Cr	
Q10	b	Design an interface between 8251 Microcontroller, ADC 0809	7	Cr	



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DEPARTMENT OF COMPUTER SCIENE ENGINEERING Session 2022-23(ODD)

> Sessional 1 Time Table

> > Date: 18/08/2022

Day	Date	Time	VII SEM
Mon	22/08/2022	10.00am-11.00am	LANGUAGE PROESSOR
Tue.	23/08/2022	10.00am-11.00am	MOBILE COMPUTING
Wed.	24/08/2022	10.00am-11.00am	DATA WAREHOUGING AND MINING
Thu.	25/08/2022	10.00am-11.00am	TCP / IP

Note:

- 1) It is compulsory for all the students to appear for the exam.
- 2) The question paper of each subject will be of descriptive pattern
- 3) Total number of questions:08(From 2units), Solve any 5, Maximum marks: 20, Time: 1Hr.
- 4)Sessional exam will be conducted in first class, remaining classes will continue as per time table.

Prof Ayaz Khan HOD,CSE, GNIET



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501.

Department of Electrical Engineering

Year/Sem: IV Yr/VII SEM

Time: - 01 Hours

Subject:

Maximum Marks: - 20

Date:-

INSTRUCTIONS TO CANDIDATE.

- 1) All Questions carry marks as indicated.
- Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
- 4) Assume suitable data whenever necessary.
- 5) Use of non programmable calculator is permitted.
- Write short note on 2G services.
- Describe antennas and its types.
- Explain the simplified reference model of wireless system.
- Differentiate between wired and wireless network.
- Compare SDMA, FDMA, TDMA and CDMA with 5. various factors
- What are the applications of wireless communication?
- Explain in detail cellular system in wireless communication system
- Explain space and frequency division multiplexing With detail diagram



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY Dahegaon, Kalmeshwar Road, Nagpur-441 501 Department of Fleetrics Engineering Sessional - 1 Year Sem: IV Yr VII SEM Subject: Time: - 01 Hours Maximum Marks: - 20 Date:-INSTRUCTIONS TO CANDIDATE. 1) All Questions carry marks as indicated. Solve FIVE questions. 3) Illustrate your answer wherever necessary with the help of neat sketches. Assume suitable data whenever necessary. 5) Use of non programmable calculator is permitted. What is Computer Network? What is need of Computer Network? What is mean by Protocol? Explain its Standards. Explain TCP/IP Reference Model In detail Different between wired LAN &wireless LAN or Different between Ethernet (802.3) &IEEE802.11 Explain the concept of Classful addressing Change the following IP Address from dotted Decimal notation to binary notation. i)114.34.2.8 ii)129.14.6.8 iii)208.34.54.12 iv)241.34.2.1 Explain Class A addressing. Find the Class of following IP address. i)208.34.54.12 ii)238.34.2.1 iii)114.34.2.8 iv)129.14.6.8 v)241.34.2.8



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SESSION 2022-23 DEPARTMENT OF COMPUTER SCIENCE&ENGINEERING SESSIONAL-I

	Year/Sem; IV Yr/VII SEM Time: - 01 Hours Date:-	Subject: Dataware Housin Data Mining Maximum Marks: -20	
0	INSTRUCTIONS TO CANDIDATE. 1) All Questions carry marks as indicated. 2) Solve-FIVE questions. 3) Illustrate your answer wherever necessary with the help of nea 4) Assume suitable data whenever necessary. 5) Use of non programmable calculator is permitted.	t sketches.	
	1 Explain the evolution of Database technology?		4
H	2. Explain the steps of knowledge discovery in databases?		4
	3. Explain various tasks in data mining?		4
	4. Explain various techniques in data mining?		4
	5. Explain the major issues in data mining.		4
6	5. Explain the classification of data mining systems.		4
7	Explain the data mining functionalities in detail.		4
23.	Explain the Three-tier data warehouse architecture?		4



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Department of Computer Science Engineering Sessional - I

Year/Sem: IV Yr/VII SEM

Time: - 01 Hours

Date: - 22/0/22

Maximum Marks: - 20 Subject: LP

Maximum Marks: - 20

Subject: LP Sessional - I

Year/Sem: IV Yr/VII SEM

fime: - 01 Hours

Date: - 22/0/22

Department of Computer Science Engineering

INSTRUCTIONS TO CANDIDATE.

All Questions carry marks as indicated

Solve FIVE questions.

Illustrate your answer wherever necessary with the help of neat sketches

Use of non programmable calculator is permitted. Assume suitable data whenever necessary.

What are different phases of compiler. Explain them in detail.

bootstrapping is used in design of a compiler What is Cross compiler? Explain How

What are compiler writing tools

to identify the Token keyword program dentfer and special symbol Write and LEX 4

S

Explain RMD and LMD

Give the difference between One pass compiler and two pass compiler 6

What s CFG. r.

What s ambiguous grammar? Check whether the given CFG ambiguous or not. 00

S —> aSbS /bSaS / €

What are different phases of compiler. Explain

Illustrate your answer wherever necessary with the help of neat sketches

1) All Questions carry marks as indicated

Solve FIVE questions.

INSTRUCTIONS TO CANDIDATE.

Use of non programmable calculator is permitted. Assume suitable data whenever necessary.

them in detail.

bootstrapping is used in design of a compiler What is Cross compiler? Explain How ci

What are compiler writing tools 3 Token keyword Write and LEX program to identify the dentier and special symbol. Explain RMD and LMD 4

iń

7

What s CFG.

Give the difference between One pass compiler and two pass compiler 9

What s ambiguous grammar? Check whether the given CPG

ambiguous or not.

aSbS



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Guru Nanak Institute Of Engineering & Technology, Nagpur

Dahegaon, Kalmeshwar Road, Nagpur-4415001 Department of Computer Science and Enginnering

(NAAC Accredited)

Date :- 05/09/2022

Noitce

All the studnets of 5sem cse are hereby Informed that the Sessional -I Exam will be starting from 12Sep2022 and exam time table will be displayed on Notice Board as well as in students whatsup group.

Prof Ayaz Khan

I/c HOD, CSE

Copy to

- 1. Principal, GNIET
- 2. Vice-Principal, GNIET



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DEPARTMENT OF COMPUTER SCIENE ENGINEERING

Session 2022-23(ODD) Sessional I

Time Table

1	
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Date: 5/09/2022

Day	Date	Time	V SEM
Mon	12/09/2022	10.00am-11.00am	Design Pattern
Tue.	13/09/2022	10.00am-11.00am	Artificial Intelligence
Wed.	14/09/2022	10.00am-11.00am	SEPM
Thu.	15/09/2022	10.00am-11.00am	Design & Analysis of Algorithm
Friday	16/09/2022	10.00am-11.00am	Effective Technical Communication

Note:

 It is compulsory for all the students to appear for the exam. 2) The question paper of each subject will be of descriptive pattern

3) Total number of questions: 08(From 2units), solve any 5, Maximum marks: 20, Time: 1Hr.

4) Sessional exam will be conducted in first class, remaining classes will continue as per time table.

Prof. Ayaz Khan

Prof. Neha Chourasia

Prof. R.M.Bhombe

Dr. Hemant Hajare

HOD, CSE

Associate Dean Academics

Vice Principal

Principal



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Guru Nanak Institute of Engineering & Technology Nagpur

Subject- Effective Technical Communication

Semester -V sem (CSE)

Total Marks-20

NOTE-1) Solve questions as per given instructions, 2)Each questions carry one marks

Q.1.a)Correct any four of the following sentences [4 M]
i)She is an university student.
ii)The drainage and sanitary system of this town is not good.
iii)When I went there I found the tiger was disappear.
iv)Sanskrit, as well as French, were taught there.
v)Copy word to word.

Q.1.b)Do as directed any four.[4 M]
i)'Do you really come from Japan?' said the prince. (Change the narration)
ii)I am certain of giving you satisfaction. (Change into compound sentence)
iii)You have done this before (Change the voice)
iv)You made up your own study material (Add a Question tag)
v)Your malicious remarks compel me to write this letter.(Change the voice)
vi)Iron is more useful than copper.(Change the degree)

Q.1.c)Use any four of the following idioms and phrases in sentences of your own.[4 M]
i)White elephant
ii)A snake in the grass
ii)To bark upon a wrong tree
iv)Blue in the face
v)A cold fish
vi)End of the world.

Q.2.a)Do as directed any four.[4 M]
i)Explain the meaning and origin of the word 'Wanderlust'.
ii)Make two words using the prefix 'Sub'.
iii)Make two words using the suffix 'dom'.
iv)Write any two technical jargon related to medical field.
v)Explain the phrase 'Kept in the Loop'.
vi)Write a one word for "one who knows many languages"

Q.2.b)Do as directed any four. [4 M]
i)Give one word for "one who copies from other writers".
ii)Choose the word which can be substituted for "a thing which is no longer in use"
iii) Write a substitute for "A government runs by a people"

iv)Find similar relationship from the options given

Pulse : Heart

a)pitch : music b)nerve : brain

e)tick : clock d)motor : engine e)blood : arteries

v)Find similar relationship from the options given

Purity: Evil

a)Night:Day

b)north:Climate

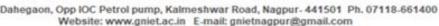
· c)angle:horns

d)boldness:victory



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Dahegaon, Kalmeshwar Road, Nagpur-441 501

Department of Computer Science and Engineering Sessional - I

Year Sem:- III Yo'V Sem

Time: - 01

Date:- 12/09/022

Subject: Design Pattern Maximum Marks: - 20

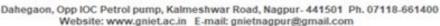
INSTRUCTIONS TO CANDIDATE.

- 1) All Questions carry marks as indicated.
- 2) Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
- 4) Assume suitable data whenever necessary.
- Use of non programmable calculator is permitted.
- 1. What is Design Pattern?
- 2. What is Object Oriented Design Pattern?
- 3. What is Gang Of Four(GOF)?
- Explain in details the Catelog / Classifications or a categories of design pattern.
- 5. How to use of Design Pattern?
- 6. What is a Creational Pattern?
- 7. What is Signleton Pattern and their use?
- 8. Write a program to demonstrate the use of Singleton pattern.



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Dahegron Kalmeshwa Road Nagpin 441 Str.

Department of Computer Science & Engineering Sessional - I

Subject: Artificial Intelligence Your Sean 111 V V SEM Time 01 Hours Maximum Marks: - 20 Date INSTRUCTIONS TO CANDIDATE O All concerns carry mans as indicated 2) Solve FIVE questions: O Illustrate your answer wherever necessary with the help of near sketches. a) some smable data whenever necessary of non-programmable calculator is permitted. 4 What is production system? Explain its characteristics. You are given two jugs, a 4-gallon and a 3-gallon one. Neither 4 have any measuring marks on It. There is a pump that can be used to fill the jugs with water. How can you get exactly 2-Callons of water into a 4-gallon jug? Discuss the problem in view of A.J. What is state space? Explain with example. Discuss Al techniques in details. State and explain 4 Compare & contrast between BFS and DFS What is Henristic search? Explain in detail. 4 Explain Informed and un-informed search techniques in detail.

Differentiate between A* and AO* Algorithm.



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SESSION 2022-23

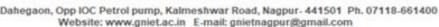
DEPARTMENT OF COMPUTER SCIENCE&ENGINEERING SESSIONAL-I

Year/Sem: III Yr/V SEM Subject: Software Engineering& Product Management Time: - 01 Hours Maximum Marks: - 20 Date: INSTRUCTIONS TO CANDIDATE. 1) All Questions carry marks as indicated. Solve FIVE questions. Illustrate your answer wherever necessary with the help of neat sketches. Assume suitable data whenever necessary. Use of non programmable calculator is permitted. 1 Explain the software engineering Myths Explain software engineering as layered technology? Explain Waterfall Model ? 4. Explain Incremental Model? Explain evolutionary process model Explain communication practices in details. 7. Explain system engineering hierarchy 8. Write short note on product engineering?



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Dahegaon, Kalmeshwar Road, Nagpur-441 501.

Department of Computer Science & Engineering Session: 2022-23

Sessional - 1

Year/Sem: IIIYr/V SEM

Time: - 01 Hours Date: 15/09/2022 Subject: Design Analysis & Algorithm Maximum Marks: - 20

INSTRUCTIONS TO CANDIDATE.

- 1) All Questions carry equal marks(4 marks each)
- Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
- 4) Assume suitable data whenever necessary.
- 5) Use of non programmable calculator is permitted.

Q1. Explain algorithm design strategy in detail

Q2. Solve by characteristic equation method an = 5an-1 - 6an-2 (2) with initial conditions a0 = 1 and a1 = 4

Q3. Explain worst-case and averge case analysis

Q4. Solve by characteristic equation method an = 6an-1 - 9an-2 (3) with initial conditions a0 = 4 and a1 = 6.

Q5. Differentiate dynamic programming and greedy strategy

Q6. Solve by master method T(n) = 3T(n/2) + n 2

Q7. Differentiate dynamic programming and divide-and-conquer

Q8. Solve by master method T(n) = 4T(n/2) + n 2



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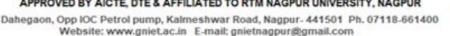
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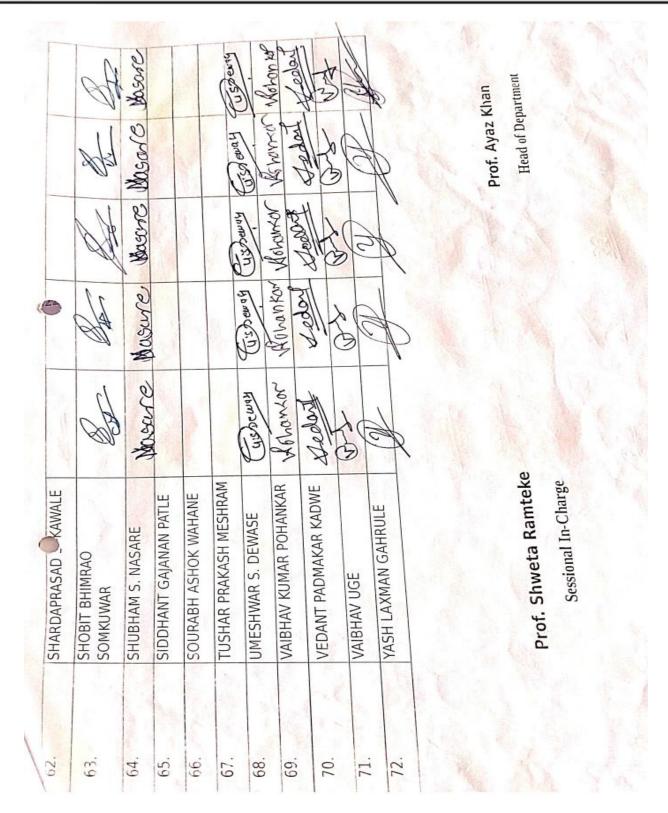


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Guru Nanak Institute Of Engineering & Technology, Nagpur

Dahegaon, Kalmeshwar Road, Nagpur-4415001 Department of Computer Science and Enginnering

(NAAC Accredited)

Date :- 10/10/2022

Noitce

All the studnets of 3rd sem cse are hereby Informed that the Sessional -I Exam will be starting from
17 Oct 22 and exam time table will be displayed on Notice Board as well as in students whatsup group.

Prof Ayaz Khan

I/c HOD, CSE

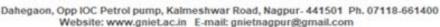
Copy to

- 1. Principal, GNIET
- 2. Vice-Principal, GNIET



GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY









GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY, NAGPUR

DEPARTMENT OF COMPUTER SCIENE ENGINEERING

Session 2022-23(ODD)

Sessional 1 Time Table

CLASS: III SEM

Date: 10/10/2022

Day	Date	Time	III SEM
Mon.	17/10/2022	10.00am-11.00am	OS
Tue.	18/10/2022	10.00am-11.00am	CAO
		01.30pm-02.30pm	OOPS
Ved.	19/10/2022	10.00am-11.00am	UHV
hu.	20/10/2022	10.00am-11.00am	MATHS-3

Note:

It is compulsory for all the students to appear for the exam.

2. The question paper of each subject will be of descriptive pattern

3. Total number of questions:08(From 2units), Solve any 5, Maximum marks: 20, Time: 1Hr.

4. Sessional exam will be conducted in first class, remaining classes will continue as per time table.

Prof. Shweta Ramteke Exam Incharge, CSE Prof. Ayaz Khan HOD,CSE



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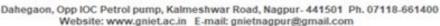
Dahegaon, Opp IOC Petrol pump, Kalmeshwar Road, Nagpur- 441501 Ph. 07118-661400 Website: www.gniet.ac.in E-mail: gnietnagpur@gmail.com





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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501. Department of Computer Science & Engineering Sessional - I

Year/Sem: II Yr/III SEM Time: - 01 Hours

Subject:Object Oriented Programming

Maximum Marks: - 20

Date:-18-10-22

INSTRUCTIONS TO CANDIDATE.

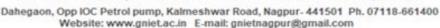
- 1) All Questions carry marks as indicated.
- Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
- 4) Assume suitable data whenever necessary.
- 5) Use of non programmable calculator is permitted.

	Q.1	What are the features of Java	-
	Q.2	a. How to declare Class and object in Java? What is the use of new keyword in Java b. what are jump statement in Java	4
	Q.3	Differentiate between Object Oriented Programming and Procedure Oriented	4
	Q.4	Write a program to declare a class box and define two constructor. One will initiate the abject with default value and other with arguments	4
	Q.5	a. What is array? How to declare one and two dimensional array in Java. b. Why java is platform independent ?explain	4
	Q.6	Define class student with data member rollno, name and percentage. Using this class create 5 student and display information of student who scored highest percentage	4
	Q.7	Write a program to declare a class Box and define data member length, breadth and height. Write a method to calculate the area of the box. Write suitable code to invoke	4
)	Q.8	a. What is ternary operator explain with example b. Explain for each loop in Java with example	4
		a. What is JVM in Java programming b. What is scope and lifetime of variables	4



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501.

Department of Computer science Sessional - I

Year/Sem: II Yr/ III Sem Time: - 01 Hours Subject: - Ethics in it. Date:- //022 Maximum Marks: - 20

- INSTRUCTIONS TO CANDIDATE. Questions carry marks as indicated.
- 2) Solve FIVE questions.
- 3) Illustrate your answer wherever necessary with the help of neat sketches.
- 4) Assume suitable data whenever necessary.
- 5) Use of non programmable calculator is permitted.

1.	Explain privacy in it.	4
2.	How to control access to information on the internet.	4
3.	Explain education employees.	4
4.	Explain ethics in it.	4
5.	What are common types of security attacks.	4
7.	What is IT professionals malpractice. Write short notes on software privacy.	4 4
8.	Explain prevention in computer and internet crime.	4



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Dahegaon, Kalmeshwar Road, Nagpur-441 501

Department of Computer Science Engineering

Session 2022-2023 (ODD)

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Prof. Shweta Ramteke

Sessional In charge, CSE

Prof. Ayaz Khan HOD, CSE



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY, NAGPUR

DEPARTMENT OF COMPUTER SCIENE ENGINEERING Session 2022-23(ODD)

Sessional II Time Table

Date: 22/09/2022

Day	Date	Time	VII SEM
Mon	26/09/2022	10.00am-11.00am	TCP / IP
Tue.	27/09/2022	10.00am-11.00am	MOBILE COMPUTING
Wed.	28/09/2022	10.00am-11.00am	DATA WAREHOUGING AND MINING
Thu.	29/09/2022	10.00am-11.00am	LANGUAGE PROESSOR

9)

Note:

- 1) It is compulsory for all the students to appear for the exam.
- 2) The question paper of each subject will be of descriptive pattern
- 3) Total number of questions:08(From 2units), Solve any 5, Maximum marks: 20, Time:1Hr.
 - 4)Sessional exam will be conducted in first class, remaining classes will continue as per time table.

Prof. Shweta Ramteke Exam Incharge, CSE Prof. Ayaz Khan HOD,CSE



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501.

Department of Computer Science and Engineering Sessional - II

Maximum Marks: Subject: TCP/IP

 All Questions carry marks as indicated 2) Solve FIVE questions.

 Illustrate your answer wherever necessary with the help of near sketches Assume suitable data whenever necessary.

Use of non programmable calculator is permitted.

INSTRUCTIONS TO CANDIDATE.

Explain IP Datagram in Detail?

List and explain various packet forwarding techniques used

Explain how to calculate the optimal path for routing the Packet,

Write short note on IGMP.

Explain TCP header format.

Explain TCP connection Establishment and Termination.

State different Timers used in TCP

Write short note on Extension Header

Year/Sem: IV Yr/VII SEM Time: - 01 Hours 20 Date:-

INSTRUCTIONS TO CANDIDATE.

Date:-

Illustrate your answer wherever necessary with the help of neat sketches

All Questions carry marks as indicated.

2) Solve FIVE questions.

Assume suitable data whenever necessary.
 Use of non programmable calculator is permitted.

Explain IP Datagram in Detail?

List and explain various packet forwarding techniques used by IP.

Explain how to calculate the optimal path for routing he Packet.

Write short note on IGMP.

Explain TCP header format.

Explain TCP connection Establishment and Termination. 9

State different Timers used in TCP

Write short note on Extension Header.

Department of Computer Science and Engineering. ad, Nagpur-441 501. Dahegaon, Kalmeshwak

Sessional - II Year/Sem: IV Yr/VII SEM Time: - 01 Hours

Subject: TCP/IP

Maximum Marks: - 20



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Dahegaon, Kalmeshwar Road, Nagpur-441 50 L

(NAAC Accredited)

SESSION 2022-23

DEPARTMENT OF COMPUTER SCIENCE&ENGINEERING SESSIONAL-II

Year/Sem 4:/VII SEM	Subject: Mobile Computing
Time: - 01 Hours Date:- 27/09/2022	Maximum Marks: - 20
INSTRUCTIONS TO CANDIDATE. 1) All Questions carry marks as indicated. 2) Solve FIVE questions. 3) Illustrate your answer wherever necessary with the had a Assume suitable data whenever necessary. 5) Use of non programmable calculator is permitted.	nelp of neat sketches.
Describe snooping TCP.	4
2. Explain home location register.	4
3. What is cell layout and frequency planning.	the state of the state of
4. Explain fast retransmit.	4
5. Explain TCP over wireless networks .	4
6. What is mobile ip.	4
Write short note on Echo canceller.	4
8. Explain Traditional TCP a	nd Intraditional TCP.



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Use of non programmable calculator is permitted.

Construct LL (1) parsing table for the following grammer

AaAb/BaBb , A → ∈ .B → ∈

Explain use of following function in TAC using SDTS

(i) Makelist (I) (ii) Merg (P1, P2)

(iii) Backpatch (P, i) (iv) Gen ()



left

factorization for the given CFG while controcuring LL(1) parser.

we remove left recursion and

Explain why these errors

	GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY Dehregson, Kalmeshwar Road Naggur-441 501. Department of Computer Science Engineers.	Year/Sem: 1V Yr/VII SEM Sessional - II Subject: - 01 Hours Subject: 29/09/2022 Maximum Marks: - 20	INSTRUCTIONS TO CANDIDATE. 1) All Questions carry marks as indicated. 2) Solve FIVE questions.	 Illustrate your answer wherever necessary with the help of neat sketches. Assume suitable data whenever necessary. Use of non programmable calculator is security.
Dahregam, Kalmeshwar Road, Nagpur-441 501. Department of Control of the Control	Year/Sem: IV Yr/VII SEM Sessional - II Time: - 01 Hours Date: - 29/9/2022 Maximum Marker, 20	D CANDIDATE. marks as indicated. nns.	 Ittustrate your answer wherever necessary with the help of near sketches. Assume suitable data whenever necessary. Use of non programmable calculator is permitted. 	

GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

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1. Construct LL (1) parsing table for the following grammor	S → AaAb/BaBb , A → € ,B → €	 Explain use of following function in TAC using SDTS. Makelist (I) (ii) Merg (P1, P2) (iii) Backpatch (P, I) (iv) Gen () 	Define (i) Inherited attribute (ii) Synthesized attribute(iii) Semantic action	 Generate three address code for the following statements using SDTS A:= - B * (C + D) 	\$\frac{\frac{\partial}{\partial}}{\partial}}\$ (i) Error recovery in LR parsing (ii) Error recovery in lexical phase	6Aplain error recovery in LL parsing using suitable example.	7. What are syntactic and semantic errors suggest methods to recover from

Generate three address code for the following statements using SDTS

A:=-B*(C+D)

5,xplain

3Define (i) Inherited attribute (ii) Synthesized attribute(iii)

Semantic action

perform left factorization for the given CFG while contracturing LL(1) parser. left recursion and we remove Explain why

What are syntactic and semantic errors suggest methods to recover from

these errors

Gxplain error recovery in LL parsing using suitable example.

(ii) Error recovery in lexical phase

(i) Error recovery in LR parsing



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501

Department of Computer Science Engineering

Session 2022-2023 (ODD)

Sessional II Attendance

Class: VIITH SEMESTER

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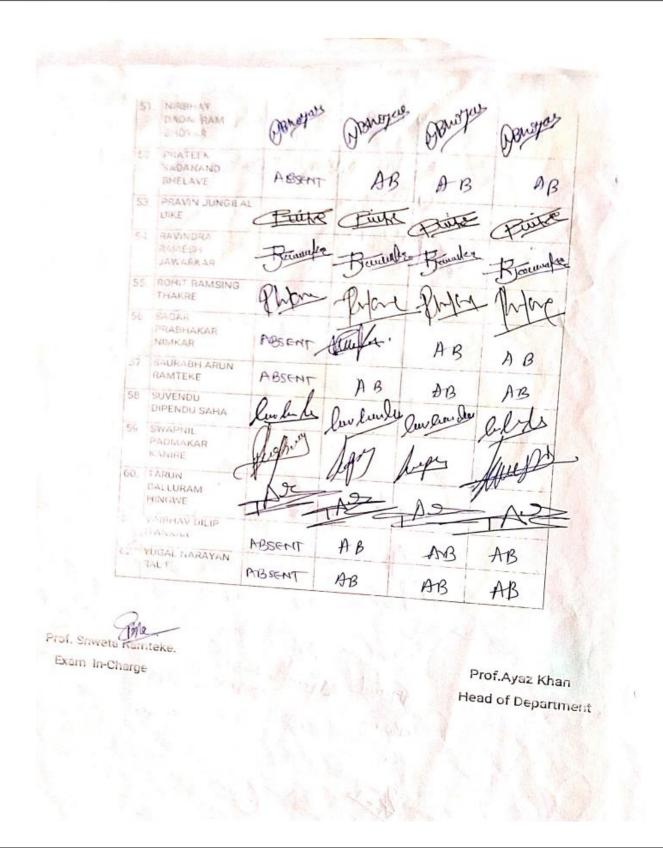
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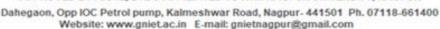
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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY







GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY. NAGPUR DEPARTMENT OF COMPUTER SCIENE ENGINEERING

Session 2022-23(ODD) Sessional II Time Table

CLASS: VIII SEM

Date: 10/10/2022

Day	Date	Time	V SEM
Mon.	17/10/2022	10.00am-11.00am	DP
Tue.		10.00am-11.00am	AI
	18/10/2022	01.30pm-02.30pm	SEPM
Wed.	19/10/2022	10.00am-11.00am	DAA
Thu.	20/10/2022	10.00am-11.00am	ETC

Note:

- It is compulsory for all the students to appear for the exam.
- 2. The question paper of each subject will be of descriptive pattern
- 3. Total number of questions:08(From 2units), Solve any 5, Maximum marks: 20, Time: 1Hr.
- 4. Sessional exam will be conducted in first class, remaining classes will continue as per time

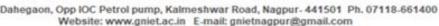
Prof. Shweta Ramteke Exam Incharge, CSE

Prof. Ayaz Khan HOD, CSE



GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY







GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501.

Department of Computer Science and Engineering Sessional - II

Year/Sem: III Yr/V Sem Time: - 01 Hours Date:- 17/10/022

Subject: - Design Pattern Maximum Marks: - 20

INSTRUCTIONS TO CANDIDATE.

- All Questions carry marks as indicated.
 Solve FIVE questions.

 - 3) Illustrate your answer wherever necessary with the help of neat sketches. 4) Assume suitable data whenever necessary.

 - 5) Use of non programmable calculator is permitted.

1.	What is Structure 1.5	
2.	What is Structural Design Pattern? Explain and the type of	4
3.	Draw the UML diagram of Adaptor patterns. Define and Explain Bridge patterns. Explain the present the patterns.	4
4.	Explain the properties of composite patterns.	4
5.	Define Behavioural parts	4
6.	Define and Explain Chair D	4
7. 8.		4
	Explain the Template method patterns with the help of examples.	4



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501.

Department of Computer Science & Engineering Session: 2022-23

Sessional - II

Year/Sem: IIIYr/V SEM

Time: - 01 Hours Date: 19/10/2022 Subject: Design Analysis & Algorithm

Maximum Marks: - 20

INSTRUCTIONS TO CANDIDATE.

1) All Questions carry equal marks(4 marks each)

Solve FIVE questions.

Illustrate your answer wherever necessary with the help of neat sketches£

4) Assume suitable data whenever necessary.

Use of non programmable calculator is permitted.

1. Find the optimal binary merge pattern for ten files whose length are 38, 42, 22, 15, 94, 63, 101, 43, 13, 21.

2.Explain Job Sequencing approaches. Find the best possible sequence for the following deadlines.

Job	1	2	3	4	5	6	7
Gain	35	20	18	16	12	10	8
Deadline	3	1	3	4	2	2	1

- 3.What is Dynamic Programming? Explain the difference between Dynamic Programming and Greedy method.
- 4. Prove that P£NP (P is subset of NP)
- 5.Explain Deterministic Algorithms
- 6. What is P class problems
- 7. Explain NP-hard problems
- 8. Explain NP-complete problemss



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Guru Nanak Institute of Engineering & Technology Nagpur

Dahegaon, Kalmeshwar Road Nagpur

Department of computer science and engineering Sessional - II

Year /Sem-5th sem Time- 1 Hr

Subject- Effective Technical communication Max Marks- 20

Q.1.A) find the most suitable word which is farthest in meaning to the word given (Any two) [2Marks]

1) Alleviation

A)Lessening B)Magnification C)intensity D)aggravation E)Exaggeration

A)viciouis B)vulgar C)miserly D)insincere E)none

3) Obey

A)attract B)disobey C)repel D)diffuse

E)none

Q.1.B) Pick out the word that has most nearly the same in meaning as the words in capitals. [2Marks]

1)repeal

A)Pay B)Undress C)iterate D)withdraw

2)Grasp

A)sorrow B)Release C)hinder D)enlarge

Q.2.A) write a one word for group of words to replace them(any two) [2 1)One who specializes in hand writing

2)One who works for free

3)That through which light can pass

(

Q.2.B) Write a various Types and stages in interviews and Explain it.[4 M] OR What you prepapre for interview And write the questions can ask in interview

Q.3.A) Bobby Lawrence orderd 100 sets of shirtys cordinates from caspani of Milan for testing the market . Buut the shipment arrives in poor condition. Bobby wants to write a complaint letter. [5M]

Q.3.B)Place an order of 600 drafters and 400 engineering calculators to be within 15 days from the date of order, reseving rights to reserves the goods if delivered late.[5M]

Q.4.A) draft the letter of enquiry to brite wattch company, pune asking for a copy of thier latest catalogue . Ask for their best trade term. [5M]

Q.4. B) write a notice informing all the employees of fahell company that new Community hall is going to be innaugrated on 1st December, 2022. [5M]



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501

Department of Computer Science Engineering

Session 2022-2023 (ODD)

Sessional II Marks

CLASS: VIII SEMESTER

Ro II	Student Name	DP	AI	SEPM	DAA	Para
-	DATE	17/10/22		0/22	10/10/22	ETC 20/10/22
1,	AACHAL D. MESHRAM		Buethram	Brailmoun	Book on	Aneshra "
2.	AMISHA ROSHAN RAMTEKE	Absent	Absent	Absent	Absent	Absent
3	ANAM M.AARIF SHEIKH	Ancury	Anage	Anupa	Anara	Brem
4,	ANJUM SHEIKH	Absent	Absent	Absent	Absent	Absent
5,	ASHWINI VIJAY CHAUHAN	Matolia.	ASTROPHE	Astoria	Betroin	Assorbei
6.	DIVYANI P. PATURDE	Absent	Absent	Absend	Absent	Absent
7.	DIVYANI SUJIT NITNAWARE	Durana	Absent	Absent	Absent	Absent
8.	HRUTU SHISHSUPAL' WALDE	Au tald	1 Jusable	- Andreade	Absent	A Drolds
9.	ISHA MURLIDHAR GOTMARE	Formpe	Formos	Patryol	Jamus	(Jahn)
10.	JAYASHRI KISHORE PAHADE	Jahan	Thrade	Thode	Trabal	: Tahade
11.	JYOTI YOCESH BAWANE	Absent	Absent	Absent	Absent	Absend
12.	KALLYANI ASHOK GEDEKAR	Bedeker	Absint	Absent	Absent	Absent



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l	9. PRANITA CHARRADHAR PATIYE	Ab	AB	46	AB	AL
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20	ROJI TEJRAM WAGHMARE	AL	Ab.	Аь	Ab	AL.
21.	RUCHIKA KESHAV KANGALE	Аъ	As	AB	AB	AL
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58.	RITIK SHAMRAO DHARANE	Ab	Ab	AB	Bb	1
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60.	SAURABH SUDHAKAR PRANJALE			AB	AL	AL AL
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•	Prof. Shweta Ramteke Sessional In charge, CSE	The state of			Prof. Ayaz 1 HOD, CSF	



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGYNAGPUR DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(Odd)

Pre-University Test (PUT) EXAM Time Table

OFFLINE EXAM III SEM

Date: 20/01/2023

Day	Date	Time	Subject	T/P
		10.00am-1.00pm	CADS	T
Mon 23/01/2023	2.00 pm – 4.00 pm	Computer Workshop 1	P	
Wed 25/01/2023		10.00am-1.00pm	os	T
		2.00 pm – 4.00 pm	OS	P
7. 27/01	27/01/2023	10.00am-1.00pm	OOPS	T
Fri.	27/01/2023	2.00 pm – 4.00 pm	OOPS	P
		10.00am-1.00pm	MIII	T
Mon.	30/01/2023	2.00 pm - 3.00 pm	UHV	T
16		10.00am-1.00pm	EIT	T
Tue.	31/01/2023	2.00 pm - 3.00 pm	EVS	T

Note

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 80, Time: 3Hr.
- 4) Clear balance fees .dues if any

Prof. Nayan Shambharkar

Exam In-charge

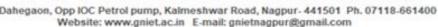
Copy to

- 1) Principal, GNIET
- 2) Vice Principal, GNIET
- 3) Associate Dean

Prof. Ayaz Khan Head of Department



GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY





GNIET/2022-2023/ODD/BECSE62T/M.S

Pre University Examination 2022-23 V-B. E. (Computer Science & Engg.) (C.B.C.S.) Examination

SUBJECT: UNIVERSAL HUMAN VALUES

Time: - Three Hours Maximum Marks: - 35 INSTRUCTIONS TO CANDIDATE.

- - All questions carry marks as indicated.
 - 2) Answer any Six questions
 - Assume suitable data wherever necessary.
- 4) Illustrate your answer wherever necessary with the help of peat sketches.
- Q1. (A) Define Value Education in detail. (07)(B) Explain the importance of Self Exploration in Value Education (07) Q2. (A) What do you mean by Harmony of Self with Body? (07)(B) Differentiate between Need of Self and Need of Body. (07)Q3. (A) Highlight the importance of values in relationship. (07)OR (B) State five dimensions of Human Endeavour. (07)Q4. (A) Explain the basics of Ethical Human Conduct. (07)(B) Define the Human Rights Violation in detail. (07)
- Q5. Attempt (Any Two)
 - (A) Explain Happiness as part of Value Education. (31/2)
 - (B) What do you mean by Activities of Body? (31/2)
 - (C) Define Harmony. (3^{1/2})
 - (D) What is Value Based Life? (31/2)

Subject In-charge

HOD [CSE]



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GNIET/2022-2023/EVEN/BECSE62T/M.S

Pre University Examination 2022-23

V-B. E. (Computer Science Engg.)

(C.B.S.) Examination

SUBJECT : Ethics In IT Maximum Marks: - 70 Time: - Three Hours INSTRUCTIONS TO CANDIDATE. 5) All questions carry marks as indicated. Answer any Six questions Assume suitable data wherever necessary. 8) Illustrate your answer wherever necessary with the help of n sketches. (5)Q1. (a) What is Ethics? Explain with examples. (b) What are Ethics in business world? Why ethics are important in (6)business world? OR Q2. (a) What is the relation between IT professional and IT users? What are the common ethical issue for IT users? (6)(b) What are Exploits? And their types? (5)Q3. (a)Explain government electronic survillence. (6)(b) Explain workplace monitoring. (6)OR Q4. (a) What are the Perpetrators? And their types? (6)(b) Explain Rootkits. And what are its Symptoms? (6)(a)Explain Risk Assessment in details. (6)(b) Describe in brief Intellectual property rights(IPR's)? (6)OR O6. (a) What is Copyright? Explain in details. (6)(b) Explain: 1)Patent 2)Trademark 3)Hallmark 4) Plagiarism (6) 5)Reverse Engineering 6)Open source code



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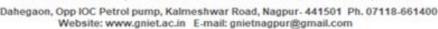
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Q7. (a) What are Key Privacy and Anynomity issue?	(6)
(b) Explain data Encryption? Write the advantages and disadvant	ages
of data encryption?	
of data energy don't	(6)
OR	10
Q8.(a) What is Software Development Process? Explain.	(6)
	(6)
(b) Explain the key issue in software development?	(6)
Q9.(a) Explain the stage of Software Development Life Cycle. (b) What is the impact of IT in the standard of living and Workers Productivity?	(6)
Floductivity:	(6)
OR	
Q10.(a)Who are H-1B workers?Explain H-1B application process?	(6)
Q10.(a) Who are 11.12	10
(b) What is Whistle-Blowing?	(6)
	(6)
Q11.(a)Write in details about contingent workers.	(0)
(b)Explain: 1)Worms 2)Trojan Horse 3) Spam 4) DDoS 5) Phishing 6)Smishing & Vishing 7)Viruses	(6)
OR	
Q12.(a)What is the compcept of low cost computers, OLPC, classmat Eee, Laptop?	e, (6)
(b) What is Telemedicine? Explain its types?	(6)
Subject In-charge HOD [CSE]	



GURU NANAK INSTITU





GNIET/2022-2023/ODD/BECSE62T/M.S

Pre University Examination 2022-23 V-B. E. (Computer Science Engg.) (C.B.S.) Examination

SUBJECT: OOPs in Java

Time: - Three Hours

Maximum Marks: - 70

INSTRUCTIONS TO CANDIDATE.

- All questions carry marks as indicated.
- Answer any Six questions
- 3) Assume suitable data wherever necessary.
- 4) Illustrate your answer wherever necessary with the help of

neat sketches.

- Q1. (a) What are the features of Object Oriented Programming?
- (b) Write a short notes: 1) Class 2) Object 3) Abstraction 4) (06)

Encapsulation 5) Polymorphism (06)

- Q2. (a) Explain concept of JVM with respect to portability features of Ja (06)
 - (b)Explain any four components of JDK. (06)
- Q3. (a) What is Constructor? Explain constructor overloading with (06)examples.
- (b) Described static member & state the characteristics of static (06)members.

- Q4. (a) Write the effect of access specifies public, private and protected in package.(06) (b) Described String class method with their examples.
- Q5. (a) Explain method of wrapper class .List various wrapper class and it's features. (05)
 - (b) Described Super Keyword & final modifier. (06)

Q6. (a) What is difference between Array & Vectors? Explain any two methods of vector with their examples. (05



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501

Department of Computer Science Engineering

Session 2022-2023 (EVEN)

Attendance

Pre University Test (Internal Practical)

Examination: III SEMESTER (CBS) (Regular)

Sr. No.	Students Name	CW 1 (P)	OS (P)	OOPS (P)
	4	23/1/023	25/1/023	27/1/023
1.	ABHISHEK ANIL RANGARI	-	_	
2.	ABHISHEK SUNIL	. Ard	. v.c	فلاء ا
5	WASHIMKAR	Miraninger	Bucchin. K	& Soul in Mo
3.	AMAN PRAKASH BORKAR	(A) booker	@ books	> who kee
4.	AMIT ABHAY WALEKAR	Rualelie		
5.	ARYAN ARVIND SAMBHE	Pul:	ABUL.	. AM
-6.	ASHISH HARICHAND RATHOD	Owner!	Oper	Damed
7.	AYUSH SANJAY BHAISARE	_	-	
8.	AYUSH KHEMRAJ RAMTEKE	A 50	100	A
9.	BHAVESH SAMPAT SAHU	Blahu	Mahy	Bahy
10.	BIPASHA NILAMKUMAR RANGARI	Bosnord	Bands	Dany
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Prof. Nayan Shambharkar

Exam In-Charge

Prof. Mayuri Shrungare

Class In-Charge

Prof.Ayaz Khan

Head of Department



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501



Department Of Computer Science Engineering Session 2022-2023 (Even)

Date: 22/02/2023

NOTICE

Respective Subject Faculties of VI and VIII semester of Computer Science Engineering are hereby to inform that Sessional Exam 1 will be conducted from 27/02/2023 onwards.

- Time Table will be displayed on notice board.
- Sessional Exam 1 will be on Two Units.
- Please mail your Sessional Paper on <u>shambharkar.nayan.gni@gmail.com</u>

 With subject name and semester by 23rd Feb 2023 before 2 o'clock.

Prof. Nayan Shambharkar Exam Incharge, CSE Prof. Vijaya Kamble HOD CSE

Head of Department
Computer Science & Serginearing
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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGYNAGPUR DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23 (Even)

Sessional Exam 1 Time Table

OFFLINE EXAM

Date: 22/02/2023

Day	Date	Semester	Subject
Mon		VI	IoT
	27/02/2023	VIII	Project Presentation
Tue	28/02/2023	VI	Complier Design
		VIII	ICS
Wed		VI	DOS
	01/03/2023	VIII	CCC
		VI	IPR
Thus	02/03/2023	VIII	DOS
		VI	Economics IT
Fri	03/03/2023	VIII	DF

Note

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.

3) Maximum marks: 20, Time: 1Hr.

Prof. Nayan Shambharkar Exam In-charge Prof. Vijaya Kamble Head of Department

Computer 5 ... Comput



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GURU NANAK INSTITUTE OFENGINEERING & TECHNOLOGY



Dahegaon, Kalmeshwar Road, Nagpur-441 501

Department of Computer Science Engineering

Session 2022-2023 (EVEN)

Roll List

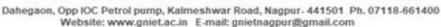
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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY

Dahegaon, Kalmeshwar Road, Nagpur-441 501



Department Of Computer Science Engineering Session 2022-2023 (Even)

Date: 03/04/2023

NOTICE

All the Students of VI and VIII semester of Computer Science Engineering are hereby to inform that Sessional Exam 2 will be conducted from 05/04/2023 onwards.

Time Table will be displayed on notice board. Sessional Exam 2 will be on Two Units.

Prof. Shubhangi Ghadinkar Exam Incharge, CSE

riead of Department Computer Science & Engineerin GNIET, Dahegaon, Nagpur



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY NAGPUR DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23(Even)

SESSIONAL 2 Time Table

EXAM VI SEM

Date: 31/03/2023

Day	Date	Time	Subject
Wed.	05/04/2023	11.45am-12.45pm	Compiler Design
Thu.	06/04/2023	11.45am-12.45pm	ЮТ
Mon.	10/04/2023	10.00am-11.00am	DOS
Tue.	11/04/2023	10.45am-11.45am	Economic in IT
Wed.	12/04/2023	1.30pm-02.30pm	Open Elective

Note:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 20, Time: 1Hr.

Prof. S.Ghadinkar Sessional In-charge

Head of Department
Head of Department
Computer Science & Engineering
GNIET, Dahegaon, Nagpur



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GURU NANAK INSTITUTE OF ENGINEERING & TECHNOLOGY NAGPUR

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING Session 2022-23 (Even) SESSIONAL 2 Time Table

EXAM VIII SEM

Date: 31/03/2023

Day	Date	Time	Subject
Wed.	05/04/2023	11.45am-12.45pm	Digital Forensic
Thu.	06/04/2023	11.45am-12.45pm	Information & Cyber Security
Mon.	10/04/2023	10.45am-11.45am	Distributed Operating System
Tue.	11/04/2023	10.45am-11.45am	Clustering & Cloud Computing

lote:

- 1) It is compulsory for all students to solve paper in a proper format.
- 2) The question paper of each subject will be of descriptive pattern.
- 3) Maximum marks: 20, Time: 1Hr.

Prof. S.Ghadinkar Sessional In-charge Dr. B Alliande

Head of Department
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Computer Science & Engineering
GNIET, Dahegaon, Nagpur